System Integration and Packaging of a Terahertz Photodetector at W-Band

Dong, Yunfeng; Olvera, Anuar d. J. Fernandez; Morales, Alvaro; Aller, Mario Mendez; Preu, Sascha; Zhurbenko, Vitaliy; Hanberg, Peter J.; Okonkwo, Chigo; Tafur Monroy, Idelfonso; Johansen, Tom Keinicke

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Abstract—This paper presents the system integration and packaging of a photodetector at W-band (75-110 GHz) for terahertz (THz) communications. The ErAs:In(Al)GaAs photodetector and its feeding network based on semi-insulating indium phosphide (InP) substrate are introduced. The design of the bias-tee at W-band is described and the effect of parasitic modes is discussed. Besides, the transition using E-plane probe between a W-band rectangular waveguide (WR-10) and a coplanar waveguide (CPW) is illustrated. The bias-tee as well as the E-plane probe transition including the WR-10 rectangular waveguide are fabricated, integrated, and measured. The measurement is carried out on-wafer in a back-to-back configuration and the results are presented. The assembly of the fully-packaged photodetector is demonstrated and on THz heterodyne communication system is implemented which validates the proposed system integration and packaging approach of the photodetector at W-band.

Index Terms—Bias-Tee, coplanar waveguide (CPW), E-plane probe, integration, packaging, photodetector, terahertz (THz), rectangular waveguide, wire bonding.

I. INTRODUCTION

W ith the fast development of lightwave systems and the increasing demand for high-speed data transmissions through not only optical fibers over a long distance but also wireless links to mobile terminals [1]–[4], terahertz (THz) photodetectors as one of the principal devices used in communication systems are under intensive study. As the opto-electronic devices that can convert signals from optical domain to electrical domain, photodiodes as well as photoconductors operating at THz frequencies have been pushed to achieve larger bandwidths [5]–[9]. Though both components can be implemented into photodetectors, they are based on different electro-optic effects which result in different characteristics. By adding an undoped region in a semiconductor junction, the photodiodes work as a rectifier converting light photons into an alternating current (AC). For the photoconductors, with a direct current (DC) bias its resistance is related to the number of free carriers in the photoconductive material generated by the light photons. At millimeter-wave and THz frequencies, not only the components but also the strategies for system integration and packaging are being challenged and efforts have been put to improve the performances [10]–[15]. Fig. 1 shows the system diagram of the THz photodetector as a part of the transmitter (Tx) used for wireless data transmissions at W-band. Instead of directly using a log-periodic antenna with a silicon lens [16], the photoconductor is integrated with other components and packaged inside a rectangular waveguide structure which allows the photodetector being cascaded with the amplifiers as well as the horn antenna in the system.

In THz communication systems, as one of the possible methods, a laser can be used as the light source and the data is modulated to the optical carrier by using a Mach-Zehnder modulator (MZM). After that, the modulated optical signal is transmitted to the photodetector through an optical fiber which needs to be aligned accurately with the photoconductor. The photoconductor is attached to a feeding network and the radio frequency (RF) signal is generated by the photoconductor based on the optical input. Alternatively, depending on the modulation scheme of the communication system, not only the DC bias but also the intermediate frequency (IF) signal might have to be provided to the photoconductor. As a consequence, a bias-tee is connected to the feeding network and chip-level
connections between them are required. The bias-tee is a three-port component based on planar transmission lines where the RF signal is transmitted freely towards the connected component while the DC bias and IF signal can only be guided to the photoconductor [17]. Since a rectangular waveguide serves as the interface of the packaging structure, a transition based on planar transmission lines is inserted for guiding the RF signal from the bias-tee to the rectangular waveguide. Besides, the rectangular waveguide isolates the DC bias as well as the IF signal so that the DC block can be avoided in the packaging structure. The organization of the paper is as follows. In Section II, the designs of feeding network, bias-tee, and transition are described. The fabrication, integration, and packaging of the components are addressed. In Section III, the assembly of the fully-packaged photodetector is demonstrated. Besides, a THz heterodyne communication system is implemented and the measurement results are presented. Conclusions are finally drawn in Section IV.

II. PACKAGING CONSIDERATIONS

A. Feeding Network

In order to guide the RF signal generated by the photoconductor to the bias-tee and the IF signal together with the DC bias to the photoconductor, a transmission line feed is implemented. A coplanar waveguide (CPW) is used due to its simple planar structure, tunable dimension, and low dispersion loss in a wide frequency range. Fig. 2 shows the proposed CPW feed based on a semi-insulating indium phosphide (InP) substrate, which has a thickness of 350 µm. The dielectric constant ($\epsilon_r$) and dissipation factor (tanδ) of the InP substrate are 12.56 and 2e-4, respectively [18]. Gold is used as the material of the conductor layer and its thickness is 200 nm. The metal carrier under the InP substrate also works as the bottom ground plane. The CPW is designed to have a characteristic impedance of 50 $\Omega$ which supports not only DC bias but also low-loss transmissions up to 110 GHz. The width of the signal trace is 50 µm and the width of the ground traces is 300 µm with a gap of 50 µm. For aligning the photoconductor, the width of the signal trace is tapered to 8 µm at the end while the ground traces are merged into a ground pad. The distance between the signal trace and the ground pad is 6 µm. Besides, wire bonding bridges on the top of the substrate are implemented along the CPW which are used for restricting parasitic modes. The height of the wire bonding bridges is 50 µm and they are placed 160 µm from each other. In the simulation, lumped ports are used as the excitation scheme and the photoconductor is represented by a port which is assigned to a surface located in the gap between the signal trace and the ground pad.

Fig. 3 shows the simulation results of the CPW feed based on InP substrate with wire bonding bridges in comparison with the situation when wire bonding bridges are removed. Due to the simple through line structure of the CPW feed and a length of 680 µm, similar performances are achieved and no visible notch is observed up to 110 GHz. In both cases, the return loss remains better than 17 dB and the insertion loss is less than 0.5 dB.

B. Bias-Tee

As is illustrated in Fig. 4, a bias-tee is designed for guiding the RF signal from the connected feeding network to the following transition while bringing the DC bias as well as the IF signal to the photoconductor at the same time. Since the bias-tee is a three-port component, except for supporting low-loss transmissions at W-band, it is also important to provide a good isolation for the RF signal so that it will be guided solely along the RF path. Besides, due to the rectangular waveguide involved in the packaging structure, the DC block on the RF path in order to prevent the DC bias from being brought to the following components can be avoided. By taking into account the feeding network as well as the transition, the bias-tee is realized using CPWs based on a high-resistivity silicon (Si) substrate which has a thickness of 280 µm. The $\epsilon_r$ and tanδ of the Si substrate are 11.6 and 4e-3, respectively [19].

The RF path consists of a CPW through line with a length of 1 mm. The characteristic impedance is 50 $\Omega$. For easier integration with the feeding network, the signal trace is designed to have an identical width which is 50 µm. The DC/IF path is connected to the RF path in the middle and the width of the signal trace is optimized to 20 µm. In both cases, the gap between the signal and ground traces is 30 µm. In order to prevent the RF signal from leaking to the DC/IF path, two open-circuited CPW stubs are implemented in parallel. Fig. 5 demonstrates the equivalent schematics of the proposed
The signal is more than 18.6 dB. In contrast, for the situation loss of 0.9 dB. Besides, the simulated isolation for the RF return loss is better than 16.6 dB with an associated insertion bias-tee based on Si substrate with wire bonding bridges. The scheme. Fig. 6 shows the simulation results of the proposed paths. In the simulation, lumped ports with vertical perfect bridges on the top of the substrate are also added along the signal at W-band. For restricting parasitic modes, wire bonding tee is designed and the dimensions are optimized for the RF increase the occupation area of the bias-tee.

Isolation for the RF signal and at the same time, it does not circuited stub is added in parallel which further improves the CPW open-circuited stub. As an alternative, the half-wavelength isolated from the DC/IF path, the DC bias is directly short-circuited stub. Though the RF signal can be quarter-wavelength CPW is followed by a half-wavelength transformer converting the termination from short to open. In this case, a quarter-wavelength transformer on the DC/IF path which has a length of 1 mm and a characteristic impedance of 50 Ω which has a length of 1 mm and a characteristic impedance of 50 Ω. Though the CPW is patterned on the same substrate, its dimensions are optimized for the new environment conditions due to the metal cover around the transition. In addition, efforts are put to restrict parasitic modes by adding wire bonding bridges, reducing the width of the substrate as well as the height of the cavity. As a result, the width of the signal trace is 80 µm and the width of the ground traces is 110 µm with a gap of 50 µm. For easier integration with the bias-tee, the CPW is tapered at the beginning.

In the simulation, the length of the rectangular waveguide is set to be 10 mm and wave ports are used as the excitation scheme. The rounded corners at the end of the rectangular waveguide are also included for being compatible with the milling process which results in a radius of 250 µm. Fig. 8 shows the simulation results of the proposed E-plane probe transition at W-band. With wire bonding bridges, the E-plane probe transition based on Si substrate exhibits a wideband.
behavior. The return loss remains better than 18.8 dB with an associated insertion loss of 0.6 dB. In contrast, for the E-plane probe transition without wire bonding bridges, a notch can be observed around 93 GHz which is due to the parasitic mode in the transition cavity.

D. Chip-level Connection and Integration

Based on the system diagram shown in Fig. 1, the individual components are integrated and chip-level connections are implemented. Fig. 9 demonstrates the proposed approach for integrating the feeding network, bias-tee, and E-plane probe transition. A metal carrier is used for realizing the WR-10 rectangular waveguide, packaging the E-plane probe transition, and providing physical support for other components. By milling the metal carrier into several platforms, the thickness difference between the InP and Si substrates can be compensated. As a result, the components are aligned by their top surfaces so that the vertical gap is avoided at the connections. In addition, wire bondings with a diameter of 25 µm are used for not only chip-level connections but also providing ground connections to the components at different positions on the substrates. Since the wire bonding connections introduce parasitic inductance especially at high frequencies, the components are placed as close as possible and the height of the wire bondings is kept around 50 µm.

The bias-tee is optimized from the original design for an easier integration. On the RF path, by reducing the width from 300 µm to 145 µm, the ground trace touches the metal carrier and it is aligned to the edge of the transition at the same time. By taking into account the DC bias as well as the potential IF signal, the signal trace is extended in order to reach the inputs which are connected to the bias-tee through a SMA connector at the far end. With the purpose of enlarging the bandwidth of the potential IF signal, an impedance transformer based on microstrip line is inserted to the DC/IF path whose width and length are 550 µm and 2 mm, respectively. After that, a microstrip line with a characteristic impedance of 50 Ω is implemented for guiding the potential IF signal. The width is 200 µm and the length is 15.685 mm. At the end of the DC/IF path, there is a square pad with a length of 400 µm which is large enough for connecting to the SMA connector. In order to be compatible with the experimental setup at W-band, the WR-10 rectangular waveguide is bent 90° with a total length of approximately 25 mm. For minimizing the reflection, the quadrant structure has an inner radius of 1 mm and it is placed 4 mm from the end of the rectangular waveguide.

In the simulation, different types of excitation schemes are used. A lumped port is assigned to a surface located in the gap between the signal trace and the ground pad on the feeding network. It represents the photoconductor and is used as the input of the RF signal as well as the output of the IF signal. A wave port is assigned to the outer surface of the rectangular waveguide which is the output of the RF signal. As for the input of the IF signal, another lumped port is assigned to the surface between the square pad on the bias-tee and the bottom metal carrier. The simulation results of the integrated components for both RF and IF signals are shown in Fig. 10. Since the RF signal operates at W-band, the simulation results range from 75 GHz to 110 GHz. The return loss remains better than 15 dB with an associated insertion loss of 2.3 dB and the isolation is more than 16 dB. However, the IF signal is
simulated up to 10.5 GHz. It achieves a return loss of 10 dB and an associated insertion loss of 1 dB up to 3 GHz while they reach 7.7 dB and 2 dB, respectively, at 10 GHz.

E. Fabrication and Experimental Results

In order to prove the designs and validate the proposed integration approach, the bias-tee, E-plane probe transition, and WR-10 rectangular waveguide demonstrated in Fig. 9 were fabricated. The bias-tee and E-plane probe transition were patterned on an n-type high-resistivity Si wafer which has a diameter of 50.8 mm and a crystal orientation of <100>. At the beginning, a conductor layer was deposited on the top of the Si wafer with a thickness of 200 nm using gold evaporation. After that, different layouts were patterned by following the process of photolithography. The wafer was diced into samples which were integrated into two identical prototypes at the end. Fig. 11 shows the integration of the fabricated bias-tee and E-plane probe transition. The metal package including the rectangular waveguide was divided into two parts and each part was fabricated using a brass block by milling process which was carried out by using a spinner with a radius of 250 μm. For accurate assembly, guide pins as well as screws were used and a standard WR-10 rectangular waveguide flange with annular recess was also applied to the fabricated brass package. The bias-tee and E-plane probe transition were first aligned on the brass carrier and the gaps were filled with silver epoxy. Then the wire bonding bridges along the CPWs, wire bonding connections between the components, and ground connections at different positions were added by ball bonding process. The brass cover was assembled at the last step.

Fig. 12 illustrates the experimental setup for on-wafer measurement at W-band as well as the device under test (DUT). The experimental setup comprises an Anritsu ME7808B vector network analyzer (VNA), two WR-10 extenders from Anritsu, and two ground-signal-ground (GSG) probes with a pitch size of 125 μm. The DUT has a back-to-back configuration which consists of two identical prototypes connecting to each other through the rectangular waveguide flanges.

By applying the GSG probes to the integrated bias-tees of the prototypes, two-port S-parameters were measured at W-band and on-wafer calibration was executed before the measurement. Fig. 13 shows both the measurement and the simulation results of the integrated bias-tee and transition in a back-to-back configuration. From 75 GHz to 110 GHz, the measured return loss remains better than 9.4 dB with an associated insertion loss of 8.6 dB. As a result, each prototype introduces less than 4.3 dB insertion loss at W-band. Compared with the simulation results, an extra insertion loss of 2.7 dB is achieved by each prototype which is mainly caused by the integration process especially the wire bonding connections between the components and the tanδ variation of the Si substrate at such high frequencies. From the system integration point of view, a measured return loss of 9.4 dB is acceptable while the insertion loss can be compensated to some extent by the cascaded W-band amplifiers in the communication system.

III. SYSTEM INTEGRATION

A. Fully-Packaged Photodetector

Though the integration of the feeding network, bias-tee, and E-plane probe transition has been addressed, the photoconductor as well as other supplementary components such as optical fibers, connectors, and outer metal covers are also necessary in order to have a fully-packaged photodetector. At telecom
wavelengths, the photoconductor is challenged by finding suitable photoconductive materials with a short carrier lifetime. Due to the low dark resistance, the DC bias that can be applied to the photoconductor is normally limited which makes it difficult to achieve a sufficient output power. According to [8], by combining erbium arsenide (ErAs), indium gallium arsenide (InGaAs), and indium aluminium arsenide (InAlAs) layers, the fabricated photoconductor exhibits excellent THz performances with a high dark resistance under continuous wave (CW) operations. As a result, the ErAs:InAlGaAs photoconductor is packaged into a THz photodetector which is used as a part of the transmitter in the communication system.

Fig. 14 illustrates the assembly of the fully-packaged photodetector. Apart from the principal components, a SMA connector inserting through the sidewall of the brass package was connected to the DC/IF path on the bias-tee and an angled physical contact (APC) connector with an optical fiber was used for guiding the optical signal all the way to the photoconductor. Since the optical signal needs to be illuminated to the photoconductor from the top, by taking into account the efficiency as well as accuracy, a fiber holder with customized dimensions was employed. The fiber holder was fabricated using a polyvinyl chloride (PVC) block by milling process and the optical fiber was fixed inside a though hole drilled from the top. Besides, the outer metal covers made from brass plates were added so that a fully-packaged device is set up with both physical and electrical protections.

B. Experimental Setup and Results

In order to validate the proposed system integration and packaging approach of the photodetector, the THz heterodyne communication system demonstrated in Fig. 15 is implemented. On the transmitter (Tx) side, a CW laser operating at 1550 nm is used as the light source and a polarization controller (PC) is added before the optical signal reaches the MZM where a RF signal at local oscillator frequency \( f_{LO} \) is modulated to the optical signal achieving two optical tones with the expected distance \( 2f_{LO} \) in the spectrum. Then an erbium-doped fiber amplifier (EDFA) is employed to enlarge the power of the optical signal and followed by an arrayed waveguide grating (AWG) which demultiplexes the two optical tones. The data is modulated to one of the tones by using another MZM and a variable optical attenuator (VOA) is applied on the other tone which adjusts the power of the optical signal. After that, the two optical tones are coupled together and the spectrum of the optical signal is also presented. Another EDFA pushes up the power of the optical signal to 30 mW before being transmitted to the photodetector. The RF signal with the modulated data has a frequency component of \( 2f_{LO} \) which is generated by the photodetector and then guided to a low noise amplifier (LNA) followed by a medium power amplifier (MPA). The W-band LNA and MPA from Radiometer Physics GmbH can provide gains of 40 dB and 10 dB, respectively. For radiating the RF signal into free space, a W-band standard horn antenna is used which has a gain of 21 dB.

On the receiver (Rx) side, a comb fiber laser operating at 1550 nm is used as the light source and the repetition rate \( f_{comb} \) is 40 GHz. The generated optical frequency comb goes through a wavelength selective switch (WSS) which reserves the two optical tones with a distance of 80 GHz \( (2f_{comb}) \) in the spectrum and the rest of the tones are suppressed. The power of the filtered optical signal is increased to 30 mW by using an EDFA and the spectrum is also shown in Fig. 15. The photodetector is based on an ErAs:InGaAs photoconductor which is attached to a log-periodic antenna and packaged using a hemispherical silicon lens. Since the DC bias is not required by the photoconductor in this case, the InAlAs layer is excluded, which according to [8] provides a higher mobility and a better sensitivity compared with the photoconductor used on the Tx side. When the IF signal is extracted by the photodetector, it has a frequency component of \([2f_{comb}−2f_{LO}]\) which can be tuned by sweeping the \( f_{LO} \) on the Tx side. Besides, a transimpedance amplifier (TIA) and two electrical amplifiers (EAs) are employed which provide...
Since the bandwidth of the communication system is limited to 2 GHz due to the TIA on the Rx side, the IF signal is set to operate at 1.3 GHz, 1.4 GHz, and 1.5 GHz, which correspond to 78.7 GHz, 78.6 GHz, and 78.5 GHz, respectively, for the transmitted RF signals. The measurement results at different operating frequencies are compared in Fig. 16. Instead of a single frequency component, the measured IF signal exhibits several tones in the spectrum which might be due to a beating interference on the Tx side. In addition, the total powers of the IF signal at different operating frequencies are -2.75 dBm, -2.78 dBm, and -5.43 dBm, respectively, which are calculated by integrating the power over the corresponding frequency ranges read out from the ESA. Though the IF signal spreads slightly in the spectrum, the recovered tones achieve sufficient stability as well as power for supporting data transmissions with different modulation schemes. As a consequence, it validates the proposed system integration and packaging approach of the THz photodetector.

IV. CONCLUSION

In this work, the system integration and packaging of a photodetector at W-band for THz communications have been presented. Though the fully-packaged THz photodetector is based on an ErAs:In(Al)GaAs photoconductor, the proposed system integration and packaging approach can also be applied to other photoconductors as well as photodiodes. As the principal components involved in the integration, the feeding network, bias-tee, and transition have been designed, fabricated, and integrated. However, changing of environment conditions due to the packaging structure results in parasitic modes on the individual components. Wire bonding bridges were added along the planar transmission lines and it turned out to be effective in restricting parasitic modes. By implementing a CPW, the feeding network based on semi-insulating InP substrate supports low-loss transmissions from DC to 110 GHz. For the bias-tee at W-band based on high-resistivity Si substrate, the RF path was realized using a CPW through line while the DC/IF path was extended in order to reach the inputs at the far end. The E-plane probe transition between the CPW and the WR-10 rectangular waveguide has been illustrated and it exhibits a wideband behavior. The proposed bias-tee and the E-plane probe transition including the WR-10 rectangular waveguide were integrated and measured on-wafer by connecting two prototypes in a back-to-back configuration. The wire bondings were used not only for chip-level connections but also for providing ground connections to the components at different positions. The assembly of the fully-packaged photodetector and the fabricated packaging structure have been demonstrated. A THz heterodyne communication system has been implemented in which the photodetector is involved on the Tx side as a fully-packaged device which can be used for other communication systems as well. Though the measured IF signal spreads in the spectrum, the stability as well as power of the recovered tones are sufficient for supporting data transmissions with different modulation schemes.

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REFERENCES


Yunfeng Dong received the B.Sc. degree in electronic information engineering from the Beijing University of Technology (BJUT), Beijing, China, in 2012, the M.Sc. and Ph.D. degrees in electrical engineering from the Technical University of Denmark (DTU), Lyngby, Denmark, in 2014 and 2019, respectively. From September 2011 to July 2012, he was an exchange student with the South-Eastern Finland University of Applied Sciences (XAMK), Mikkelin, Finland. From October 2014 to December 2014, he was a visiting scholar with the Terahertz Devices and Systems Group, the Technische Universität (TU) Darmstadt, Darmstadt, Germany. He is currently working as a Postdoc at EMS, DTU, Lyngby, Denmark. His current research interests include developing innovative strategies that are suitable for hybrid integration and packaging at millimeter-wave and terahertz frequencies.

Alvaro Morales received the B.Sc. degree in telecommunications technologies engineering, the M.Sc. degree in telecommunications engineering from the University of Valladolid (UVa), Valladolid, Spain, in 2014 and 2016, respectively, and is currently working towards the Ph.D. degree at the Technische Universität Eindhoven (TU/e), Eindhoven, Netherlands. From 2015 to 2017, he completed the thesis and worked as a research assistant with the Metro-Access and Short Range Communication Systems Group, the Technical University of Denmark (DTU), Lyngby, Denmark. He is currently with the Institute for Photonic Integration (IPI), TU/e, Eindhoven, Netherlands, under the framework of the EU H2020 project CELTA (Convergence of Electronics and Photonics Technologies for Enabling Terahertz Applications). As a part of the project, he had research stays at the Universidad Carlos III de Madrid (UC3M), Madrid, Spain and at Topictica Photonics AG, Grefeilting, Germany. His current research interests include the development of broadband THz sources and detectors based on photomixing for different applications, including photonic vector analyzer, THz communication systems, and non-destructive testing.

Anuar de Jesus Fernandez Olvera received the B.Sc. degree in electronic and computer engineering (summa cum laude) from the Tecnologico de Monterrey, Monterrey, Mexico, in 2010, the M.Sc. degree in electrical engineering (cum laude) from the Technische Universität Eindhoven (TU/e), Eindhoven, Netherlands, in 2015, and is currently working towards the Ph.D. degree at the Technische Universität (TU) Darmstadt, Darmstadt, Germany. He is currently with the Terahertz Devices and Systems Group, TU Darmstadt, Darmstadt, Germany, under the framework of the EU H2020 project CELTA (Convergence of Electronics and Photonics Technologies for Enabling Terahertz Applications). As a part of the project, he had research stays at the Universidad Carlos III de Madrid (UC3M), Madrid, Spain and at Topictica Photonics AG, Grefeilting, Germany. His current research interests include millimeter-wave and sub-terahertz communications, radio-over-fiber systems, beamforming and photonic integrations.
Mario Méndez Aller received the B.Sc. degree in telecommunications, the M.Sc. degree in multimedia and communications from the Universidad Carlos III de Madrid (UC3M), Madrid, Spain, in 2013 and 2014, respectively, and is currently working towards the Ph.D. degree at the Technische Universität (TU) Darmstadt, Darmstadt, Germany.

He is currently with the Terahertz Devices and Systems Group, TU Darmstadt, Germany. His current research interests include continuous wave terahertz photomixer sources, terahertz waveguides, and terahertz spectroscopy.

Sascha Preu received the Diploma and Ph.D. degrees in physics (summa cum laude) from the University of Erlangen-Nuremberg, Erlangen, Germany, in 2005 and 2009, respectively.

From 2004 to 2010, he was with the Max Planck Institute for the Science of Light, Erlangen, Germany. From 2010 to 2011, he was with the Materials and Physics Department, University of California, Santa Barbara, CA, USA. From 2011 to 2014, he was at the Chair of Applied Physics, University of Erlangen-Nuremberg. He is currently leading the Terahertz Devices and Systems Group, Department of Engineering and Information Technology, Technische Universität Darmstadt, Darmstadt, Germany. He has authored or co-authored more than 80 journal articles or conference contributions. His current research interests include the development of semiconductor-based terahertz sources and detectors, including photomixers, photodetectors, field effect transistor rectifiers, applications of terahertz radiation, in particular the characterization of novel terahertz optics, and materials such as whispering gallery mode resonators and graphene.

Vitaliy Zhurbenko (S’03–GSM’05–M’08) received the M.Sc. degree in electrical engineering from the Kharkiv National University of Radio Electronics, Kharkiv, Ukraine, in 2001, and the Ph.D. degree in electrical engineering from the Technical University of Denmark (DTU), Lyngby, Denmark, in 2008.

In 2005, he joined DTU, where he is currently an Associate Professor. His current research and teaching interests include microwave and millimeter-wave components and integrated circuits for instrumentation applications; antennas and passive circuit design and characterization; and magnetic resonance technology.

Peter Jesper Hanberg received the M.Sc. and Ph.D. degrees from the University of Copenhagen, Copenhagen, Denmark, in 1983 and 1987, respectively.

Since then he has worked over 30 years with semiconductor physics and processing gaining expertise in MOVPE and MBE epitaxial growth, thin film deposition techniques, wet and dry etching and a variety of characterization tools. After university he joined the Telecommunications Research Lab in Denmark where he set up a full III-V semiconductor cleanroom for fabricating high speed laser diodes.

This work was continued in the company Giga later acquired by Intel. After this followed work on VCSELs and nanowire LEDs in the companies Alight and GLO. He has been employed at DTU Danchip since December 2010 as Process Generalist supporting customers of the cleanroom with device and process development, training on equipments and teaching courses on processing technology.

Chigo Okonkwo (M’09–SM’18) received the M.Sc. degree in telecommunications and information systems, and the Ph.D. degree in optical signal processing from the University of Essex, Essex, UK, in 2002 and 2009, respectively.

From 2009 to 2012, he was a Senior Research Fellow at the Technische Universität Eindhoven (TU/e), Eindhoven, Netherlands. He was also the Co-Investigator on a bilateral research framework project with South Korean organization ETRI on high-capacity coherent transmission systems. Since 2012, he is an Assistant Professor at the Institute for Photonics Integration (IPI), TU/e, where he is currently leading the high capacity optical transmission laboratory of the Electro-Optical Communications Group (EOC). His current research interests include scaling the number of transmitted fiber modes, development of few-mode as well as multi-core fibers and amplifiers, Space Division Multiplexing (SDM) focused on the feasibility of Petabit/s transmission in a single fiber. He serves as a reviewer for Nature Photonics and has (co)authored more than 200 publications in conferences and journals.

Idelfonso Tafur Monroy (M’94–SM’14) received the M.Sc. degree in multichannel telecommunication from the Bonch-Bruevich Institute of Communications, St. Petersburg, Russia, in 1992, and the Ph.D. degree in electrical engineering from the Technische Universität Eindhoven (TU/e), Eindhoven, Netherlands, in 1999.

From 1999 to 2006, he was with the Electro-Optical Communications Group (EOC), TU/e, Eindhoven, Netherlands, after which he became a Professor at the Department of Photonics Engineering, Technical University of Denmark (DTU), Lyngby, Denmark. In the meantime, he was also a Guest Professor at the Beijing University of Post and Telecommunications (BUPT), Beijing, China, Visiting Scientist at the University of California, Berkeley, CA, USA, and Fellowship Professor at ITMO University (Russia), St. Petersburg, Russia. Since July 2017, He is a Professor at the Photonic Terahertz Systems, TU/e, Eindhoven, Netherlands, and since November 2018, the director of the Photonic Integration Technology Center (PITC). His current research interests include photonics technologies for terahertz systems, converged electronic-photon integrated circuits for applications in security, communications, sensing and computing. He is co-author of over 500 journal and conference papers and has graduated 22 PhD students.

Tom Reinicke Johansen (S’04–M’04) received the M.Sc. and Ph.D. degrees in electrical engineering from the Technical University of Denmark (DTU), Lyngby, Denmark, in 1999 and 2003, respectively.

In 1999, he joined the Electromagnetic Systems Group (DTU Elektro), DTU, where he is currently an Associate Professor. From September 2001 to March 2002, he was a Visiting Scholar with the Center for Wireless Communication, University of San Diego, San Diego, CA, USA. He has spent several external research stays at the Ferdinand Braun Institute (FBH), Berlin, Germany. His research interests include the modeling of high-frequency solid-state devices, millimeter-wave and submillimeter-wave integrated circuit design.