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Large process-dependent variations in band alignment and interface band gaps of Cu$_2$ZnSnS$_4$/CdS solar cells

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Abstract

Electron-hole recombination at the Cu$_2$ZnSnS$_4$/CdS interface is believed to play a major role in limiting the efficiency of Cu$_2$ZnSnS$_4$ solar cells. In this work, we experimentally determine detailed Cu$_2$ZnSnS$_4$/CdS interface band diagrams as a function of process conditions, and correlate them to chemical processes occurring during interface formation and subsequent post-annealing. The newly devised experimental method involves a combination of photoemission spectroscopy and spectroscopic ellipsometry. Our measurements reveal that, under most process conditions, the band gaps of both Cu$_2$ZnSnS$_4$ and CdS decrease by several hundred meV near the interface. Furthermore, interface band bending and conduction band offsets are highly process-dependent and roughly correlated to the amount of chemical interdiffusion. The interface electronic properties are found to be unfavorable under all process conditions studied in this work, either due to a cliff-like conduction band offset, or to substantial band gap narrowing in Cu$_2$ZnSnS$_4$, or to both effects. According to the present study, the least harmful process conditions for the interface electronic properties are a low CdS deposition temperature without post-annealing. Even in such a case, a minimum open circuit voltage loss of 230 mV is expected due to interface- or near-interface recombination.

Keywords: CZTS, kesterite, interface, ellipsometry, photoemission spectroscopy, band alignment

1. Introduction

The Cu$_2$ZnSnS$_4$/CdS interface appears to be of crucial importance for the performance of Cu$_2$ZnSnS$_4$ (CZTS) solar cells. In fact, the open circuit voltage extrapolation to 0 K temperature, as reported by different research groups on different state-of-the-art CZTS/CdS solar cells, is consistently lower than the bulk band gap of CZTS, even when recombination from band tails in CZTS is taken into account [1, 2, 3]. The reason for this experimental finding is still debated and can be attributed, for example, to a cliff-like conduction band alignment (CBO) between CZTS and CdS [4], to band gap narrowing of ideal CZTS surfaces [5], and to interdiffusion-driven band gap narrowing of CZTS near the interface with CdS [6, 7]. Even though problems with band alignment and band gap narrowing could also occur at the back contact interface (MoS$_2$/CZTS), recent results point to a low work function of the back contact as the main issue [8].

To further complicate the issue, the properties of the CZTS/CdS interface could in principle be influenced by growth process conditions [7]. However, process-dependent interface properties have only been studied for special cases, such as intentional Zn alloying in the CdS bulk [9], intentional Cd alloying in the CZTS bulk [3], and KCN etching of CZTS before CdS deposition [10]. One possible reason for the lack of systematic studies is the difficulty of determining the band gaps of the two materials near the heterointerface after interface formation. In fact, typical band alignment measurements based on direct photoemission spectroscopy can determine valence band offsets directly, but they usually rely on optical measurements of the two separate, bulk materials to determine their bulk band gaps [9, 3]. They assume therefore that the bulk band gaps of the two materials are equal to their band gaps in the interface region after interface formation. Even if complementary inverse photoemission spectroscopy measurements are performed to determine the surface band gaps of the two separate materials [10], the real interface band gaps may still be different from the surface band gaps due to interface-related phenomena such as interdiffusion, epitaxial growth, and early-stage growth mechanisms [7]. A post-annealing treatment of the as-deposited interfaces may further enhance the differences in electronic properties between the bulk materials and their near-interface region [6].

In order to characterize interface band gaps after interface preparation, we perform complementary spectroscopic ellipsometry measurements on CTZS/CdS film stacks. The ellipsometry spectra are sensitive to changes in the optical functions of the materials perpendicularly to the sub-

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strate plane; thus a band gap gradient in the out-of-plane direction can be detected by fitting the ellipsometry spectra to an appropriate multi-layer optical model [11, 12]. Complete interface band diagrams can then be drawn by combining the measured interface band gaps with the valence band offsets measured by x-ray photoemission spectroscopy. The goal of this paper is to determine those interface band diagrams as a function of process conditions, and correlate them to interfacial chemical processes.

2. Experimental details

CZTS thin films (about 100 nm thick) were deposited on a soda lime glass substrate by pulsed laser deposition from a single stoichiometric CZTS target, tuning the laser fluence to obtain the desired Cu-poor, Zn-rich composition as described previously [13]. Deposition of the typical Mo back contact was omitted in order to limit the number of free parameters necessary for fitting ellipsometry spectra [11]. The CZTS films are thinner than in typical solar cells in order to keep their roughness low. This precaution is important because high surface roughness can depolarize the incident beam used in the ellipsometry measurement and render subsequent analysis impossible [14, 15, 11]. With such thin films, there is a risk that interdiffusion from CdS into CZTS might occur over the full absorber thickness. However, it will be shown later in this paper that the depth of elemental interdiffusion into CZTS is only tens of nm, in agreement with previous studies [7]. The possibly higher Na concentration in 100 nm-thick CZTS compared to thicker absorbers deposited on Mo-coated glass is not expected to significantly alter the optoelectronic properties, because the effects of Na incorporation into CZTS depend on Na concentration only below a certain concentration threshold (about 10^{29} cm^{-3}), which has to be achieved in order to obtain efficient solar cells [16]. The as-deposited CZTS films were post-annealed in a sulfur-containing atmosphere at 550°C for 10 min. Additional process details and film characterization are available elsewhere [13]. CdS thin films (20-40 nm) were deposited on CZTS by chemical bath deposition at 55°C, 75°C, and 95°C on nominally identical glass/CZTS substrates. The solvent for CdS deposition was milli-Q water, the Cd source was 1.5 mM CdSO₄, and the sulfur source was 70 mM thiourea. To prevent homogeneous Cd(OH)₂ precipitation, an ammonia solution was added to the bath as a complexing agent. 1.7 M, 2.0 M and 2.4 M ammonia was used at 55, 75, and 95°C deposition temperature respectively, in order to account for the increasing OH⁻ concentration with increasing temperature and thus keep deposition rate roughly constant with temperature [17]. If the ammonia content is not adjusted at the different deposition temperatures, the resulting higher rate of homogeneous precipitation at higher temperature may have a stronger influence on the interface properties than the increase in temperature itself [17]. In order to avoid temperature transients in the deposition, thiourea was only added after the solution reached the desired temperature. A deposition time around 5 min yielded CdS films of 31 nm average thickness, with 8 nm standard deviation across samples. After CdS deposition, each of the three samples was cut in two halves, and one half of each sample was further annealed at 300°C for 20 min in Ar, as reported previously [6]. This yielded a total of six samples for interface electronic characterization. Note that the CZTS bulk in one of the six samples is more Cu-rich than what was intended. This could be due to the reproducibility issues of PLD discussed in [13] or to the dependence of film composition on the exact position of the substrate in the PLD chamber. The three samples that were not post-annealed will be denoted “as-deposited” in the following.

Variable-angle spectroscopic ellipsometry measurements were performed to obtain the CZTS band gap in the bulk \( E_{\text{gb,CZTS}} \), and the CZTS (CdS) band gaps near the interface \( E_{\text{gb,CZTS}} \) (\( E_{\text{gb,CdS}} \)) as shown in Fig. 1(a). The measurement was performed with a rotating compensator spectroscopic ellipsometer (M-2000, J.A. Woollam Co.) at six different angles \( \theta \) (45°-70°), with a spot size of about 200 \( \mu \)m \( \times \) 300 \( \mu \)m. Incident light of known polarization

![Figure 1](image-url)
maximum of CdS. From the valence band difference spectra, the interface valence band maximum (Fig. 1(b)). A Thermo Scientific K-Alpha instrument with a monochromated Al Kα x-ray source and a spot size of roughly 400 μm was used. The different CZTS/CdS samples were progressively etched with low-energy Ar⁺ ions (200 eV) from the CdS side. Photoemission spectra of the valence band region were recorded after each etching step to determine the valence band position with an analysis depth of a few nm (Fig. 1(b)). The first spectrum with a recognizable photoemission onset of CZTS is used to determine the interface valence band position of CZTS with respect to the Fermi level (E_{vi,CZTS}). The interface valence band position of CdS (E_{vi,CdS}) is derived from the same spectrum by the technique of valence band difference spectra [18] or "direct VBO method" [7]. The interface valence band offset (VBO) is simply calculated as VBO = E_{vi,CdS} − E_{vi,CZTS}. The Cu 2p, Zn 2p, Sn 3d, Cd 3d and S 2p core levels were also recorded after each etching step to determine the depth-dependent chemical composition and correlate it to the interface electronic properties. Further details on instrument calibration and the extrapolation of valence band offsets from photoemission spectra are discussed elsewhere [12].

3. Results

3.1. Interface electronic properties

Fig. 2 shows the data from which interface valence band maxima and near-interface band gaps are extracted. Interface properties are then plotted in Fig. 3 as a function of process conditions. Some interesting observations can be made:

1. in all samples, the near-interface band gaps of both CZTS and CdS are lower than their bulk band gaps by up to 0.25 eV and 0.16 eV respectively (Figs. 3(a),(b)).
2. the bulk band gap of CZTS does not vary significantly from sample to sample (Fig. 3(a)). Hence, any effect of the post-annealing process and of different CdS deposition temperatures is limited to a relatively shallow region (much less than 100 nm) below the interface. An exception is the as-deposited sample with a CdS deposition temperature of 75°C. As will be shown later (Fig. 4), this is the only sample with a Cu-rich composition in the CZTS bulk, which may explain the discrepancy.
3. the interface valence band maximum of CZTS (Fig. 3(c)) follows a clear trend. Both a higher

in the continuous photon spectral range 0.78-3.50 eV was used as a probe. The change in polarization upon multiple reflections at the interfaces of the CdS/CZTS/glass stack is described by the two quantities Ψ(E) and ∆(E), which are measured by the ellipsometer after reflection as a function of photon energy E (Fig. 1(a)). Since Ψ(E) and ∆(E) depend on the thickness of the layers and on their dielectric functions, fitting the Ψ(E) and ∆(E) spectra to a parametrical optical model including the layer thicknesses and their dielectric functions ε₁(E), ε₂(E) as unknown parameters, the values of those unknown parameters can be determined by least-squares regression analysis [20]. Analysis and fitting of ellipsometry spectra was performed with the CompleteEase software package (version 5.06 - J.A. Woollam Co.). Further details of our measurement approach, a full list of fitted parameters, and the validity of the optical model used in this study are discussed elsewhere [12]. Note that band tails are allowed when fitting the dielectric function of CZTS, and they are in fact detected in all samples in this study (Fig. 2(a)).

X-ray photoelectron spectroscopy (XPS) was performed after the ellipsometry measurements to determine valence band offsets and to simultaneously probe the depth-dependent composition of the interface region (Fig. 1(b)).
CdS deposition temperature and the post-annealing treatment contribute to raising $E_{V_b,\text{CZTS}}$ closer to the Fermi level (more p-type), and closer to the value measured on a bare CZTS surface. This is equivalent to less band bending in the near-interface region.

4. the interface valence band maximum of CdS is weakly dependent on process conditions and is in most cases located around 1.7-1.8 eV below the Fermi level (Fig. 3(d)). Only at the lowest deposition temperature (55°C) a significant difference exists between the as-deposited sample and the post-annealed sample.

5. the valence band offset becomes larger with increasing CdS deposition temperature (Fig. 3(e)).

6. the conduction band offsets of the different samples, calculated from the measured VBO and the measured interface band gaps, span over a 0.5 eV range (Fig. 3(f)). They are in general negative or close to zero.

3.2. Interface composition

Some properties extracted from the XPS measurement of depth-dependent chemical composition are illustrated in Fig. 4, Fig. 5, and Fig. 6(a). The most interesting trends are:

1. all samples have roughly similar compositions in the CZTS bulk, as intended in the film growth phase (Fig. 4). An exception is the as-deposited sample with a CdS deposition temperature of 75°C, which has a higher Cu content. This is also the only sample with a significantly lower CZTS bulk band gap (Fig. 3(a)), which may be ascribed to the presence of (low-band gap) copper sulfide secondary phases [21]. In fact, the bulk band gap of CZTS is found to be correlated with the average Zn/Cu ratio in the CZTS bulk (Fig. 5(a)).

2. the Zn content steadily decreases from the CZTS bulk towards the CZTS/CdS interface in all samples, while the Cu/Sn ratio remains roughly constant in the same region (Fig. 4). Further analysis of the Zn, Cu, and Sn content versus Cd content reveals an inverse correlation between Zn content and Cd content (Fig. 5(b)). This indicates that Cd interdiffusion into CZTS mainly results in Zn substitution (Cd$_{zn}$), as expected from previous studies on Cd incorporation in the CZTS bulk [22, 3].

3. the as-deposited samples exhibit a sharp Cu-poor, Zn-rich region in the interface region closest to CdS, where the Cu- and Zn content appear to be inversely correlated (Fig. 4). Conversely, no sharp Cu-poor, Zn-rich region can be identified in the post-annealed samples.

It can be interesting to determine whether a higher CdS deposition temperature and the post-annealing treatment promote interdiffusion. Interdiffusion is quantified by fitting the decay of the Cd 3d core level intensity versus depth using a Fermi-Dirac-like function, i.e., $[1 + \exp(x/d_x)]^{-1}$ (details in Fig. S1, Supporting information). The fitted broadening parameter $d_x$ of the Fermi-Dirac function is defined as the "interface grading parameter" in Fig. 6 and is taken as a measure of interdiffusion. Analysis of the interface grading parameter as a function of process conditions (Fig. 6(a)) confirms that both a higher CdS deposition temperature and a post-annealing treatment promote interdiffusion.
4. Discussion

4.1. Near-interface band gaps

The causes of near-interface band gap narrowing of CZTS and CdS are not obvious from the present experimental data. Nevertheless, there is some evidence that Cd interdiffusion is partially responsible for near-interface band gap narrowing in CZTS. As noted above, Fig. 5(b) reveals an inverse correlation between the total Cd content and the Zn/(Zn + Cu + Sn) content in the region of interdiffusion. This indicates that Cd preferentially replaces Zn, in agreement with previous studies on Cd incorporation into CZTS [23, 22, 3, 7]. The band gap of bulk Cu2Zn1−xCd_xSnS4 was previously found to decrease from 1.54 eV to 1.36 eV as x was intentionally changed from 0 to 0.5 in the bulk [22]. Such band gap changes by Cd incorporation are consistent with the $E_{\text{g},\text{CZTS}}$ values measured in this study (Fig. 3(a)). If the parameter z were the only factor affecting the near-interface band gap of CZTS in our study, the measured $E_{\text{g},\text{CZTS}}$ values would imply that Cd substitutes at least 50% of Zn in the near-interface region under most process conditions. However, the Zn content measured by XPS decreases by much less than a factor 2 from bulk CZTS to the interface (Fig. 4). Furthermore, $E_{\text{g},\text{CZTS}}$ does not show a clear inverse correlation to the interface grading parameter (Fig. 6(c)). We therefore conclude that other unidentified band gap narrowing mechanisms must coexist. They could be related, for example, to the presence of low-band gap secondary phases or to increasing cation disorder near the interface [24]. Both effects can be detected by the ellipsometry measurement and could therefore contribute to the experimentally observed band gap narrowing in CZTS. Conversely, other band gap narrowing mechanisms that involve extremely thin (1-2 nm) interface layers, such as those due to passivated surface states [5], cannot be resolved by the optical model in Fig. 1(a). If present, they would cause even stronger band gap narrowing than the values reported in this study. Regardless of the cause, near-interface band gap narrowing in CZTS is likely to have negative conse-
the resulting Zn diffusion [6, 7]. The argument was that the band gap of near the interface with CZTS due to preferential Zn inter-
previously speculated that the CdS band gap may increase with increasing E
examined in this study, Cu(In,Ga)Se2 material Cu(In,Ga)Se2, band gap narrowing in CZTS is the opposite effect of the circuit voltage measurements [5]. We note that near-interface band gap narrowing in CdS is not readily explained by qualitative arguments. Several authors, including some of the authors of the present article, had previously speculated that the CdS band gap may increase near the interface with CZTS due to preferential Zn inter-
diffusion [6, 7]. The argument was that the band gap of the resulting Zn$_x$Cd$_{1-x}$S interface phase would increase with increasing x. However, under all process conditions examined in this study, $E_{gl,CdS}$ is experimentally found to decrease with respect to $E_{gb,CdS}$ instead (Fig. 3(b)). The band gap decrease is stronger with stronger inter-
diffusion (Fig. 6(d)). We note that near-interface band gap narrowing in CdS could also be inferred by inspecting the absorption onset of CdS in quantum efficiency spectra of finished CZTS/CdS solar cells. Onsets at lower photon ener-

gies have indeed been observed in the presence of a post-
 annealing treatment [26]. While relatively deep, preferential Zn diffusion into CdS is observed in some of our samples (Fig. S2, Supporting information), the detected Zn content in CdS is never above 1% and does not have a clear correlation with process conditions. Even though preferential Zn interdiffusion has been reported by many authors, an interface phase with significant mixing of Zn and Cd could only be detected at the CZTSe/CdS interface but not at the CZTS/CdS interface [27]. Interestingly, two independent studies [28, 29] found a narrowed near-interface band gap of 2.2 eV in CdS after interface formation with Cu$_2$ZnSn(S$_{0.28}$Se$_{0.72}$)$_2$ and with CuInSe$_2$, similarly to what is found in the present study. They both measured the near-interface band gap by a combination of direct- and inverse photoemission spectroscopy on a thin CdS layer grown on the absorber material. Band gap narrowing was in those cases attributed to Se interdiffusion. However, our study shows similar $E_{gl,CdS}$ values for CdS deposited on a Se-free CZTS absorber using a significantly different characterization technique. One possible expla-

nation for the decreasing CdS band gap with increasing processing temperatures is the increase in grain size of the CdS film with temperature [17]. It is then possible that the band gap of the films processed at low temperatures is higher due to quantum confinement effects arising from the very small grain size [7]. Such effects have been ob-
erved in chemically deposited films with grain sizes of a few nm [17]. An alternative explanation could be that the increase of Cu content at the interface upon a post-

annealing treatment (Fig. 4) results in CdS band gap nar-
rowing over a very thin interface region. As noted pre-
viously, very thin interface regions are difficult to resolve by ellipsometry due to parameter correlation in the data fitting phase [12]. Therefore the measured CdS band gap may be an average band gap over the CdS layer thickness. In the sample fabrication phase, the CdS thickness was intentionally kept low in order to reduce such errors [12].

4.2. Band bending and valence band offsets

The position of the interface valence band maximum of
CZTS, related to interface band bending, is clearly corre-
lated to the interface grading parameter, i.e., to interdif-
fusion (Fig. 6(b)). If near-interface band gap narrowing in CZTS was caused by a valence band up-shift, it could also contribute to a decrease in $E_{vi,CZTS}$ (smaller apparent band bending). However, $E_{vi,CZTS}$ and $E_{gl,CZTS}$ are not correlated (Fig. 3(a,c)) so we expect this to be a minor effect. As discussed in the above sections, interdiffusion most likely results in Cd$_{Zn}$ substitutional defects in CZTS (Fig. 5(b)). An inverse correlation can then be inferred be-
tween downward band bending in CZTS and the density of Cd$_{Zn}$ defects near the interface. Importantly, the Cd$_{Zn}$ defect is known to slightly decrease the intrinsic p-type doping of CZTS in the bulk [3]. This would imply that enhanced interdiffusion would correspond to larger down-
ward band bending – exactly the opposite of the experi-

![Figure 6](image-url)

Figure 6: (a): Grading parameter of the CZTS/CdS interface as a function of process conditions. (b): Correlation between the interface grading parameter and the interface valence band maximum of CZTS ($E_{vi,CZTS}$). The dashed line is a guide to the eye. (c): Correlation between the interface grading parameter and the near-interface band gap of CZTS ($E_{gl,CZTS}$). (d): Correlation between the interface grading parameter and the near-interface band gap of CdS ($E_{gl,CdS}$).
55°C annealed
75°C annealed
95°C annealed

Cu
Zn

95°C as deposited
75°C as deposited
55°C as deposited

55°C annealed
75°C annealed
95°C annealed

Table 1: Numerical values of the quantities measured in this work, which are also plotted in Fig. 7. See Fig. 1(c) for a key to the symbols.
4.4. Optimal processing of CZTS/CdS interfaces

The interface band diagrams derived from the combined XPS/ellipsometry measurement are plotted in Fig. 7. They show that all the investigated process conditions result in some non-optimal features. Either a negative, cliff-like CBO or a strongly narrowed interface band gap (or a combination of both) are present in all cases (Fig. 7). Both effects are detrimental for the open circuit voltage of a solar cell, although they may be difficult to distinguish from each other in the device characterization phase [5]. The least harmful process conditions may be identified by calculating the maximum narrowing in the energy barrier for electron-hole recombination relative to the bulk CZTS band gap in each sample. If the CBO is positive, this simply corresponds to \( E_{gb,\text{CZTS}} - E_{gi,\text{CZTS}} \). If the CBO is negative, this corresponds to \( E_{gb,\text{CZTS}} - E_{gi,\text{CdS}} - CBO \). The calculated values, shown in Fig. 8, give an idea of the minimum open circuit voltage loss to be expected in CZTS/CdS solar cells dominated by interface- or near-interface recombination. From Fig. 8, a low CdS deposition temperature (55°C) appears as the least harmful processing route. Interestingly, the post-annealing treatment on CdS deposited at 55°C is beneficial in some aspects (increase of the CBO) but detrimental in other aspects (near-interface band gap narrowing in CZTS and smaller band bending). The detrimental effect of smaller band bending cannot be included in Fig. 8 because it does not involve band gap changes. As the as-deposited interfaces consistently achieve larger band bending, we conclude that the least unfavorable process conditions consist of a CdS deposition temperature of 55°C without post-annealing. Even in that case, a minimum open circuit voltage loss of about 230 mV compared to the Shockley-Queisser limit [34] is still expected (Fig. 8). Although the sample with Cu-rich CZTS has the lowest expected open circuit voltage loss among all samples (Fig. 8), Cu-rich CZTS has very poor photovoltaic performance due to its unfavorable bulk defect chemistry and secondary phases [21, 30]. Furthermore, the small near-interface band gap decrease of that sample may simply be due to its band gap already being low in the bulk (Fig. 3(a)). We emphasize that only selected properties have been measured in this article and have been considered as indicators of the interface quality. It is still perfectly possible that other interface properties (density of deep traps, epitaxial interface, crystalline quality of CdS) improve upon post-annealing, and even outweigh the detrimental effects shown in this paper. In fact, improved solar cell efficiency has been reported by some authors when incorporating a post-annealing step after interface formation [6, 35]. Unfortunately, the conclusions of this study cannot be correlated to device performance, because the CZTS/CdS films for ellipsometry analysis had to be deposited directly on glass. Including the standard back contact (Mo) in the device structure results in two additional layers (Mo and MoS\(_2\)), with an unacceptable increase in the number of parameters needed to fit the ellipsometry spectra [11].

![Figure 8: Maximum band gap narrowing in CZTS/CdS stacks relative to the bulk CZTS band gap \( E_{gb,\text{CZTS}} \). If the CBO is positive, the maximum band gap narrowing corresponds to \( (E_{gb,\text{CZTS}} - E_{gi,\text{CZTS}}) \). If the CBO is negative, it corresponds to \( (E_{gb,\text{CZTS}} - E_{gi,\text{CdS}} - CBO) \). The interface band diagrams derived from the combined XPS/ellipsometry measurement are plotted in Fig. 7. They show that all the investigated process conditions result in some non-optimal features. Either a negative, cliff-like CBO or a strongly narrowed interface band gap (or a combination of both) are present in all cases (Fig. 7). Both effects are detrimental for the open circuit voltage of a solar cell, although they may be difficult to distinguish from each other in the device characterization phase [5]. The least harmful process conditions may be identified by calculating the maximum narrowing in the energy barrier for electron-hole recombination relative to the bulk CZTS band gap in each sample. If the CBO is positive, this simply corresponds to \( (E_{gb,\text{CZTS}} - E_{gi,\text{CZTS}}) \). If the CBO is negative, this corresponds to \( (E_{gb,\text{CZTS}} - E_{gi,\text{CdS}} - CBO) \). The calculated values, shown in Fig. 8, give an idea of the minimum open circuit voltage loss to be expected in CZTS/CdS solar cells dominated by interface- or near-interface recombination. From Fig. 8, a low CdS deposition temperature (55°C) appears as the least harmful processing route. Interestingly, the post-annealing treatment on CdS deposited at 55°C is beneficial in some aspects (increase of the CBO) but detrimental in other aspects (near-interface band gap narrowing in CZTS and smaller band bending). The detrimental effect of smaller band bending cannot be included in Fig. 8 because it does not involve band gap changes. As the as-deposited interfaces consistently achieve larger band bending, we conclude that the least unfavorable process conditions consist of a CdS deposition temperature of 55°C without post-annealing. Even in that case, a minimum open circuit voltage loss of about 230 mV compared to the Shockley-Queisser limit [34] is still expected (Fig. 8). Although the sample with Cu-rich CZTS has the lowest expected open circuit voltage loss among all samples (Fig. 8), Cu-rich CZTS has very poor photovoltaic performance due to its unfavorable bulk defect chemistry and secondary phases [21, 30]. Furthermore, the small near-interface band gap decrease of that sample may simply be due to its band gap already being low in the bulk (Fig. 3(a)). We emphasize that only selected properties have been measured in this article and have been considered as indicators of the interface quality. It is still perfectly possible that other interface properties (density of deep traps, epitaxial interface, crystalline quality of CdS) improve upon post-annealing, and even outweigh the detrimental effects shown in this paper. In fact, improved solar cell efficiency has been reported by some authors when incorporating a post-annealing step after interface formation [6, 35]. Unfortunately, the conclusions of this study cannot be correlated to device performance, because the CZTS/CdS films for ellipsometry analysis had to be deposited directly on glass. Including the standard back contact (Mo) in the device structure results in two additional layers (Mo and MoS\(_2\)), with an unacceptable increase in the number of parameters needed to fit the ellipsometry spectra [11].

![Figure 8: Maximum band gap narrowing in CZTS/CdS stacks relative to the bulk CZTS band gap \( E_{gb,\text{CZTS}} \). If the CBO is positive, the maximum band gap narrowing corresponds to \( (E_{gb,\text{CZTS}} - E_{gi,\text{CZTS}}) \). If the CBO is negative, it corresponds to \( (E_{gb,\text{CZTS}} - E_{gi,\text{CdS}} - CBO) \). The interface band diagrams derived from the combined XPS/ellipsometry measurement are plotted in Fig. 7. They show that all the investigated process conditions result in some non-optimal features. Either a negative, cliff-like CBO or a strongly narrowed interface band gap (or a combination of both) are present in all cases (Fig. 7). Both effects are detrimental for the open circuit voltage of a solar cell, although they may be difficult to distinguish from each other in the device characterization phase [5]. The least harmful process conditions may be identified by calculating the maximum narrowing in the energy barrier for electron-hole recombination relative to the bulk CZTS band gap in each sample. If the CBO is positive, this simply corresponds to \( (E_{gb,\text{CZTS}} - E_{gi,\text{CZTS}}) \). If the CBO is negative, this corresponds to \( (E_{gb,\text{CZTS}} - E_{gi,\text{CdS}} - CBO) \). The calculated values, shown in Fig. 8, give an idea of the minimum open circuit voltage loss to be expected in CZTS/CdS solar cells dominated by interface- or near-interface recombination. From Fig. 8, a low CdS deposition temperature (55°C) appears as the least harmful processing route. Interestingly, the post-annealing treatment on CdS deposited at 55°C is beneficial in some aspects (increase of the CBO) but detrimental in other aspects (near-interface band gap narrowing in CZTS and smaller band bending). The detrimental effect of smaller band bending cannot be included in Fig. 8 because it does not involve band gap changes. As the as-deposited interfaces consistently achieve larger band bending, we conclude that the least unfavorable process conditions consist of a CdS deposition temperature of 55°C without post-annealing. Even in that case, a minimum open circuit voltage loss of about 230 mV compared to the Shockley-Queisser limit [34] is still expected (Fig. 8). Although the sample with Cu-rich CZTS has the lowest expected open circuit voltage loss among all samples (Fig. 8), Cu-rich CZTS has very poor photovoltaic performance due to its unfavorable bulk defect chemistry and secondary phases [21, 30]. Furthermore, the small near-interface band gap decrease of that sample may simply be due to its band gap already being low in the bulk (Fig. 3(a)). We emphasize that only selected properties have been measured in this article and have been considered as indicators of the interface quality. It is still perfectly possible that other interface properties (density of deep traps, epitaxial interface, crystalline quality of CdS) improve upon post-annealing, and even outweigh the detrimental effects shown in this paper. In fact, improved solar cell efficiency has been reported by some authors when incorporating a post-annealing step after interface formation [6, 35]. Unfortunately, the conclusions of this study cannot be correlated to device performance, because the CZTS/CdS films for ellipsometry analysis had to be deposited directly on glass. Including the standard back contact (Mo) in the device structure results in two additional layers (Mo and MoS\(_2\)), with an unacceptable increase in the number of parameters needed to fit the ellipsometry spectra [11].
5. Conclusion

We have applied a combination of photoemission spectroscopy and spectroscopic ellipsometry to study the electronic properties of the CZTS/CdS interface as a function of process conditions. The interface properties were unfavorable in all cases, either due to a cliff-like conduction band offset, or to substantial band gap narrowing of CZTS near the interface, or to both effects. Interface band gap narrowing of both CZTS and CdS was found to be somewhat correlated with chemical interdiffusion across the interface, but identification of the specific band gap-narrowing mechanisms will require a dedicated experimental study. A higher CdS deposition temperature and a CdS post-annealing step were both found to enhance interdiffusion. Even though interdiffusion can optimize the conduction band offset of the interface under some process conditions, this comes at the cost of near-interface conduction band offset of the interface under some propagation. Even though interdiffusion can optimize the conduction band offset of the interface under some process conditions, this comes at the cost of near-interface conduction band offset of the interface under some process conditions. The interface properties were measured in this study. Even under such conditions, a minimum open circuit voltage loss of 230 mV is expected due to interface- or near-interface recombination. A carefully selected replacement material for CdS may alleviate the CBO problem as well as the Cd interdiffusion problem believed to cause band gap narrowing in CZTS. Intentional surface oxidation of CZTS may also change the interface properties significantly compared to the samples investigated in this study.

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7. References


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