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New Technique for Fabrication of High Frequency Piezoelectric Micromachined Ultrasound Transducers

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Abstract—A novel technique for fabrication of linear arrays of high frequency piezoelectric Micromachined Ultrasound Transducers (pMUT) on silicon substrates is presented. Piezoelectric elements are formed by deposition of PZT ((PbZrxTi1-x)O3) into etched features of the silicon substrate such that the depth of these features determine the element thickness and hence the resonance frequency. The process leaves a near planar surface which is ideal for further wafer level processing such as top electrode and interconnect formation. A fabricated single element is characterized by pulse echo response.

PZT; thick film, pMUT

I. INTRODUCTION

For a number of years high frequency ultrasonic imaging has been based on single element transducers or annular arrays. The earliest high frequency ultrasound devices were based on PVDF [1] and more recently ceramic high frequency ultrasound transducers have emerged [2]. For many purposes single element transducers or annular arrays are excellent, however in order to obtain a full image they must be mechanically scanned. For many medical diagnostics cases such as intravenous investigations this is problematic and the beam steering ability of array based transducers is necessary. Fabrication of high frequency transducer arrays are however difficult but in this work we present a new method to fabricate high frequency PZT based linear array transducers.

The transducers are fabricated on silicon wafers by integration of ceramic thick film deposition technology with silicon micromachining [3]. This integration makes it possible to define the lateral element dimension and position with UV photolithography resolution. Piezoelectric elements are created by first etching out the desired element layout into a silicon wafer and subsequently filling the etched structure with PZT. In this way the element thickness, t, and hence the resonance frequency is defined by the silicon etch which is easy to control.

II. TRANSDUCER GEOMETRY

For this work [100] oriented silicon wafers are used. An anisotropic etchant, KOH, is used to create the structure in the wafer. KOH has a very low etch rate in the [111] direction and thus the etched structure has an angle given by the angle between the [100] and [111] silicon planes as illustrated in Fig. 1. This angle is θ = 54.7°. For an array of elements this puts a constraint on the minimum kerf that can be obtained. The geometric relation between the total element width, w, the depth of the etched structure, d, and half of the kerf, k½, is seen in the figure. The active element width covered by the top electrode is given as x. It should be noted that due to contraction during sintering the element thickness, t, is only 80-90% of the etched depth, d. As the element thickness, and hence the frequency, is related to the kerf we can derive the minimum obtainable element spacing, or pitch, in terms of λ, the wavelength in water. Simple geometry relates the total width, w, to the active element width, x, and depth as

\[ w = x + 2 \frac{d}{\tan \theta} \]  \hspace{1cm} (1)

From this we calculate the minimum pitch, n, in terms of λ as a function of d and x

\[ n = \frac{c_{PZT}}{c_{water}} \left( \frac{1}{\tan \theta} + \frac{x}{2d} \right) \]  \hspace{1cm} (2)

Figure 1. Schematic illustration of the structure etched in [100] oriented Si for a single piezoelectric element.
Figure 2. Plot showing the pitch as a function of active element width, $x$, and the etched depth, $d$. The color indicates the resonance frequency.

This minimum pitch as a function of etch depth, $d$, and active element width, $x$, is seen in Fig. 2. As seen it is only possible to obtain $\lambda$ pitch for quite small elements with an active width of around $x=20 \, \mu m$. For larger element sizes or frequencies above 25 MHz it is necessary to revert to 1.5$\lambda$ pitch or even higher.

III. FABRICATION

A. Fabrication prior to PZT deposition

The fabrication technique is illustrated in Fig. 3. For fabrication a standard 100 mm silicon wafer is used. A 500 nm silicon oxide film is grown and the desired element layout is patterned with UV photolithography and etched with buffered hydrofluoric acid. Subsequently the silicon oxide layer is used as an etch mask in a KOH etch. The etch rate in silicon is 1.3 $\mu m/min$ and the etch is terminated when the desired depth is reached. The etched structure will subsequently serve as moulds for the PZT deposition.

After KOH etching a new 500 nm silicon oxide is grown and a 50 nm Ti and 500 nm Pt bottom electrode is deposited by e-beam evaporation in an Alcatel SCM600, see Fig 3(a). The silicon oxide layer and bottom electrode serve as a diffusion barrier layer between the PZT film and the silicon substrate. This is necessary to prevent interdiffusion of Si and Pb which will otherwise degrade the piezoelectric coefficients [4].

B. PZT deposition

PZT is deposited into the KOH etched structure using a modified Ferroperm PZ26 paste. This has been specifically optimized for screen printing onto silicon substrates by addition of sintering agents that lowers the necessary sintering temperature. The paste is evenly distributed over the wafer and into the etched structure, after which excess paste is removed using a plastic scraper, see Fig. 3(b). Subsequently the paste is dried so that the organic vehicle evaporates and the PZT powder sediments in the bottom of the structure, see Fig. 3(c). This procedure is repeated until the structures is completely filled with PZT and the wafer is annealed at 850°C for one hour to sinter the PZT, Fig. 3(d). A cross sectional SEM micrograph of a single element is seen in Fig. 4 and in Fig. 5 three 15 $\mu m$ thick elements with a pitch of 75 $\mu m$ are seen as an example of the possibilities this technique offers.

C. Top electrode formation

After application of PZT the top electrode is deposited. It has proven difficult to wirebond to thin metal films on the
rough PZT surface so it is necessary to create top electrode bonding pads outside the PZT area. The SEM micrograph in Fig. 4 shows the edge of the KOH etched structure filled with PZT. Due to the shrinkage during sintering a gap has formed between the sloped silicon wall and the PZT. Thus the top electrode has to cross over this gap. A solution with a polymer bridge is employed.

The procedure is outlined in Fig. 6 and is a continuation of the fabrication described earlier and illustrated in Fig. 3. The first step is to define the polymer bridge for the top electrode. The polymer used is a 10 μm thick layer of AZ 4265 positive tone photoresist. It is patterned by UV photolithography and after development the sidewalls of the resist are nearly vertical, Fig. 6(b). The polymer is hard baked at 250°C for 30 min. During this baking step the polymer flows and the result is a sloped sidewall that can easily be covered with an e-beam evaporated top electrode, Fig. 6(c). Furthermore the hard baking makes the polymer resistant against acetone which is critical for the definition of the top electrode. The top electrode is made by e-beam evaporation of 500 nm Al using a lift off process using acetone to dissolve the resist, Fig. 6(d). An optical microscope image of this construction is seen in Fig. 7. The top part of eight elements are seen in the image, also the top electrode construction is seen. Every second element has a wire contacting a bond pad in the top of the elements and every other second element has a similar construction in the bottom outside of the image. The bond pads are placed on the 10 μm thick photoresist layer but due to a low dielectric constant of 3.5 of the polymer, this does not induce significant capacitances.

**Figure 6.** Schematic illustration of the second part of the fabrication process where the top electrode is deposited.

**Figure 7.** Optical microscope image showing part of a linear array. The top of each element is covered by the polymer bridge as illustrated in Fig. 6. Also the top Al and the bottom Pt electrodes are seen.

**IV. CHARACTERIZATION**

The element to element uniformity is investigated by the use of a Tencor P-1 stylus profiler. A scan of an 8 element array is seen in Fig. 8. The etched depth in this case is 54 μm and thus the baseline of the scan is set to 54 μm as seen in the figure. Due to contraction during sintering of the PZT, the elements do not have the full height of the etched structure and the thickness of each element has a minimum at the center of the element. From the profile scan it is concluded that the mean center element thickness is 47.8 μm with a standard deviation of 0.45 μm. This corresponds to an element to element thickness deviation of 1%.

In order to investigate the ability to emit and receive ultrasound a pulse echo setup is used. The setup consists of a JSR Ultrasonics DPR500 Pulsar/Reciever with a RP-H2 remote pulser. For data acquisition an Agilent Infinium DSO8064A is used. The transducer is placed in a water tank 18 mm from a metal reflector. For this test no matching layer or backing layers have been applied. A short pulse is applied to the transducer and the recorded response is seen in Fig. 9. The corresponding spectrum is seen in Fig. 10. The absence of a backing layer is apparent from both figures as the transducer shows a low bandwidth signal with peaks spaced 7.3 MHz.

**Figure 8.** Stylus profile scan of an 8 element linear array. The etched depth is 54 μm and the mean center element thickness is 47.8 μm.
apart. This resonance is caused by the silicon substrate which has a thickness of about 575 μm. With a sound velocity of 8400 m/s in silicon this corresponds to a resonance frequency of 7.3 MHz.

Figure 9. Normalized pulse echo response from a single element of the fabricated transducer array. Neither matching layers or backing layers were applied.

Figure 10. Pulse spectrum showing resonances spaced 7.3 MHz from each other. These resonance are caused by ringing in the silicon substrate.

V. CONCLUSION

The fabrication technique described in this paper offers rapid and cheap production of high frequency piezoelectric micromachined ultrasound transducers on silicon substrates. The process yields elements that has a good element to element thickness uniformity as the thickness is mainly defined by the silicon etch which is easy to control.

The use of a silicon substrate and an anisotropic etchant provides a very cheap base for fabrication of these elements. However, the characteristic angle of the KOH etch puts a constraint on the minimum pitch that can be obtained. Thus it is only possible to fabricate arrays with λ pitch with frequencies below 25 MHz. For higher frequencies the pitch will be larger than λ.

The polymer bridge technique described in the fabrication process has proven to be a good way of routing the top electrode away from the piezoelectric elements for electrical connection. The polymer has a dielectric constant of 3.5 and thus the construction does not give rise to significant additional capacitances. Furthermore the polymer layer has proven able to withstand electric fields of up to 40 V/μm, which is more than sufficient for both polarization and pulsing of the elements.

Pulse echo measurements performed on single elements demonstrate the ability to both emit and receive ultrasound waves. The quality of the response and the spectrum are however not optimal as the signal is seen to have a quite low bandwidth and is dominated by ringing in the wafer. This is due to the absence of a backing layer in the first prototype of transducers produced of this type.

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