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Investigation on the Combined Effect of VSC-based Sources and Synchronous Condensers under Grid Unbalanced Faults

Jundi Jia, Member, IEEE, Guangya Yang, Senior Member, IEEE, Arne Hejde Nielsen, Senior Member, IEEE, and Vahan Gevorgian, Senior Member, IEEE

Abstract—The short circuit response of voltage source converters (VSCs) could be significantly different from that of synchronous condensers (SCs) under unbalanced faults. This paper investigates the combined effect of VSC-based sources and synchronous condensers under grid unbalanced faults considering two groups of VSC fault-ride-through (FRT) control strategies. It aims to provide an evaluation of the different control strategies and to explore the impact of equipping a synchronous condenser at the point of common coupling (PCC). By examining the combined short circuit currents, PCC voltages, DC-side voltages and system frequency responses, this study points out the merits and demerits of the examined control strategies; illustrates the benefits of synchronous condensers on mitigating the disadvantages of certain control strategies; and provides suggestions on selecting the control strategies for the VSC when incorporating a synchronous condenser at the PCC.

Index Terms—Converter; inertia; short circuit power; synchronous condensers; unbalanced faults.

I. INTRODUCTION

When a grid experiences an unbalanced fault, the negative-sequence voltage appearing at the PCC can propagate into the control system of a VSC and affects its performances. If the negative-sequence components are not controlled properly, undesirable behaviors such AC voltage and current distortions can be observed. Therefore, a variety of control strategies based on symmetrical component theory and instantaneous power theory [1] have been proposed to improve the FRT capability of VSCs under grid unbalanced faults [2]–[5]. For a voltage-oriented current-controlled VSC system, these control strategies mainly deal with the generation of current references for the inner current controllers. In spite of various control objectives, most of the proposed control strategies can be categorized by flexible oscillating power control (FOPC) and flexible positive- and negative-sequence power control (FPNSPC) in a general way [4]. With different control strategies, the short circuit response of VSCs under unbalanced faults can be significantly different from each other and from that of synchronous sources. However, the existing grid codes on VSC-based sources generally lack informative statements specifically for unbalanced faults and hence how a VSC should be controlled under grid unbalanced faults is still under open discussion.

On the other hand, with proliferated renewable energy integration and gradual retirement of conventional generation units, modern power systems are currently undergoing significant changes in the generation mix and facing a variety of challenges. Conventional generation units usually consist of large synchronous generators, while renewable-based generations and high-voltage direct current (HVDC) transmission systems typically interface with the grids through power converters. Firstly, synchronous generators are natural voltage sources, which are able to provide a large amount short circuit current under grid fault conditions. This current is of great importance to activate protective relays. However, due to the limited overload capacities of semiconductors, power converters can only provide a limited amount of short circuit current. Secondly, as strong voltage sources, synchronous generators help stabilize voltage changes and enhance the connectivity of power electronic devices. In contrast, power converters behave more like current sources and operate under the presumption of having “strong” grids. In addition, synchronous generators have large rotating shafts and provide inertia to the system to counter frequency deviations. Nevertheless, power converters do not have rotating shafts directly coupled with the grids. Therefore, the transition to a converter-based power system from a synchronous-generator-based one tends to reduce the system short circuit current level, the system strength, as well as the system voltage and frequency stability.

Given the above-mentioned challenges, synchronous condensers (SCs) have gained increasing attention in recent years as it can serve as a potential solution as a single device. For example, two 225 Mvar synchronous condensers for Tegala substation have been committed to increase short circuit current level, voltage support capability, and system inertia in California, USA [6]. Several studies [7]–[13] have also shown the benefits of SCs on the short circuit current contribution, improved voltage regulation and voltage stability, and frequency stability enhancement. Even though conventional power plants can be refurbished into synchronous condensers as a cost-effective approach [14], [15], the conventional power plants may not always serve as the best locations for SCs and newly-
installed ones can be anticipated. In [15]–[17], the optimal allocation of synchronous condensers is of a interest and the results have indicated that there is a need of installing new SCs at the PCCs of HVDC systems or wind farms in future power systems. Therefore, it is necessary to investigate the combined effect of VSC-based sources and synchronous condensers taking different VSC control strategies into considerations under unbalanced faults. This has not been studied in the literature to the best of the authors’ knowledge.

This paper investigates the combined effect of a VSC-based HVDC system and an SC under grid unbalanced faults when an SC is equipped at the PCC. Considering the converter current limit in each phase, the investigation helps to evaluate the two generalized groups of the VSC control strategies (FOPC and FPNSPC) in a unified manner and explore the impacts of incorporating an SC with a VSC-based source on the system behaviors under unbalanced faults. Besides pointing out the pros and cons of different control strategies, this study illustrates the benefits of SCs on mitigating the disadvantages of certain control strategies, providing suggestions on selecting the control strategies for the VSC when incorporating an SC at the PCC.

The rest of the paper is organized as follows. Section II briefly presents the two generalized groups of VSC control strategies under unbalanced faults and discusses their relationship. In Section III, synchronous condensers and their benefits are shortly introduced. The combined effect of a VSC-HVDC system and a synchronous condenser are investigated and analyzed through various case studies in Section IV. Finally, Section V provides the conclusions.

II. VSC CONTROL STRATEGIES UNDER GRID UNBALANCED FAULTS

For a three-phase three-wire voltage source converter (zero-sequence components are neglected), the instantaneous active and reactive powers at the PCC can be expressed as [18]:

\[
\begin{align*}
    p &= \frac{v^+ \cdot i^+ + v^- \cdot i^-}{\bar{P}} \\
    q &= \frac{v^+ \cdot i'_q + v^- \cdot i'_q}{\bar{Q}} \\
    &\text{where } v = [v_a \ v_b \ v_c]^T \text{ is the voltage vector at the PCC; } \bar{P} \text{ and } \bar{Q} \text{ are the four factors that determine the control strategy. According to (1) and (2), the instantaneous power contains constant terms (} \bar{P}, \bar{Q} \text{) and oscillating terms (} P, Q \text{). As the average value of each oscillating term is zero, the power delivery is satisfied by constant terms that consist of positive-sequence powers (} P^+, Q^+ \text{) and negative-sequence powers (} P^-, Q^- \text{).}
\end{align*}
\]

The control strategy of VSCs under unbalanced faults mainly depends on how current references are formulated. Even though the current references are generated based on various objectives and can be expressed in different ways, a common feature is that these control strategies are able to provide positive- and negative-sequence current simultaneously. In this paper, two groups of current references expressed in a more general form are considered.

A. Group 1–Flexible Oscillating Power Control (FOPC)

In [19], the current references under unbalanced faults are formulated in a way that the amplitudes of the oscillating powers \( \bar{P} \) and \( \bar{Q} \) can be directly controlled with:

\[
\begin{align*}
    i^+ &= \frac{P_{\text{pref}}}{|v^+|^2 + x |v^-|^2} v^+ + \frac{Q_{\text{pref}}}{|v^+|^2 + y |v^-|^2} v^+ \\
    i^- &= \frac{x P_{\text{pref}}}{|v^+|^2 + x |v^-|^2} v^- + \frac{y Q_{\text{pref}}}{|v^+|^2 + y |v^-|^2} v^- \\
    \text{where } x \text{ and } y \text{ are the factors that change the formation of the current references and hence determine the control strategy; } P_{\text{pref}} \text{ and } Q_{\text{pref}} \text{ are the references of active and reactive powers. If the relationship between } x \text{ and } y \text{ is chosen as } x = y = k (-1 \leq k \leq 1), \text{ three most representative control strategies can be obtained. They are constant active power control (oscillation-free active power from the VSC with } k = -1), \text{ constant reactive power control (oscillation-free reactive power from the VSC with } k = 1) \text{ and balanced current control (only positive-sequence current provision from the VSC with } k = 0).}
\end{align*}
\]

B. Group 2–Flexible Positive- and Negative-sequence Power Control (FPNSPC)

In [18], the current references under unbalanced faults can be formulated in a way the relative amount between the positive- and negative-sequence powers can be directly controlled with:

\[
\begin{align*}
    i^+ &= a \frac{P_{\text{pref}}}{|v^+|^2} v^+ + c |Q_{\text{pref}}| v^+ \\
    i^- &= b \frac{P_{\text{pref}}}{|v^-|^2} v^- + d |Q_{\text{pref}}| v^- \\
    \text{where } a, b, c \text{ and } d \text{ are the four factors that determine the control strategy. By substituting (5) and (6) into (1) and (2), there are:}
\end{align*}
\]

\[
\begin{align*}
    P &= a P_{\text{pref}} + b P_{\text{pref}} + \bar{P} \\
    Q &= c Q_{\text{pref}} + d Q_{\text{pref}} + \bar{Q}
\end{align*}
\]

As the average value of the oscillating power terms is zero, the relationships \( a + b = 1 \) and \( c + d = 1 \) have to be satisfied so that the output powers meet their references. This indicates that two factors \( a \) and \( c \), which represents the share of \( P^+ \) and \( Q^- \) respectively, can be used to characterize the control strategy. Therefore, different control strategies can be considered as different combinations of the sequence output powers.
C. Relationship between FOPC and FPNSPC

In spite of different expressions, the current references of these two groups are indeed interchangeable. For example, if the control strategy factors \( a \) and \( c \) in (5) and (6) satisfy:

\[
\frac{1}{a} + 1/c = 2
\]

\[
a, c \in \left[ \frac{|v^+|^2}{|v^+|^2 + |v^-|^2}, \frac{|v^+|^2}{|v^+|^2 - |v^-|^2} \right]
\]

FPNSPC becomes equivalent to FOPC with \( x = -y = k \) \((-1 \leq k \leq 1)\). It is worth mentioning that constant active power control is achieved with:

\[
a = \frac{|v^+|^2}{|v^+|^2 - |v^-|^2}, \quad c = \frac{|v^+|^2}{|v^+|^2 + |v^-|^2}
\]

while constant reactive power control is achieved by flipping the values of \( a \) and \( c \) in (11). As indicated in (11), the share of the positive-sequence powers are not directly controlled in FOPC, but depends on the fault conditions since \( a \) and \( c \) are functions of the PCC voltages.

To illustrate the above relationship, Fig. 1(a) is used to represent the \( a-c \) plane for FPNSPC, where the values of \( a \) and \( c \) in (5) and (6) can be freely chosen on this plane; while Fig. 1(b) is for FOPC, but the values of \( x \) and \( y \) in (3) and (4) are restricted to the black line \((x = -y)\). If FOPC is expressed using the share of the positive-sequence powers, the black line in Fig. 1(b) will be interpreted as (9) (the blue line in Fig. 1(c)). This means that if \( a \) and \( c \) in (5) and (6) are restricted to the blue line in Fig. 1(c), FPNSPC will be equivalent to FOPC. Therefore, a common feature of the different control strategies is the flowing of active and reactive powers in both positive- and negative-sequence networks. With \( 0 < d < 1 \), the VSC provides \( Q^+ \) and \( Q^- \) simultaneously under unbalanced faults. If there is \( c > 1 \) (e.g. control strategy moves from balanced current control to constant reactive power control), \( d \) becomes negative since \( c + d = 1 \) is always maintained. This means, the VSC will absorb \( Q^- \) and provide \( Q^+ \) at the same time. Similarly, the VSC begins to absorb \( P^- \) with \( a > 1 \) (e.g. control strategy moves from balanced current control to constant active power control).

D. Voltage Support Concept

Figure 2 illustrates a simplified power system as seen from the PCC. The mathematical relationship between the PCC voltage \( V \) and grid voltage \( V_g \) can be expressed by:

\[ V = V_g + R_g i + L_g \frac{di}{dt} \]

(12)

If the grid resistance is ignored, (12) can be further decomposed into (13)–(14) in the stationary reference frame:

\[ v_a = v_ga + L_g \frac{di_a}{dt} \]

(13)

\[ v_b = v_g\beta + L_g \frac{di_\beta}{dt} \]

(14)

By decomposing the voltage and current in the stationary frame into symmetrical components, (13)–(14) can be expressed by:

\[ V^+\sin(\omega t + \phi_+) + V^-\sin(\omega t + \phi_-) = \]

\[ V_g^+\sin(\omega t + \phi_g^+) + V_g^-\sin(\omega t + \phi_g^-) + \]

\[ \omega L_g \sqrt{3} I^+ \cos(\omega t + \delta_+) + \omega L_g \sqrt{3} I^- \cos(\omega t + \delta_-) \]

(15)

\[ -\sqrt{3} V^+\cos(\omega t + \phi_+) + \sqrt{3} V^-\cos(\omega t + \phi_-) = \]

\[ -\sqrt{3} V_g^+\cos(\omega t + \phi_g^+) + \sqrt{3} V_g^-\cos(\omega t + \phi_g^-) + \]

\[ \omega L_g \sqrt{3} I^+ \sin(\omega t + \phi_+) + \omega L_g \sqrt{3} I^- \sin(\omega t + \phi_-) \]

(16)

\[ \omega L_g \sqrt{3} I^+ \sin(\omega t + \phi_+ - \frac{\pi}{4}) + \sqrt{3} V^- \cos(\omega t + \phi_- - \frac{\pi}{4}) \]

\[ -\omega L_g I^+ \cos(\omega t + \phi_+ - \frac{3\pi}{4}) - \omega L_g I^- \cos(\omega t + \phi_- - \frac{3\pi}{4}) \]

(17)

where \( V^+ \) and \( V^- \) are the root mean square (RMS) values of the sequence voltages; \( \phi_+ \) and \( \phi_- \) are the corresponding phase angles for the voltages; \( I^+ \) and \( I^- \) are the RMS values of the sequence currents; \( \delta_+ \) and \( \delta_- \) are the corresponding phase angles for the currents. Next, by adding (15) to (16) on both sides of the equation, there is:
If only reactive current is injected from the PCC, the angle difference between the sequence voltage at the PCC and the sequence current is given by:

$$\varphi_+ - \delta_+ = \frac{\pi}{2}$$  \hspace{1cm} (18)

$$\varphi_- - \delta_- = -\frac{\pi}{2}$$  \hspace{1cm} (19)

With (18)–(19) substituted into (17), the different terms on both sides of (17) can be categorized in terms of sequence components:

$$V^+ [\sin(\omega t + \varphi_+ - \frac{\pi}{4})] = V_g^+[\sin(\omega t + \varphi_g+ - \frac{\pi}{4})] + \omega L_g I^+ [\sin(\omega t + \varphi_+ - \frac{\pi}{4})]$$  \hspace{1cm} (20)

$$V^- [\cos(\omega t + \varphi_- - \frac{\pi}{4})] = V_g^- [\cos(\omega t + \varphi_g- - \frac{\pi}{4})] - \omega L_g I^- [\cos(\omega t + \varphi_- - \frac{\pi}{4})]$$  \hspace{1cm} (21)

Finally, with only reactive current injected, all the terms within the same sequence are in phase with each other. As a result, there are:

$$V^+ = V_g^+ + \omega L_g I^+$$  \hspace{1cm} (22)

$$V^- = V_g^- - \omega L_g I^-$$  \hspace{1cm} (23)

which indicates that $Q^+$ and $Q^-$ have a direct impact on the sequence voltages for an inductive grid. Table I summarizes the impact of sequence reactive power on the PCC voltage for an inductive grid. However, as indicated by (22)–(23), $P^+$ and $P^-$ have less effect on grid voltages for an inductive grid.

On the other hand, if $L_g$ is ignored rather than $R_g$ in Fig. 2 and only active current is injected at the PCC, (24)–(25) can be obtained by repeating the procedure (12)–(21):

$$V^+ = V_g^+ + R_g I^+$$  \hspace{1cm} (24)

$$V^- = V_g^- + R_g I^-$$  \hspace{1cm} (25)

which indicates the injection of $P^+$ and $P^-$ will increase the positive- and negative-sequence voltages respectively.

### III. Synchronous Condensers

A synchronous condenser (SC) is essentially a common synchronous machine that runs idle without any connected load at the drive end [11]. It can supply or absorb reactive power by varying its field current to provide voltage support dynamically like a synchronous generator (SG). For short circuit analysis, both SC and SG act as voltage sources, whose voltages are induced by the rotating magnetic field, behind an impedance. The impedance varies with time and the corresponding fault current can be classified into three characteristic periods: sub-transient, transient and steady stages. Under grid unbalanced faults, SC and SG can naturally provide positive- and negative-sequence short circuit current and the reactive power consists both positive- and negative-sequence components [20]. The characteristics (magnitudes and phases) of the short circuit current from SC and SG mainly depends on the sub-transient, transient impedances and the fault characteristics. Figure 3 gives an example illustrating the short circuit behavior of an SC and an SG (with the same parameters) subject to a phase A-B fault. Prior to the fault, both SC and SG are excited to exchange zero reactive power with the grid. During the fault, the short circuit currents in the faulty phases are with the same order in magnitudes. The currents in phase A and phase B are almost $180^\circ$ out of phase. As a synchronous machine, SC provides a large amount of reactive power during the short circuit, giving a higher short-term over-current capability compared to VSCs. The difference in phase C current is due to the fact that SC cannot deliver sustaining active power.

Since the SC is a rotating device, it can provide inertia for the system resulting its active power exchange with the grid during the transients [see Fig. 3(a)]. When there is a frequency deviation, kinetic energy can either be released or stored in the rotating mass so that the power system frequency changes are alleviated. For example, when there is a generation trip, synchronous condensers will release the rotational energy as active power to counter the system frequency drop. Therefore, frequency stability enhancement is another advantage of using synchronous condensers, especially for future converter-dominated power systems.

#### Table I

<table>
<thead>
<tr>
<th>Sequence reactive power</th>
<th>Effect</th>
</tr>
</thead>
<tbody>
<tr>
<td>Provide $Q^+$ ($Q^+ &gt; 0$)</td>
<td>Increase $V^+$</td>
</tr>
<tr>
<td>Provide $Q^-$ ($Q^- &gt; 0$)</td>
<td>Decrease $V^-$</td>
</tr>
<tr>
<td>Absorb $Q^+$ ($Q^+ &lt; 0$)</td>
<td>Decrease $V^+$</td>
</tr>
<tr>
<td>Absorb $Q^-$ ($Q^- &lt; 0$)</td>
<td>Increase $V^-$</td>
</tr>
</tbody>
</table>

#### Table II

<table>
<thead>
<tr>
<th>Control strategy factors</th>
<th>FPNCPC (with (5) and (6))</th>
</tr>
</thead>
<tbody>
<tr>
<td>$a = {1, 0, 5, 0}$</td>
<td>$3 \times 3 = 9$</td>
</tr>
</tbody>
</table>

![Fig. 3. Short circuit behaviour of synchronous sources subject to A-B fault. (a) Synchronous condenser. (b) Synchronous generator.](image-url)
IV. CASE STUDIES

A. System Overview

The investigations use a detailed electromagnetic transient (EMT) model in Real Time Digital Simulator (RTDS). All the simulation results are exported from RTDS to MATLAB and replotted for comparisons. The single-line diagram of the studied system is given in Fig. 4. A synchronous generator SG1 is connected to bus 1 (slack bus). Two VSC-HVDC systems are connected to bus 5 and 6 respectively, where synchronous condensers SC1 and SC2 are also equipped on each bus. The HVDC systems deploy two-level converters and interface with the grid through ΔY-g connected transformers. Based on [21], both VSC-HVDC systems are equipped with a dynamic braking resistor on the DC-side in order to drain the excess electrostatic energy from the DC link into the resistor under fault conditions. It is assumed that the active power from the other side of each VSC-HVDC system remains constant at all time. In this study, the focus is given to the marked area around bus 6. In order to determine different scenarios, SC1 can either be disconnected from the grid or has a capacity of 50/150/250 Mvar. The control strategy factors of VSC1 can be varied according to Table II. The remaining power system has the same condition in all the scenarios, where the capacity of SC2 is fixed at 160 Mvar and the control strategy of VSC2 is fixed at balanced current control under unbalanced faults. Figure 5 presents the configuration of the current control for VSC stations during grid unbalanced faults.

This case study investigates the characteristics of the combined short circuit current from VSC1 and SC1. With the control strategy factors of VSC1 (deploying FOPC) fixed, the capacity of SC1 is varied from 0 (SC1 is disconnected from the grid) to 250 Mvar. The corresponding combined short circuit currents in phase A and phase B measured at point M are compared in Fig. 6 with different scenarios. When SC1 is not present (no SC), the phase relationship of the currents in phase A and phase B shifts from nearly 180° out of phase (Fig. 6(a)) to almost in phase (Fig. 6(d)) with the control strategy moving from constant active power control ($k = 1$) to almost in phase (Fig. 6(d)) with the control strategy moving from constant active power control ($k = 1$).

<table>
<thead>
<tr>
<th>Table III</th>
<th>Parameters of the Studied System</th>
</tr>
</thead>
<tbody>
<tr>
<td>Rated power [MVA]</td>
<td>Pre-fault active power [MW]</td>
</tr>
<tr>
<td>SG1</td>
<td>600</td>
</tr>
<tr>
<td>SC1</td>
<td>50/150/250</td>
</tr>
<tr>
<td>SC2</td>
<td>160</td>
</tr>
<tr>
<td>VSC1</td>
<td>500</td>
</tr>
<tr>
<td>VSC2</td>
<td>500</td>
</tr>
</tbody>
</table>

With limited semiconductor overload capabilities, the current flowing through power electronic devices should be restricted in each phase. In this paper, a 1 p.u. current limit is imposed for both VSC-HVDC converter stations using the current ellipse theory presented in [18] for each individual phase. In following simulations, a solid A-B fault occurring in the middle of line 6–2 is initiated when the phase A voltage of bus 6 crosses zero from a negative to a positive value, which corresponds to the zero time instant. It is assumed that VSC1 and VSC2 stations prioritize reactive power injection under fault conditions. The reactive power reference is generated using $Q_{ref} = |v^+|I_Q$, where the value of $I_Q$ is obtained from [22] and can be expressed by $I_Q = -2.5|v^+| + 2.25$ ($0 \leq I_Q \leq 1$).
to constant reactive power control ($k = 1$). The application of SC1 helps to increase the short circuit current level but this advantage is gradually deteriorated with $k$ changing from -1 to 1. In the worst case with $k = 1$, the application of a 50 Mvar SC1 has almost no improvement on the amplitude of the phase A current (by comparing “SC50 phase A” with “no SC phase A” in Fig. 6(d)). This is due to the fact that there is a phase difference between the short circuit current provided by VSC1 and SC1. The phases of the short circuit current from VSC1 are mainly affected by its control strategy while those from SC1 are mainly consistent with a certain type of unbalanced faults. Therefore, this difference could cause the currents to cancel each other to some extent. To illustrate this effect, Fig. 7 compares the combined short circuit current under different VSC1 control strategies using FOPC when SC1 has a capacity of 250 Mvar. It can be observed from Fig. 7 that the currents have relatively lower amplitudes with $k > 0$ than other scenarios. This indicates that, with $k > 0$, a higher capacity of SC1 is needed to achieve the same short circuit level as with $-1 \leq k \leq 0$ for FOPC.

The same investigation is repeated when VSC1 deploys FPNSPC with the different control strategy factors specified in Table II. In this case, the current cancellation issue is not as significant as FOPC with $k > 0$. The combined short circuit current level in all the examined scenarios of FPNSPC can be increased with a 50 Mvar SC1 and the values of $a$ (share of $P^+$) and $c$ (share of $Q^+$) have less effect on the amplitudes of the combined short circuit current. The corresponding combined short circuit currents under different capacities of SC1 are similar to Fig. 6(b).

With converter current restricted in each phase, the short circuit current level drops significantly when it is only provided by a VSC. Among all the examined control strategies, it is not recommended to use FOPC with $0 < x = -y = k \leq 1$ (absorbing $Q^-$) together with a SC. FOPC with $0 < k \leq 1$ requires a higher capacity of SC to achieve the same short circuit current level as the other control strategies. This deteriorates the advantage of SC and might pose threats to the reliability of protection systems.

### C. PCC voltage

Firstly, the effect of the VSC1 control strategies on the PCC (bus 6) voltage is investigated. Given a certain capacity of SC1, the control strategy factors of VSC1 are varied according to Table II. For example, with an SC1 of 250 Mvar, Fig. 8 and Fig. 9 compare the sequence voltages of the PCC and the sequence currents measured at point M under different control strategies with FOPC and FPNSPC respectively.
In Fig. 8(a), both positive- and negative-sequence voltages are increased when the control strategy moves from constant active power ($k = -1$) towards balanced current control ($k = 0$), during which procedure the positive-sequence current is gradually increasing while the negative-sequence current reduces. As discussed in Section II, an increase of positive-sequence reactive current helps to improve positive-sequence voltage and a reduction of negative-sequence reactive current deteriorates the negative-sequence voltage attenuation, which increases the negative-sequence voltage. Therefore, with $k$ shifting from -1 to 0, the changes of both sequence currents in Fig. 8(a) have the same effect on the sequence voltages due to the mutual coupling between the positive- and negative-sequence networks under phase-phase faults. On the other hand, with the control strategy changing from balanced current to constant reactive power control ($k = 1$), the changes in the sequence voltages are not obvious anymore in Fig. 8(b). As a result of the current cancellation problem discovered in Section IV-A, both positive- and negative-sequence currents are reducing during this procedure, in which the effect of reducing positive-sequence voltage and increasing negative-sequence voltage counters each other.

In Fig. 9(a), both positive- and negative-sequence voltages are reducing with the share of $Q^+$ changing from 100% towards 0%. This is due to the fact that a reduction in positive-sequence current and an increase in negative-sequence current have the same effect. Since the system in Fig. 4 is an inductive system, voltage support on the PCC is mainly achieved by reactive current and hence a change in $a$ (the share of $P^+$) has a less notable influence on the PCC voltages in Fig. 9(b).

Since most grid codes do not impose requirements on VSCs regarding the negative-sequence current provision, VSC-based sources are commonly controlled to provide only positive-sequence currents during unbalanced faults [20], [23]. Therefore, VSCs behave as open circuits in the negative-sequence network and the negative-sequence voltage cannot be attenuated, which might lead to over-voltage problems in healthy phases. The examined control strategies affect the PCC voltages mainly through sequence reactive power. From this point of view, by moving the strategy from balanced current to constant active power control in FOPC, or having $c$ between 0 and 1 with FPNSPC, VSCs have the potential to help to attenuate negative-sequence voltages under grid unbalanced faults. Other control logic can be implemented to generated the exact values of $k$ or $c$ based on the needs of the grid.

Secondly, the combined effect with an SC on the PCC voltages is investigated. In this case, the capacity of SC1 is varied from 0 (disconnected from the grid) to 250 Mvar given a fixed VSC1 control strategy. As an example, the sequence voltages of the PCC and the sequence currents measured at point M with different capacities of SC1 are plotted in Fig. 10 when VSC1 deploys constant active power control. The short circuit current level in both positive- and negative-sequence networks are increased with a higher capacity of SC1. During grid unbalanced faults, synchronous condensers can inherently provide both positive- and negative-sequence reactive currents. The increase in the positive-sequence reactive current tends to support the positive-sequence voltage. However, an increase on the negative-sequence reactive current helps to attenuate the voltage unbalance, which in turn tends to reduce the positive-sequence voltage. Therefore, SC1 does not alter the sequence voltages as notably as those in Fig. 8 and Fig. 9. This indicates that, once the control strategy of VSC1 is fixed, the application of SC1 has less impact on the PCC sequence voltages for
unbalanced faults.

**D. DC-side Voltage**

Firstly, the control strategy factors of VSC1 are varied to examine the effect of the control strategies on the DC-side voltage given a fixed SC1 capacity. The corresponding DC-side voltages and output active powers of VSC1 are plotted in Fig. 11. With reactive power provision prioritized and the current limit imposed, the active power from VSC1 is curtailed under fault conditions. This results in an increase in the DC-side voltages. In Fig. 11(a), the amplitudes of the DC-side voltage oscillations, as well as output active power oscillations, are growing when the control strategy moves from constant active power control to constant reactive power control. When FPNSPC is deployed, a variation on the share of $P^+$ does not have a notable influence on the amplitudes of the output active power oscillations in Fig. 11(b) and the DC-side voltage profiles are close to each other. However, with the share of $Q^+$ changing from 100% to 0%, the amplitudes of the active power oscillations in Fig. 11(c) reduces first and then increases, giving the same trend in the DC-side voltage oscillations. These facts indicate that the DC-side voltage oscillations can be reduced by the injection of positive $Q^+$, while $P^-$ does not have a notable effect. By moving the control strategy from balance current control to constant active power control in FOPC or choosing the share of $Q^-$ between 100% ($c = 1$) to 50% ($c = 0.5$) in FPNSPC, the DC-side voltage oscillations can be reduced.

Secondly, the capacity of SC1 is varied to examine the effect of an SC on the DC-side voltage given a fixed VSC1 control strategy. For example, when VSC1 deploys balanced current control, Fig. 12 compares the DC-side voltages and output active powers of VSC1 under different capacities of SC1. It can be observed that SC1 has almost no effect on the DC-side voltage under unbalanced faults regardless the presence or the capacity of SC1.

**E. Frequency Response**

This case study investigates the impact of a synchronous condenser and VSC1 control strategies on the system frequency responses under unbalanced faults. As a base scenario, with SC1 disconnected from the grid, the frequency responses of the positive-sequence voltage at the PCC (bus 6) are plotted in Fig. 13 with dashed lines. The VSC1 provides only positive-sequence short circuit powers under unbalanced faults, with either active power provision prioritized (green-dashed curve) or reactive power provision prioritized (black-dashed curve). Then with a 250 Mvar SC1 connected to the grid, the frequency responses (black- and green-solid curves) are improved with less deviation from 50 Hz. Since a synchronous condenser is a rotating machine, it provides inertia for the system to counter the frequency deviation.

Next, with the same SC1 connected, the control strategy of VSC1 is changed to either providing 50% $Q^+$ ($c = 0.5$) with Q priority or providing 50% $P^+$ ($a = 0.5$) with P priority. The corresponding frequency responses are the red- and blue-solid curves respectively in Fig. 13. In Fig. 1(c) where FOPC and FPNSPC are illustrated in an unified way, $(a,c) = (1,1)$ means VSC1 will only provide positive-sequence short circuit power ($P^+$ and $Q^+$). If $(a,c)$ moves away from (1,1), negative-sequence current will flow through
TABLE IV
SUMMARY OF THE INVESTIGATION

<table>
<thead>
<tr>
<th>Aspects</th>
<th>VSC control strategies</th>
<th>Synchronous condensers</th>
</tr>
</thead>
<tbody>
<tr>
<td>Short circuit current</td>
<td>Avoid $k &gt; 0$ with FOPC (could raise current cancellation problem);</td>
<td>Help bring the phases and amplitudes of the short circuit currents close to those from synchronous sources;</td>
</tr>
<tr>
<td>PCC voltage</td>
<td>(1) Affected by $Q^+$ and $Q^-$ rather than $P^+$ and $P^-$ for inductive grids; (2) The injection of positive $Q^-$ ($k &lt; 0$ with FOPC or $c &lt; 1$ with FPNSPC) helps attenuate negative-sequence voltage; (3) The changes of positive- and negative-sequence currents have to be in opposite directions to effectively change sequence voltages;</td>
<td>Natural control on sequence-voltages;</td>
</tr>
<tr>
<td>DC-side voltage</td>
<td>(1) The injection of positive $Q^-$ ($k &lt; 0$ with FOPC or $0.5 &lt; c &lt; 1$ with FPNSPC) helps reduce DC-side voltage oscillations; (2) $P^-$ has no notable effect</td>
<td>No notable effect;</td>
</tr>
<tr>
<td>Frequency response</td>
<td>(1) Affected by how much active power is curtailed; (2) For under-frequency, avoid the injection of positive $P^-$ ($k &gt; 0$ with FOPC or $a &lt; 1$ with FPNSPC); (3) For over-frequency, the injection of $P^+$ and $Q^-$ in both directions helps counter frequency deviation;</td>
<td>Provide inertia for the system;</td>
</tr>
</tbody>
</table>

VSC1. As shown in Fig. 11, the further $(a, c)$ moves away from $(1,1)$, the more active power VSC1 has to curtail to comply with the current limit. Therefore, the impact of the VSC1 control strategy on the system frequency responses is indeed a question of how much active power is curtailed. For a system experiencing under-frequency issues during the unbalanced fault, there is no need to have $a < 1$. On the one hand, the injection of positive $P^-$ will result in more active power to be curtailed. On the other hand, it neither helps with voltage support nor helps to reduce the DC-side voltage oscillations. For a system experiencing over-frequency issues during unbalanced faults, the injection of $P^-$ and $Q^-$ in both directions can help to reduce the frequency deviation.

F. Summary

Based on the above case studies, a summary of the investigations is provided in Table IV. Regarding the combined short circuit current, FOPC with $k > 0$ should be avoided as it tends to reduce the short circuit level when incorporating with synchronous condensers. In comparison, constant active power control (FOPC with $k = -1$) gives short circuit current similar to that of synchronous condensers in terms of the phases, and hence helps increase the total short circuit current level. The PCC voltage in an inductive grid is more affected by $Q^+$ and $Q^-$ rather than $P^+$ and $P^-$. The injection of positive $Q^-$ ($k < 0$ with FOPC or $c < 1$ with FPNSPC) can actively help reduce the voltage unbalance under unbalanced faults. On the other hand, the injection of positive $Q^-$ also helps reduce the DC-side voltage oscillations. Since the oscillations of the DC-side voltage are directly related to the oscillations of the VSC output active power, constant active power control is more suitable when DC-side voltage is a concern. The control strategy of VSC-based sources affects system frequency response in terms of how much active power is curtailed during the fault. Since the flow of negative-sequence powers ($P^-$ and $Q^-$) occupies the current margin left for active power injection, the control over negative-sequence powers has a potential to be used to improve the system frequency response as stated in Table IV.

As a result, the authors recommend constant active power control for VSC-based sources when incorporating synchronous condensers. This helps increase the short circuit current level, attenuate negative-sequence voltage, and reduce DC-side voltage oscillations at the same time. Even though this could lead to more active power from VSC being curtailed, the existence of the synchronous condenser provides inertia for the system to counter the frequency deviation under fault conditions.

V. CONCLUSIONS

With different control strategies, the short circuit responses of VSC-based sources under grid unbalanced faults are significantly different from that of synchronous condensers. Different control strategies could raise problems such as non-conventional characteristics of the short circuit current, a lack of negative-sequence reactive current, DC-side voltage oscillations and excess active power curtailment under fault conditions. The application of a synchronous condenser at the PCC helps to increase the short circuit level and bring the phases of the combined short circuit current close to that from a synchronous source. By providing positive- and negative-sequence reactive power simultaneously, synchronous condensers help to control sequence voltages naturally. As a rotating device, it can provide inertia for the system to counter the frequency deviation. Therefore, synchronous condensers are able to mitigate the disadvantages of certain control strategies. This study provides suggestions on the selections of the control strategies for a VSC-based source with the incorporation of a synchronous condenser at the PCC. Future work could be the optimal allocation of synchronous condensers and the design of their protection system.

APPENDIX
SYSTEM PARAMETERS

Line impedance

<table>
<thead>
<tr>
<th>Line</th>
<th>Impedance</th>
</tr>
</thead>
<tbody>
<tr>
<td>Line 1-2:</td>
<td>(1.47+j18.13)Ω; Line 2-3: (0.85+j14.00)Ω; Line 2-4: (0.85+j14.00)Ω; Line 3-5: (1.83+j22.57)Ω; Line 4-5: (1.83+j22.57)Ω; Line 2-6: (0.65+j8.65)Ω</td>
</tr>
</tbody>
</table>

Transformer leakage inductance

| T1: | 8%; T2: 11%; T3: 10%; T4: 8%; T5: 11% |

VSC1 and VSC2
Rated DC link voltage: 350 kV; Switching frequency: 1650 Hz; DC link capacitor: 250 µF; Reactor inductance: 0.02 H; Proportional gain of current controllers: 0.4; Integrator time constant of current controllers: 0.03

REFERENCES


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