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Towards High Power Density Metal Supported Solid Oxide Fuel Cell for Mobile Applications

Jimmi Nielsen, Asa H. Persson, Thuy Thanh Muhl, and Karen Brodersen

Department of Energy Conversion and Storage, Technical University of Denmark, 4000 Roskilde, Denmark

For use of metal supported solid oxide fuel cell (MS-SOFC) in mobile applications it is important to reduce the thermal mass to enable fast startup, increase stack power density in terms of weight and volume and reduce costs. In the present study, we report on the effect of reducing the technical University of Denmark (DTU) SOaMS-SOFCs support layer thickness from 313 μm gradually to 108 μm. The support layer thickness decrease in the DTU co-sintering MS-SOFC fabrication route results in an increased densification of the support layer and a slight decrease in performance. To mitigate the performance loss, two different routes for increasing the porosity of the support layer and thus performance were explored. The first route is the introduction of gas channels by puncturing of the green tape casted support layer. The second route is modification of the co-sintering profile. In summary, the cell thickness and thus weight and voltage was reduced and the cell power density at 0.7 V at 700°C was increased by 46% to 1.01 Wcm⁻² at a fuel utilization of 48%. All modifications were performed on a stack technological relevant cell size of 12 cm × 12 cm.

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Solid oxide fuel cell (SOFC) is attractive due to the excellent power generation efficiency and fuel flexibility. The conventional ceramic electrolyte and anode supported SOFC (AS-SOFC) is limited to stationary power generation applications as a result of the difficulty of quick startup and the high operating temperatures of 700 to 1000°C. If quick startup of a SOFC is realized, mobile applications may be considered. To enable this, it is important to shorten the startup time and reduce the heat cycle performance degradation and increase stack power density both in terms of weight and volume. As an approach to solving these problems, metal supported SOFCs (MS-SOFCs) have attracted attention. The development of this new generation of SOFC is currently in progress. DTU Energy’s MS-SOFC technology is based on co-sintering of laminated tape casted electrolyte, anode and support layers in a reducing atmosphere. This implies that the sintering shrinkage of the different layers must be matched sufficiently so that the mechanical stresses originating from any mismatch in sintering shrinkage of the individual layers can be absorbed by the cell structure. If the cell structure is unable to absorb the mechanical stresses, the cell will crack. Perfect matching of sintering shrinkage of the layers is practically very difficult as e.g. the electrolyte layer needs to be completely dense and thus gastight, while the anode and support layers in contrast needs to be highly porous to allow sufficient gas transport.

An overview and recent progress of MS-SOFCs have been reviewed in Ref. 1. The company Ceres Power founded in 2001 is at present the organization, which most effectively has demonstrated up scaled large cell sized >80 cm² MS-SOFC stack technology. This is followed by consortia involving the company Plansee. At last MS-SOFC stacking has been demonstrated within consortia consisting of Topsoe fuel cell (TOFC no longer in operation), DTU and others. Apart from the mentioned players other MS-SOFC R&D activities are predominantly performed on button cell level by various university groups.2–11 The present MS-SOFC designs are to our knowledge not optimized for mobile applications in terms of rapid startup and volumetric and gravimetric power densities. For example, the Plansee design consist of very thick substrates of 1 mm,12,13 whereas exact thickness of 300 μm from a modelling study in 2005.14 The performance of the different MS-SOFC designs are for various reasons difficult to compare for among other reasons very different aimed operation temperatures e.g. Plansee 800°C and DTU 650–700°C. Ceres Power is aimed at an even lower temperature of 500–600°C due to the use of a doped ceria based electrolyte layer. Doped ceria based electrolyte is a mixed ionic and electronic conductor (MIEC). The MIEC behavior increases with increasing temperature and results in a partial short circuit. This is observed as a lowered open circuit voltage (OCV) compared to the theoretical value. It furthermore complicates performance evaluation and comparison as the degree of electronic conduction of the doped ceria electrolyte is polarization dependent.

Nonetheless, to overcome the issues of electrolyte MIEC behavior, Ceres Power has recently introduced a so-called electronic blocking layer.15 The result in a 3 layered electrolyte configuration. How such further complications to the cell design affects the overall stack cost is unclear.

It is the aim of the present study to explore the feasibility of reducing the metal support (MS) layer thickness and for improved performance explore the feasibility of opening up the support layer. For opening up the support layer two routes are explored. The first route is introduction of gas channels via puncturing of the green tape casted support layer. The second route is modification of the sintering profile. Realizing this may serve as a means of enabling faster startup and increase the stack power density in terms of weight and volume.

Experimental

Cell fabrication.—The half-cell (i.e. metal support, anode, electrolyte) processing and infiltration route, described in Ref. 16,17, was also used in this study. The processes involved tape casting of the layers: metal-support (a ferritic stainless Fe22Cr steel alloy), cermet backbone (with 0–50 vol.% Y-doped doped ZrO₂ with respect to metal), and ScYSZ electrolyte, followed by a subsequent co-sintering of these layers in a reducing atmosphere (H₂/Ar). The electrocatalytically active phase, comprising a precursor solution of Ce₀.₉Gd₀.₁O₁.₉5 and Ni (hereafter referred to as Ni:CGO), was infiltrated as a next step. The infiltration of electrocatalysts followed the same procedure as described elsewhere.16 Cells being evaluated for electrochemical performance were deposited with a Ceₒ.₉Gdₒ.₁O₁.₉₅ inter-diffusion barrier layer on the electrolyte, using the physical vapor deposition technique as described in Ref. 16. The electrocatalytically active phase, comprising of (La₀.₃Sr₀.₇CoₓNi₀.₃₋ₓ)O₂₋δ (LSC) was applied by screen printing as the last component. For introduction of gas channels into the MS-SOFC support layer, the green tape casted support layer was rolled to various degrees with a spiked roller. The increasing porosity of the support layer was achieved by a 5 fold reduction of the dwell time at the sintering temperature Tₘₙₓ.

Electrochemical characterization.—The single cells were cut out of large 12 cm × 12 cm cells and had a 54 mm × 54 mm cell area with...
Figure 1. Fabricated 12 cm × 12 cm half-cell with systematic variation of the metal support (MS) layer thickness. DTU state-of-the-art (SoA) MS-SOFC support layer thickness is ∼313 μm.

an active area of 16 cm² (defined by the screen-printed cathode layer). The cells were tested in an alumina housing used for conventional anode-supported cells, and the test house and positions of voltage probes and current pick-up points were as described in Ref. 19. A flat Ni net was used as a current collector on the anode side, and Au net was used on the cathode side. The cell rested directly on the flat Ni net with the edges being sealed with glass. The fuel gas was distributed from one side to the other via milled gas trenches in a 1 mm thick Ni block, which was embedded in the alumina test housing. On top of this arrangement, on the cathode side, an alumina block with the flat current collecting Au nets was put and a weight of 4 kg was applied. No sealing was used on the cathode side since the gas was distributed from the middle of the cathode and outwards via milled gas trenches in the alumina top block. The cells were heated to 800 °C for 5 h in order to seal the cells and sinter the cathode before the performance and durability tests were started. During startup air was supplied to the cathode and dry 9% H₂ in N₂ to the anode side (to prevent corrosion of the metal support at the elevated temperature).

Polarization curves and impedance data were collected in the temperature range 650–750 °C with 4% and 20% humidified H₂ on the anode side, and air or oxygen on the cathode side. The fuel and air flows were maximum 1.5 and 8.75 Nl h⁻¹cm⁻², respectively. Specified fuel utilization (FU) corrected Area Specific Resistance (ASRₘₜₜ) within the present paper was calculated as outlined in Ref. 19.

The electrochemical impedance (EIS) data was recorded using a Solartron 1260 Gain-Phase Analyser and a Solartron 1255B frequency response analyser (Solartron Instruments, Houston, Texas) with a perturbation amplitude of 60 mA. The impedance was recorded in the frequency range 96.850 kHz–0.096850 Hz with 12 points per decade. For visualization at which frequencies changes occur in the impedance spectra during testing, the method “Analysis of Difference in Impedance Spectra” (ADIS) was used²⁰ along with the method “Distribution of Relaxation Times” (DRT).²¹ In the ADIS method the difference between the derivative of the real part of the impedance with respect to frequency is plotted as a function of log(frequency).

Microstructural characterization.—The microstructure of the various cell samples was investigated using polished cross-sections. The polished cross-sections were prepared by vacuum embedding the samples in Struers epoxy resin (Epofix); ground using SiC paper; polished using 6, 3 and 1 μm diamond paste, and then carbon coated to eliminate surface charging. Scanning electron microscopy (SEM) imaging with backscattered electrons was performed using a Hitachi TM1000 tabletop SEM.

Results and Discussion

Effect of reducing the metal support thickness.—The effect of reducing the half-cell metal support layer thickness is shown in Figure 1. As can be seen, the effect of reducing the support layer thickness from the standard DTU SoA MS-SOFC thickness of ∼313 μm gradually to 108 μm is predominantly an increase in support layer densification. The cause for the densification is the sintering mismatch between the layers. The ScYSZ layer sintering starts at a lower temperature than the sintering of the anode and support layer. Thus, at reduced support layer thicknesses the electrolyte layer has a more dominating role in the overall shrinkage of the co-sintered multilayered half-cell structure. A lower support layer thickness in itself will lower the anode gas diffusion length. However, the increase in support layer densification results in a reduced porosity ε, reduced
density of percolated gas transport pathways through the support layer and furthermore it increases the tortuosity $\tau$ of the gas transport pathways. That it is reasonable to expect higher tortuosity values with increasing support layer densification has previously been illustrated by studies of a DTU MS-SOFC with a relatively dense support layer.\textsuperscript{22} In here high tortuosity values around 5 were found from modelling of 3D microstructural reconstructions. For comparison, AS-SOFCs with the conventional Ni:YSZ cermet anode have typically a tortuosity value of around or slightly below 2.\textsuperscript{23-25} It must be noted that some confusion regarding tortuosity $\tau$ and tortuosity factor $\tau^2$ exist within literature as pointed out by Epstein.\textsuperscript{26} The impact of porosity and tortuosity on the effective diffusion through a porous structure is conveniently illustrated by the following relationship:\textsuperscript{26}

$$D_{eff} = D \cdot \frac{\varepsilon}{\tau^2}$$ \hspace{1cm} [1]

Here $D_{eff}$ is the effective diffusion coefficient, while $D$ is the bulk diffusion coefficient. Thus, the gas permeability but also the uniformity of the flowfield through the support layer is reduced. The latter may result in a larger degree of lateral gas transport within the anode layer. The envisioned consequences of an increased support densification is summarized in Figure 2.

Besides having an effect on the gas transport properties, a reduced support porosity and channel connectivity will also have an effect on the electrocatalyst loading and distribution when deposited by infiltration. A higher porosity will accumulate a larger amount of liquid and thus electrocatalyst upon an infiltration cycle. For these reasons it is expected that higher loadings and thus thicker electrocatalyst coatings will be the result for highly porous microstructures with large pores. Furthermore, in the case of dense microstructures it may be difficult to deposit sufficient anode electrocatalyst by infiltration without blocking or severely reducing the gas transport properties of the support layer. For a broad pore size distribution, there seems to be a tendency that there is an accumulation of electrocatalyst associated with the smaller pores. Thus, the electrocatalyst is not evenly distributed within the microstructure. A possible explanation could be that the capillary forces has a more predominant effect for the smaller pores. Therefore upon water evaporation when the sample is heated after infiltration, the water within the smallest pores is the last to evaporate. Since the concentration increases as water is evaporated a larger electrocatalyst deposition will take place in association with the smallest pores. For a more even electrocatalyst deposition it therefore seems desirable with a narrow pore size distribution. However, for other purposes such as a given desirable sintering characteristics this may not be feasible. It should be noted that the effect of infiltration procedures and microstructure on electrocatalyst distribution has been considered before for example in Ref. 27.

**Increased support porosity by creation of gas channels.**—In Figure 3A a gas channel is seen from the top, which has been prepared by puncturing of the green tape casted support layer. A cross section of such a gas channel is shown in Figure 3B. From the images it can be seen that the diameter of the introduced gas channels are approximately 40 $\mu$m. Cells A and B with two different density of gas channels were fabricated. A rough estimation of the gas channel density and average distance between gas channels is summarized in Table I. In order for the gas transport via gas channels to sense each other and thereby have an effect on the anode area, it seems as a first approximation reasonable to assume that the spacing between

<table>
<thead>
<tr>
<th>Table I. Gas channel density of studied cells.</th>
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<tr>
<td>Cell</td>
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<tr>
<td>-----</td>
</tr>
<tr>
<td>A</td>
</tr>
<tr>
<td>B</td>
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</tbody>
</table>
Figure 4. Electrochemical characteristics at 650°C and 700°C with air as oxidant and 20% humidified H₂ as fuel. A) IV characteristics at 650°C and 700°C. The fuel utilization corrected area specific resistance (ASRcorr) of the cells at 650°C are as follows: ASRcorr (DTU SoA 313 μm MS) = 0.453 Ω cm², ASRcorr (175 μm MS) = 0.626 Ω cm², ASRcorr (175 μm MS with gas channels) = 0.492 Ω cm². B) The associated impedance characteristics at open circuit voltage at 650°C. The solid points indicate the frequencies 96.85 kHz, 9.685 kHz, 968.5 Hz, 96.85 Hz, 9.685 Hz, 968.5 mHz, 96.85 mHz.

The gas channels should be comparable to that of the thickness of the support layer. This is the case for B cells with the highest density of gas channels where the average distance between the gas channels is approximately 235 μm, which is close to the support layer thickness of 175 μm. In Figure 4 the electrochemical test results are shown of a DTU SoA MS-SOFC with 313 μm thick support together with cells with a support thickness of 175 μm with and without introduced gas channels at 650°C and 700°C respectively. As can be seen, the lowering of the support layer thickness from 313 μm to 175 μm results in a performance loss with an increase in ASRcorr (650°C) from 0.453 Ω cm² to 0.626 Ω cm². The introduction of gas channels in the cell with 175 μm thick support with a low density of gas channels 2 per mm² according to Table I did not result in any performance improvement. However, the high density of gas channels 18 per mm² according to Table I did result in a performance improvement with an ASRcorr (650°C) of 0.492 Ω cm². This is to be considered similar to the performance of SoA cells. The associated impedance characterization at 650°C at open circuit voltage conditions are shown in Figure 4B. In Figure 5 the impedance spectra and differences between the spectra is visualized in terms of distribution of relaxation times (DRT) and analysis of difference in impedance spectra (ADIS). For the ADIS plots in Figure 5B, the impedance spectrum of the worst performing cell with MS 175 μm at 650°C was used as reference. Since the differences between the MS-SOFCs lies in the anode architecture only the anode processes are affected. From previous anode development studies and detailed impedance studies the following processes summarized in Table II have been identified. From the two ways of visualizing the processes and changes thereof in Figure 5 consensus seems exist that it is predominantly differences in the two processes located at ~100 Hz and at ~50 kHz, which is observed among the cells. The process at 100 Hz is the coupling between oxide ionic conduction of the infiltrated Ni:CGO electrocatalyst coating and the faradaic oxidation reaction at the surface of the coating. The coupling is appropriately described by a so-called transmission line. For further details and justification the reader is referred to Refs. 28–30. The high frequency process at around 50 kHz is the oxide ion charge transfer at the interface between the anode and the electrolyte layer.

Table II. Summary of impedance spectroscopy identified anode related processes. The given impedance arc summit frequencies are for a temperature of 650°C.

<table>
<thead>
<tr>
<th>Freq.</th>
<th>~500 kHz</th>
<th>~100 Hz</th>
<th>~30 Hz</th>
<th>~1 Hz</th>
</tr>
</thead>
<tbody>
<tr>
<td>Process</td>
<td>Possible charge transfer of O₂⁻ at anode/electrolyte interface</td>
<td>Electrochemical response of mixed ionic and electronic conducting composite anode</td>
<td>Gas diffusion</td>
<td>Gas conversion</td>
</tr>
</tbody>
</table>

Figure 5. Distribution of Relaxation Times (DRT) and Analysis of Difference in Impedance Spectra (ADIS) plots of the impedance data in Figure 4B. For the ADIS plots the impedance spectrum of the cell with 175 μm support layer has been used as reference.
The process is presumably somewhat overlapped with the response of the electrolyte grain boundaries. The two processes affected are both dependent on the Ni:CGO electrocatalyst coating thickness. A thicker electrocatalyst coating will improve the oxide ion conduction of the anode and thus the anode transmission line response at \( \sim 100 \text{ Hz} \). Furthermore, a thicker coating will make it more favorable for the oxide ion charge transfer process at the anode/electrolyte interface to spread out and utilize a larger area. This leads to a lowering of the resistance. However, despite that the anode functional layer fabrication procedure is unchanged, it cannot be ruled out that the anode microstructure is affected by the changes in the support layer during co-sintering. A denser anode microstructure is expected to have a similar effect as a thinner electrocatalyst coating as discussed previously. The porous structure of the anode can as in porous electrode theory be modelled as an array of columns each representing a transition line. A denser anode microstructure would in this interpretation correspond to a lower density of columns/transmission lines per area. Thus, it is a simple active anode/electrolyte area affect, where both the processes at 100 Hz and \( > 50 \text{ kHz} \) are affected.

For comparison of performances we will from here on use 0.7 V at 700 °C as bench marking condition for the performance of MS-SOFCs. In Figure 4 it is possible to see that the performance at benchmark conditions of DTU SoA MS-SOFCs is 0.69 Wcm\(^{-2}\) at a FU of 33%.

Increased support porosity via 5 fold reduction in the dwell time of the maximum sintering temperature.—In Figure 6A a cross section of a MS-SOFC half cell with a \( \sim 175 \mu \text{m} \) thick support is seen, where the porosity of the support layer has successfully been increased in comparison to the DTU SoA MS-SOFCs in Figure 1. The associated performance is shown in Figure 6B, where it is possible to see an improved performance of 0.89 Wcm\(^{-2}\) at bench marking conditions @700 °C, 0.7 V with a FU of 43%. In addition, the performance characteristics can be seen of using pure oxygen as oxidant, which results in significant performance improvements. This illustrates that there also is a clear scope for further improvement of the cathode in future cell performance development. For historical reasons and as a standard polarization curves are stopped at 0.6 V at DTU. However, there are no indications or guideline within the literature that SOFCs cannot be operated at lower potentials and thereby higher power densities. Nevertheless, it is commonly accepted that degradation rates are usually dependent on the degree of polarization/current load, temperature and used fuel utilization. To illustrate the potential of the studied MS-SOFCs the polarization curves are linearly extrapolated, which furthermore provide potential peak power densities (PPD). In Figure 7A a cross section of a MS-SOFC half cell with a \( \sim 240 \mu \text{m} \) thick support can be seen, with the most open support microstructure (highest porosity) of the studied MS-SOFCs. From Figure 7B the associated performance characteristics can be seen, which shows a significantly improved performance of 1.01 Wcm\(^{-2}\) at bench marking conditions @700 °C, 0.7 V with a FU of 48%. The peak power densities at 700 °C of DTU SoA MS-SOFC in Figure 4 and the improved cells in Figures 6 and 7 are 0.78 Wcm\(^{-2}\) (0.52 V & FU 50%), 1.12 Wcm\(^{-2}\) (0.48 V & FU 78%) and 1.20 Wcm\(^{-2}\) (0.50 V & FU 81%) respectively. This corresponds to a performance increase of 54% and even more if the...
effect of FU is taken into consideration. The latter will be discussed further in the following section overall discussion. For the peak power densities at 750°C in Figures 6 and 7 the fuel utilization is 92% and 100%, which clearly is unrealistic in the sense that the linear extrapolation is invalid. The gas conversion resistance (change in Nernst potential) will approximately for fuel utilization lower than 10% and higher than 90% be highly nonlinear and increase drastically. The consequence is that in particular the peak power density at 750°C in Figure 7 will be somewhat lower than the 1.56 W cm⁻², which the linear extrapolated curve indicate.

Overall Discussion

It was demonstrated, that it was feasible to decrease the support layer thickness substantially. From a processing perspective MS-SOFCs with a 108 μm thick support is handling wise somewhat challenging, whereas MS-SOFC with a support thickness of at least 150 μm seems okay. However, an increasing densification of the support layer was observed as the thickness was lowered. To avoid this, a better sintering shrinkage match of the individual co-sintered layers need to be achieved or alternative routes for opening up of the support layer needs to be explored. In this study, it was demonstrated that it is possible to introduce gas channels in the MS-SOFCs via puncturing of the green support layer tape. From the results, it is clear that a clear of the situation is needed before it is possible to get an effect on the cell performance. Even with a relatively high density of gas channels with a mutual average spacing of 235 μm, gastight electrolyte layers on a large cell size of 12 cm × 12 cm were achieved. However, it is expected that when continuously increasing the gas channel density by puncturing of the green support layer tape, the sintering characteristics will be affected at some point. This change in sintering characteristics might ultimately affect the co-sintering process and thus the quality of the electrolyte layer. A further implication of the introduced gas channels is that it represents a significant broadening of the pore size distribution. This is expected to have some impact on the distribution of Ni:CGO electrocatalyst deposition via infiltration. For instance it seems reasonable to expect a somewhat higher electrocatalyst loading at or near the large pores (e.g. gas channels). The impedance and the change hereof in Figures 4B and 5 were found to be dependent on the support layer densification. This can be understood in terms of the deposited electrocatalyst coating thickness as a function support layer densification. However, a denser support microstructure is also expected to affect and limit gas transport properties as illustrated in Figure 2. The tested condition with 20% humidified H₂ as fuel minimizes both the gas diffusion and gas conversion resistances and are at this condition relatively insensitive to small variations. This is in agreement with either low H₂ or H₂O. This may explain why these processes seem relatively unaffected upon the microstructural changes. However, if we for simplicity consider the H₂ gas flux J(H₂) to be governed by diffusion (Ficks first law):

\[
J (H_2) = -D_{eff} \frac{\partial C_{H_2}}{\partial x}
\]

The driving force for the flux J(H₂) will be the concentration gradient through the anode and support layers, which still is high with 20% humidified H₂ as fuel at OCV conditions. This is unlike the SOFC technology relevant condition of high fuel utilization, where the H₂ concentration gradient through the anode and support layer at the fuel outlet is small and hence the H₂ flux may be insufficient resulting in an apparent fuel starvation. Indications of such unwanted and limiting situations have been observed for cells with a dense metal support layer. Thus, from a performance and technological perspective a more open microstructure is needed. It should be noted that for a more accurate modelling of the microstructural gas transport properties more advanced models such as e.g. the dusty gas model is needed. Since single cell testing is used for performance evaluation, the fuel utilization is not insignificant as Figures 4, 6 and 7 illustrate. This is unlike button cell testing where the cells are flushed with fuel and the effect of fuel utilization is suppressed. This is practical feasible as the active area is very small (typically ≤ 0.5 cm²). In addition many variations of button cell setups exist with different gas flow configurations. In the used single cell testing, the flow configuration is similar to what will be present in a stack that is plug flow. Thus, the condition in single cell testing mimics stack conditions and the measured performance of the cell will therefore be very close to what can be expected in a stack environment. The importance and impact of fuel utilization has been considered before in Ref. 19. In here, a methodology for compensating for the fuel utilization has been suggested. An exact compensation of fuel utilization is not feasible, but estimation of a conservative first approximation is feasible. Basically, the methodology attempts to compensate for the change in Nernst potential as the fuel is utilized. Area specific resistances (ASRs) corrected by this methodology is specified as ASRcorr. In Figure 8 the effect of fuel utilization is illustrated on the performance characteristics with air as oxidant of the cell in Figure 7. Assuming linear polarization relationship the calculated ASRcorr from the measured data has been used to forecast the effect of fuel utilization. As expected it is possible to see that the higher performance at the highest temperature of 750°C is affected the most. From the plot it is possible to see a fuel utilization corrected performance of 1.20 W cm⁻² at bench marking conditions @0.7 V, 700°C and a PPD of 1.4 W cm⁻² at 700°C. Key performance characteristics of the studied cells are for convenience summarized in Table 3. Thus, MS-SOFCs with higher porosity and therefore improved gas transport properties and higher electrocatalyst loadings does lead to improved performance of the anode and therefore also the cell. Recently, M. Tucker has also shown for a MS-SOFC design with comparable electrode architectures and electrocatalysts that boosting infiltrated electrocatalyst loadings lead to significant performance improvements. The result was an impressive boosting of the performance at 700°C with a peak power density increase from 0.240 W cm⁻² (0.5 V) to 1.1 W cm⁻² (0.58 V). However, long term testing at an operation at 700°C and at 0.7 V for 1200 h showed severe degradation with a peak power density loss of 73%. A summary of reported key performance characteristics of laboratory scale, using different manufacturing process and materials is provided in Table 13 of the recent MS-SOFC review from 2017 by Krishnan. From the summary it is clear that the performances reported within the present study are superior compared to the reviewed studies in Ref. 1 in particular if temperature is taken into consideration. From the present data it is certain how much the anode can be further improved. In any case, there is for even further performance improvements a clear potential for the cathode to be further optimized considering the effect on performance of varying the oxidant gas between air and pure oxygen in both Figures 6B and 7B. At last the electrolyte layer can be further densified and made thinner and thereby additionally boost the performance.
Table III. Summary of performance characteristics of studied cells. The used abbreviations are as follows FU = fuel utilization, ASR = area specific resistance, ASRcorr = fuel utilization corrected ASR, PPD = peak power density, PPDcorr = fuel utilization corrected PPD.

<table>
<thead>
<tr>
<th>DTU SoA MS 313 μm</th>
<th>DTU open MS 175 μm</th>
<th>DTU open MS 240 μm</th>
</tr>
</thead>
<tbody>
<tr>
<td>ASR (0.6 V) / μcm²</td>
<td>0.48</td>
<td>0.38</td>
</tr>
<tr>
<td>ASRcorr (0.6 V) / μcm²</td>
<td>0.45</td>
<td>0.36</td>
</tr>
<tr>
<td>PPD / Wcm⁻²</td>
<td>0.53 (0.52 V, FU 34%)</td>
<td>0.75 (0.47 V, FU 53%)</td>
</tr>
<tr>
<td>PPDcorr / Wcm⁻²</td>
<td>0.60 (0.52 V)</td>
<td>0.76 (0.52 V)</td>
</tr>
<tr>
<td>ASR (0.6 V) / μcm²</td>
<td>0.32</td>
<td>0.25</td>
</tr>
<tr>
<td>ASRcorr (0.6 V) / μcm²</td>
<td>0.29</td>
<td>0.22</td>
</tr>
<tr>
<td>PPD / Wcm⁻²</td>
<td>0.78 (0.52 V, FU 50%)</td>
<td>1.12 (0.48 V, FU 78%)</td>
</tr>
<tr>
<td>PPDcorr / Wcm⁻²</td>
<td>0.90 (0.51 V)</td>
<td>1.22 (0.52 V)</td>
</tr>
<tr>
<td>Benchmarking / Wcm⁻²</td>
<td>-</td>
<td>0.18</td>
</tr>
<tr>
<td>Benchmarking corr / Wcm⁻²</td>
<td>-</td>
<td>0.15</td>
</tr>
<tr>
<td>Benchmarking / Wcm⁻²</td>
<td>1.40 (0.50 V, FU 92%)</td>
<td>1.56 (0.62 V, FU 100%)</td>
</tr>
<tr>
<td>Benchmarking corr / Wcm⁻²</td>
<td>-</td>
<td>1.69 (0.51 V)</td>
</tr>
<tr>
<td>Benchmarking @ 0.7 V, 700°C</td>
<td>0.69 (FU 33%)</td>
<td>0.89 (FU 43%)</td>
</tr>
</tbody>
</table>

Conclusions

It was demonstrated possible in the DTU MS-SOFC design and fabrication route to reduce the metal support considerably from standard 313 μm to 108 μm. However, upon the reduction of the metal support layer thickness an increased densification of the layer and an associated performance loss was observed. To mitigate the performance loss, two routes for increasing the porosity of the support layer was demonstrated possible. The first route was introduction of gas channels via puncturing of the green tape casted support layer. In the case of an average gas channel spacing of 235 μm on MS-SOFCs with a support thickness of 175 μm a significant performance improvement was observed. The second route was a 5 fold reduction of the sintering temperature dwell time, which lead to significant increase of the support porosity. This resulted in significant performance improvements with a maximum increase of 46% in performance to 1.01 Wcm⁻² at 0.7 V, 700 ºC with a FU of 48%. This corresponds to fuel utilization corrected peak power densities of 0.84 Wcm⁻² at 650 ºC, 1.40 Wcm⁻² at 700 ºC and 1.90 Wcm⁻² at 750 ºC. All results were obtained on gastight stack technology relevant sized cells of 12 cm × 12 cm.

Acknowledgment

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References

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