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Efficiency Investigations of a 3 kW T-Type Inverter for Switching Frequencies up to 100 kHz

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Abstract—This paper deals with a 3 kW multilevel inverter used for PV applications. A comparison has been made based on simulations using IGBTs and SiC MOSFETs to see how much efficiency can be gained when SiC diodes are used. A prototype with the same IGBTs and SiC MOSFETs has been built but using regular soft-recovery Si diodes instead of SiC diodes. Efficiencies and switching transitions for different switching frequencies up to 100 kHz have been measured. Thermal investigations of both IGBTs and SiC MOSFETs have been conducted to analyze the feasibility of increased switching frequencies. When SiC MOSFETs are used in combination with Si diodes, switching frequencies could be doubled achieving the same efficiencies than the IGBT converter.

Keywords—SiC MOSFET, IGBT, multilevel inverter, reverse recovery current

I. INTRODUCTION
Photovoltaic (PV) systems have become more and more attractive in recent years. Especially residential PV inverter systems gained much attraction. Due to the low efficiency of the PV panels themselves, much attention must be paid in the design of the PV inverter which leads to a strong demand for low cost and high efficiency power converters. Two-level inverters have the advantage of having a lower cost factor due to the smaller amount of components, being simple in structure and control but suffer from a strong switching frequency and power dependency as well as a relatively large output filter [1]. Multilevel topologies such as the Neutral-Point-Clamped (NPC) inverter have, on the other hand, efficiencies which are less depending on the switching frequency and they give a good compromise between system complexity, cost and efficiency [2]-[3]. Among the three-level inverter topologies, the T-Type inverter (also called Conergy [4] or BSNPC [5]) shows a higher efficiency than the NPC counterpart for low to medium switching frequencies [3]. Furthermore, the efficiency of the T-Type inverter can be improved by using Silicon Carbide (SiC) switching devices in order to reduce switching losses by increased switching transitions and hence increase the overall efficiency. Previous work has shown that SiC switching devices such as normally-on/off SiC JFETs, SiC BJTs and SiC MOSFETs show superior switching performance in various applications over their silicon counterparts, [6]-[7]. An all SiC MOSFET T-Type inverter has been introduced in [8] achieving efficiencies over 98%. A major aspect when using fast switching SiC devices is to equip the converter with SiC diodes instead of Si diodes in order to keep the switching losses low; otherwise the reverse recovery current caused by a high di/dt will increase the switching losses again and hence dampen the efficiency improvements. The feasibility of using SiC MOSFETs in the T-Type converter is investigated on a practical approach in this paper. Two 3 kW T-Type inverters equipped with 1200 V IGBTs and 1200 V SiC MOSFETs are compared for different power levels and switching frequencies. In Section II the topology including its modulation and current commutation is explained. Simulations of the topology have been carried out in Section III, in which expected efficiencies are obtained and a breakdown loss analysis is conducted. Practical results and efficiency measurements of a 3 kW prototype are introduced in section Section IV. Efficiency investigations for increased switching frequencies are investigated in Section V.

II. THE T-TYPE INVERTER
The T-Type inverter is a derivation from the NPC inverter. One phase leg comprises of four switching devices and four diodes as shown in Fig. 1. The output voltage of the inverter has three states with reference to the midpoint M, i.e. \(+0.5V_{DC}\), \(0\) and \(−0.5V_{DC}\). It is a commonly used topology in three-phase PV inverters in the medium power range and rather low switching frequencies of up to 16 kHz. Switches \(S_1\) and \(S_3\) including their free-wheeling diodes \(D_3\) and \(D_4\) require a breakdown voltage of at least the full DC link voltage \(V_{DC}\) whereas switches \(S_3\), \(S_4\) and the diodes \(D_1\) and \(D_2\) require a breakdown voltage of at least half the DC link voltage. In PV inverter systems, the DC link voltage can usually increase up to 1000 V, so \(S_1\), \(S_2\), \(D_3\) and \(D_4\) are 1200 V and \(S_3\), \(S_4\), \(D_1\) and \(D_2\) are 600 V devices to have a margin for overvoltages. A sinusoidal output voltage can be obtained by having switches \(S_1\) and \(S_2\) operated at a chosen switching frequency whereas switches \(S_3\) and \(S_4\) operate at grid frequency as shown in Fig. 1. The T-Type topology benefits from having lower conduction losses than its NPC counterpart because only...
one switch conducts current at the same time. The current commutations for a resistive load are shown in Fig. 2.

### III. SIMULATION RESULTS WITH SiC DIODES

The simulations were done in PLECS and the semiconductor parameters were taken from their datasheets. The specifications for the inverter are shown in Table I. Switches $S_1$ and $S_2$ are chosen to be IGBTs due to their higher breakdown capabilities compared to Si MOSFETs. Their SiC counterpart will be a 1200 V SiC MOSFET C2M0080120D from Cree. Switches $S_3$ and $S_4$ are chosen to be SiC diodes to show possible achievable efficiencies when no reverse recovery is taken into account. Table II shows the semiconductors used in the simulations. The Si converter comprises of 1200 V IGBTs and the SiC converter comprises of 1200 V SiC MOSFETs.

The simulation results of the T-Type inverter for 16 kHz and 30 kHz are shown in Fig. 3. At a switching frequency of 16 kHz, a maximum efficiency of 97.9% is achieved when IGBTs are used and 98.6% when SiC MOSFETs are used. A larger efficiency difference between the IGBT version and SiC MOSFET version can be obtained if the switching frequency is increased to 30 kHz. Then a maximum efficiency of 97% with IGBTs and 98.2% with SiC MOSFETs are achieved. Although the specifications do not exactly match with [8], the results are close to what has been presented in previous work so that the simulations can be considered a proper representation of what to expect. A breakdown loss analysis has been conducted to show the loss distribution of the converter system. Apart from the semiconductors, losses in the filter inductor as well as the DC link capacitors have been included. The results are shown in Fig. 3. It can be seen that due to the modulation applied, switching losses mainly occur in the 1200 V switches.

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**TABLE I. SPECIFICATIONS**

<table>
<thead>
<tr>
<th>Symbol</th>
<th>Meaning</th>
<th>Value</th>
</tr>
</thead>
<tbody>
<tr>
<td>$L$</td>
<td>Output filter inductance</td>
<td>3 mH</td>
</tr>
<tr>
<td>$V_{DC}$</td>
<td>DC link voltage</td>
<td>800 V</td>
</tr>
<tr>
<td>$V_{out}$</td>
<td>Filtered output voltage, RMS</td>
<td>230 V</td>
</tr>
<tr>
<td>$P_{out}$</td>
<td>Output power</td>
<td>250 W to 3000 W</td>
</tr>
</tbody>
</table>

**TABLE II. SEMICONDUCTORS**

<table>
<thead>
<tr>
<th>Version</th>
<th>$D_1$ and $D_2$</th>
<th>$S_1$ and $S_2$</th>
<th>$S_3$ and $S_4$</th>
</tr>
</thead>
<tbody>
<tr>
<td>Si Converter</td>
<td>C4D20120A</td>
<td>IKW15N120T2</td>
<td>IKP15N60T</td>
</tr>
<tr>
<td>SiC Converter</td>
<td>C4D20120A</td>
<td>C2M0080120D</td>
<td>IKP15N60T</td>
</tr>
</tbody>
</table>

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![Fig. 1. Schematic of a single phase T-Type inverter and sinusoidal PWM scheme](image1)

![Fig. 2. Current paths in the T-Type inverter. a) Positive output voltage b) Zero output voltage c) Negative output voltage d) Zero output voltage](image2)

![Fig. 3. Current paths in the T-Type inverter for 16 kHz and 30 kHz are shown in Fig. 3. At a switching frequency of 16 kHz, a maximum efficiency of 97.9% is achieved when IGBTs are used and 98.6% when SiC MOSFETs are used. A larger efficiency difference between the IGBT version and SiC MOSFET version can be obtained if the switching frequency is increased to 30 kHz. Then a maximum efficiency of 97% with IGBTs and 98.2% with SiC MOSFETs are achieved. Although the specifications do not exactly match with [8], the results are close to what has been presented in previous work so that the simulations can be considered a proper representation of what to expect. A breakdown loss analysis has been conducted to show the loss distribution of the converter system. Apart from the semiconductors, losses in the filter inductor as well as the DC link capacitors have been included. The results are shown in Fig. 3. It can be seen that due to the modulation applied, switching losses mainly occur in the 1200 V switches.](image3)
Hence the switching frequency is a limiting factor for the efficiency of the T-Type inverter. However, switching losses can be reduced by using SiC switching devices. The effect of the fast switching capabilities of SiC devices becomes more important when a higher power density is targeted because switching losses in regular IGBTs become dominant degrading overall efficiency. Based on the simulations, switching and conduction losses in the 1200 V IGBT are relatively balanced at a switching frequency of 16 kHz whereas switching losses of the SiC MOSFETs are still smaller than the conduction losses at a switching frequency of 30 kHz. Both the size of the filter inductor and the DC link capacitors were kept constant, though a redesign of these could have reduced losses at increased switching frequencies. However, a main requirement to the simulated efficiencies is that the diodes $D_1$ and $D_2$ do not show any reverse recovery current.

IV. EXPERIMENTAL RESULTS

To see how the T-Type inverter performs with IGBTs and SiC MOSFETs, a prototype has been built which is shown in Fig. 4. For both the 1200 V IGBTs and SiC MOSFETs, a TO-247 package was used having the same pinning and hence the same printed circuit board (PCB) and layout could be used for a fair comparison. For layout optimization, $S_3$ and $D_1$ are packed in one TO-220 package and so are $S_4$ and $D_2$. Hence the whole converter could be built with four discrete devices. Only the gate drivers (Dr1 - Dr4) for the IGBTs and SiC MOSFETs were adjusted to stay within their absolute maximum ratings for the Gate-Source voltage. The IGBTs were switched on and off with a Gate-Source voltage of $\pm 15$ V whereas the SiC MOSFETs were switched on with a Gate-Source voltage of 19 V and switched off with a Gate-Source voltage of $-5$ V. For further comparisons to the simulations, the prototype is equipped with soft-recovery Si diodes instead of SiC diodes. At full power, the filtered output voltage and current are shown in Fig. 5.

A N4L PPA5500 power analyzer was used for efficiency measurements. A first comparison is made with...
an IGBT version having a gate resistance of 2.2 Ω and a SiC MOSFET version having a gate resistance of 5 Ω. The results are shown in Fig. 6.

It can be seen in Fig. 6 that the efficiency could be improved when a SiC MOSFET with a gate resistance of 5 Ω is implemented. However, efficiency improvements are larger as the switching frequency is increased. At 16 kHz, a maximum efficiency improvement of 0.3 % is achieved. Increasing switching frequency to 30 kHz leads to a maximum efficiency improvement of 0.8 %. It can furthermore be seen that the SiC MOSFET inverter has similar efficiencies at 30 kHz than the IGBT inverter at 16 kHz. The switching frequency for the SiC converter is therefore increased to 60 kHz and plotted in Fig. 6. It can be seen that the SiC converter at 60 kHz has similar efficiencies than the IGBT converter at 30 kHz which yields to the conclusion that the switching frequency can be doubled when SiC MOSFETs are implemented without degrading the efficiency. The case temperatures of the IGBTs and SiC MOSFETs were measured to get a comparison of the power dissipation in such devices. The operating conditions are at full power, i.e. 3 kW and 20 kHz for the IGBT and 30 kHz for the SiC MOSFET. The case temperatures were measured with an infrared camera and the results are shown in Fig. 7.

It is seen in Fig. 7 that even though the switching frequency is increased, the case temperature for the SiC MOSFET is around 10 °C lower. The thermal resistance of the 1200 V IGBT is given in the datasheet to be 0.63 K/W and the thermal resistance for the SiC MOSFET is given to be 0.60 K/W. Hence the junction temperature of the SiC MOSFET is around 10 °C lower as well. That the case temperature of the 600 V IGBT is higher than the case temperature of the SiC MOSFET can be explained by the fact that a regular TO-220 package for the IGBT was used. In that package, the IGBT comes along with a Si soft recovery free-wheeling diode. These free-wheeling diodes for the two 600 V IGBTs are used to be D1 and D2. As a consequence, the TO-220 package withstands the power dissipation for both the IGBT and the free-wheeling diode. A switching transition for both turn on and turn off of the SiC MOSFET has been captured. The gate resistance is kept to be 5 Ω, output power is 900 W and switching frequency is 16 kHz. The current was measured with a Rogowski coil having a 20 MHz bandwidth limitation. The Drain-Source voltage was measured with a high voltage probe with a 400 MHz bandwidth limitation and the Gate-Source voltage was measured with a voltage probe having a 500 MHz bandwidth limitation. Furthermore, the time delay of 24 ns of the Rogowski coil was compensated in the measurements and the attenuation for the current measurement was set such that 2 V/div equals to 2 A/div. The transitions are shown in Fig. 8. It can be seen in Fig. 8a that the SiC MOSFET switches 400 V within 30 ns resulting in a dv/dt of more than 13 kV/µs. A maximum dv/dt was measured to be 25 kV/µs. The current rises 2 A within 4 ns resulting in a di/dt of 500 A/µs. The peak current is measured to be 10 A. During the turn off transition as shown in Fig. 8b, the maximum dv/dt is measured to be 20 kV/µs. The maximum di/dt is 400 A/µs. For comparison, the IGBT switched 400 V within 120 ns resulting in a dv/dt of 3 kV/µs.
V. EFFICIENCY INVESTIGATIONS FOR INCREASED SWITCHING FREQUENCIES

It is seen that the efficiencies could be improved when SiC MOSFETs are implemented and the switching frequency could be doubled achieving the same efficiencies when IGBTs are used. It is therefore of interest to furthermore increase the switching frequency and to see how it affects the efficiency. As a last operating point, the switching frequency is increased to 100 kHz. The efficiency curves for the SiC converter at different switching frequencies are shown in Fig. 9.

It can be seen that the overall efficiency dramatically drops as the switching frequency increases up to 100 kHz. Also, the maximum efficiency point is shifted down to a lower power operating point compared to lower switching frequencies. The measurements were limited to a maximum power of 1.6 kW as the case temperature of the TO-220 packages became too high and hence the risk of a thermal damage was increased. However, the case temperature of the SiC MOSFETs were still below 80 °C at an output power of 1.6 kW. So the limiting factor are the 600 V devices in the TO-220 package. A thermal picture of the TO-220 package at an operating point of 60 kHz and 2.7 kW was taken to verify the limiting factor at increased switching frequencies. The result is shown in Fig. 10.

VI. CONCLUSIONS

In this paper the feasibility of SiC switching devices on a 3 kW T-Type inverter topology for PV applications has been investigated. Simulations with regular IGBTs and SiC MOSFETs have been carried out including a breakdown loss analysis to investigate the loss contribution on the overall efficiency. It is shown that efficiency improvements can be achieved when SiC MOSFETs are equipped in combination with SiC diodes. A prototype has been built using the same IGBTs and SiC MOSFETs but regular Si diodes instead of SiC diodes. Efficiency measurements have been done to see how much the reverse recovery current of the Si diodes will affect the overall efficiency. Using Si diodes instead of SiC diodes, efficiency improvements could be achieved but not as much as it could be in the simulations.
with SiC diodes. However, switching frequency could be doubled achieving the similar efficiency curves when IGBTs are used. Switching frequencies were increased up to 100 kHz to see how much efficiency drop one might expect. The limiting factor at increased switching frequencies are the 600 V devices in a TO-220 package. Using external SiC diodes in combination with 600 V IGBTs could furthermore improve efficiencies and enable higher switching frequencies.

REFERENCES


