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Detailed Behavior Analysis for High Voltage Bidirectional Flyback Converter Driving DEAP Actuator

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Abstract—DEAP (Dielectric Electro Active Polymer) is a new type of smart material. The tubular actuator based on DEAP material has various potential applications and is fundamentally a capacitive load. A high voltage bidirectional converter is required to provide power for the actuator. A bidirectional flyback based converter has been implemented. The parasitic elements have serious influence for the operation of the converter, especially in the high output voltage condition. The detailed behavior analysis has been performed considering the impact of the critical parasitic parameters. The converter has been analyzed for both charging and discharging processes in low and high output voltage operating occasions. The experimental waveforms can validate the analysis.

Keywords—DEAP actuator; bidirectional; flyback; high voltage; behavior analysis

I. INTRODUCTION

As a smart material, DEAP (Dielectric Electro Active Polymer) material started to receive increasing attention among scientists and engineers since early 1990s [1], [2]. DEAP material possesses simple structure - a thin dielectric elastomer sheet sandwiched between two compliant electrodes [2]. Because of the electrostatic pressure, the elastomer is compressed in the thickness direction and expands in the transverse direction by applying voltage to the electrodes [1], [2]. This actuation property makes it possible to develop DEAP material based actuator. In addition, the same construction as capacitor enables the DEAP to be represented by a plate capacitor whose values changes with the geometry [1], [3]. Hence, DEAP has the potential to work as a sensor when applied mechanical or electrical force changes its capacitance. Further, DEAP is a good alternative over other smart materials in the application of energy harvesting [3].

Taking the advantage of light weight, noiseless operation, large strain, high energy density, fast speed of response and high efficiency, DEAP actuators can be a promising replacement for conventional actuators as well as other smart material based actuators [1], [2], [3]. Numerous potential or conceptual applications have already been proposed by material or mechanical researchers in the field of biomimetics, micro electro mechanical systems (MEMS), acoustics, etc. Correspondingly, a variety of configurations is continuously emerging, such as the unimorph, bimorph, diaphragm, tube and roll constructions [1], [2].

Much research concerning DEAP tends to concentrate on the understanding and improvement of the polymer materials as well as the implementation of different configured DEAP actuators in various potential applications. However, not much research related to the development of driving converters for DEAP actuators has been carried out, especially for the driving devices with small size, high efficiency as well as low cost [4]. The object of this paper is to develop a bidirectional flyback based converter in order to drive DEAP actuator and perform a detailed behavior analysis for the operation of this high voltage converter.

This paper is organized as follows. Part II discusses the characteristics of tubular DEAP actuator. Part III addresses the implementation of a low input voltage high output voltage DC-DC driving converter. The corresponding operation behaviors are detailedly analyzed in Part IV. Finally, we make a conclusion in Part V.

II. CHARACTERISTICS OF TUBULAR DEAP ACTUATOR

With a simple configuration, tubular DEAP actuator, shown in Fig. 1, can be applied in a variety of occasions and has attracted considerable concern among researchers [4], [5]. To



Fig. 1. Tubular DEAP actuator.

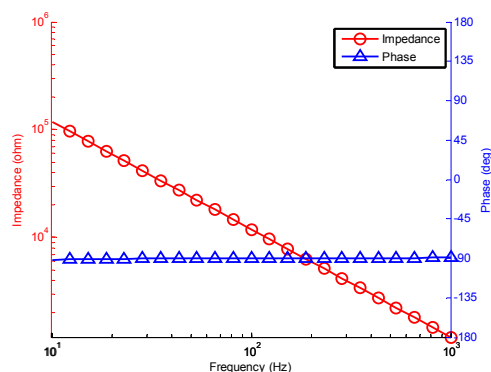


Fig. 2. Impedance versus frequency characteristic of tubular DEAP actuator.

have a better understanding of the characteristics of tubular DEAP actuator is beneficial to design desirable stimulating circuits. Firstly, the impedance versus frequency characteristic is measured using N4L PSM1735 frequency response analyzer with its impedance analysis interface, shown in Fig. 2. The -90° phase angle indicates that the measured tubular DEAP actuator is purely capacitive.

In addition, the stroke and strain versus applied voltage characteristic can be obtained by numerically solving the equations based on [6]. The stroke stands for the absolute elongation distance while strain indicates the relative expansion compared to the initial length of actuator. Corresponding to around 1.8% strain, the maximum displacement 1.05 mm under 2.4 kV implies it is necessary to fully elongate the actuator with a high voltage driving device.

Further, previous research has been performed to validate the capacitance change versus voltage characteristic [7]. According to [7], the capacitance versus strain relationship can be expressed in (1), where C_0 is the capacitance of unstrained actuator and s corresponds to the strain of the actuator.

$$C = C_0 \cdot (1 + s)^{1.83} \quad (1)$$

Together with the strain versus voltage property, we can obtain the capacitance change versus voltage characteristic. Maximum 3.2% capacitance change under 2.4 kV makes it reasonable to assume this tubular DEAP actuator to be a capacitor with constant value. This assumption tends to simplify the consideration concerning the load when designing the driving circuits.

Moreover, the electrical to mechanical efficiency has been discussed and measured in [8]. The low energy conversion ratio indicates that most electrical energy must be stored in the capacitive actuator. Therefore, a high voltage driving converter with the ability to recover the energy back to the power source is beneficial to improve the overall energy utilization ratio [1], [8].

III. IMPLEMENTATION OF HIGH VOLTAGE BIDIRECTIONAL CONVERTER

In general, autonomous actuation system tends to be preferable in the case of biomimetic and MEMS applications. Batteries are more likely to be the principle energy source in these situations. A 3 V battery powered driving converter has been proposed to implement in order to charge a DEAP actuator with 220 nF capacitance to 2.4 kV and discharge to 0 V. The designed power stage of bidirectional flyback converter is shown in Fig. 3. In addition, some critical parasitic elements are depicted as well, which will be used in the detailed behavior analysis later. In fact, the primary and secondary leakage inductors cannot exist at the same time. When energy flow is from power source to secondary side, we use primary leakage inductor to describe the untransferable energy stored in flyback transformer, and vice versa. Two capacitor charger controllers LT3750 from Linear Technology are employed to implement the control for the flyback converter. One is used to realize the charge control for the capacitive load and the discharge control is achieved through the other one. The converter has been built and the picture is shown in Fig. 4. The symbols shown in Fig. 3 and other critical parameters which

will be utilized in the following analysis have been addressed in Table I with the values.

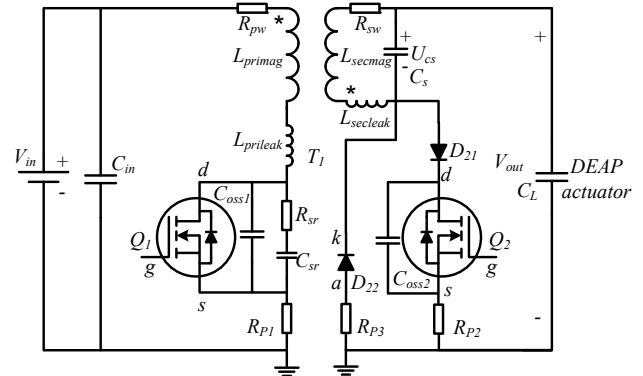


Fig. 3. Schematic of the power stage of bidirectional flyback converter.

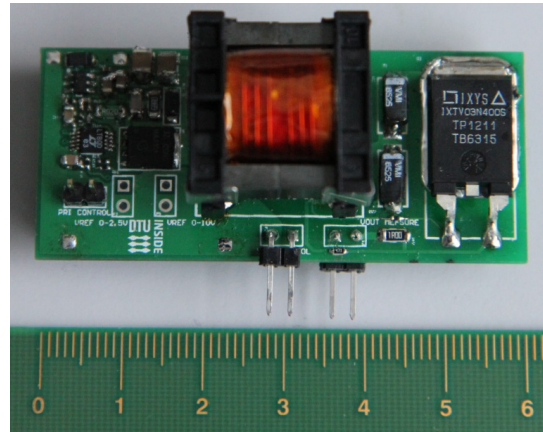


Fig. 4. Picture for the high voltage bidirectional converter.

TABLE I. HIGH VOLTAGE BIDIRECTIONAL FLYBACK CONVERTER PARAMETER DEFINITIONS AND VALUES

Symbols	Parameters	Values
L_{primag}	Primary magnetizing inductance	13 μ H
$L_{primeak}$	Primary leakage inductance	300 nH
L_{secmag}	Secondary magnetizing inductance	18.85 mH
$L_{secleak}$	Secondary leakage inductance	450 μ H
C_s	Secondary winding stray capacitance	14.2 pF
R_{pw}	Primary AC resistance	79.4 m Ω @ 1 kHz 106.81 m Ω @ 100 kHz
R_{sw}	Secondary AC resistance	4.99 Ω @ 1 kHz 29.92 Ω @ 100 kHz
R_{dsonQ1}	Q_1 drain to source on-state resistance	27 m Ω @ $V_{GS}=10$ V
C_{oss1}	Q_1 output capacitor	280 pF @ $V_{DS}=40$ V
V_{bdQ1}	Q_1 body diode forward voltage	0.7 V @ $I_F=4$ A
R_{dsonQ2}	Q_2 drain to source on-state resistance	$\leq 290 \Omega$
C_{oss2}	Q_2 output capacitor	19 pF @ $V_{DS}=2$ V
$V_{d21} V_{d22}$	High voltage diode forward voltage	7 V @ $I_F=150$ mA
R_{P1}	Primary current sensing resistor	20 m Ω
$R_{P2} R_{P3}$	Secondary current sensing resistor	1 Ω
C_{sr}	Primary snubber capacitor	220 pF
R_{sr}	Primary snubber resistor	68 Ω
C_L	Capacitive load	220 nF
V_{in}	Input voltage	3 V
N	Turns ratio (secondary turns/primary turns)	232/6
$I_{primeak}$	Primary peak current	4 A
$I_{secpeak}$	Secondary peak current	103 mA

IV. DETAILED BEHAVIOR ANALYSIS OF HIGH VOLTAGE BIDIRECTIONAL FLYBACK CONVERTER AND EXPERIMENTAL VALIDATION

Considering the critical parasitic parameters shown in Fig. 3, a detailed behavior analysis has been performed. The converter is analyzed according to the charging and discharging process respectively. In order to verify the analysis and compare analyzed results with experimental ones, the experiments have been performed and the waveforms are shown beside the analyzed waveforms.

A. Charging process

The analyzed model has been built for one single charging cycle, which is applicable for any cycle in the charging process. In fact, the analysis can be carried out based on five different time intervals. In each interval, we create model to mimic the critical behavior of the converter. For certain charging cycle, the initial output voltage is represented as $V_{outInitial}$, which is the starting condition for the current cycle.

[t_0 - t_1]: In this period, secondary winding stray capacitor resonates with the secondary inductance until primary MOSFET is switched on. As another point of view, the initial energy stored in C_s oscillates between C_s and $L_{secmag}+L_{seclak}$. Considering the effect of R_{sw} , the voltage over C_s can be obtained through solving (2) with the initial conditions in (3).

$$(L_{secmag} + L_{seclak}) \cdot C_s \cdot \frac{dU_{cs}^2(t-t_0)}{d^2(t-t_0)} + R_{sw} \cdot C_s \cdot \frac{dU_{cs}(t-t_0)}{d(t-t_0)} + U_{cs}(t-t_0) = 0 \quad (2)$$

$$\begin{cases} U_{cs}(t-t_0)|_{t=t_0} = V_{outInitial} + V_{d22} \\ \frac{dU_{cs}(t-t_0)}{d(t-t_0)}|_{t=t_0} = 0 \end{cases} \quad (3)$$

After obtaining $U_{cs}(t-t_0)$, the current through C_s can be solved through (4).

$$I_{cs}(t-t_0) = C_s \cdot \frac{dU_{cs}(t-t_0)}{d(t-t_0)} \quad (4)$$

In practice, the terminal condition in this stage for low output voltage operation is different from high voltage operation. Q_I is switched on when $U_{cs}(t-t_0)$ reaches the minimum value due to the primary controller for low output voltage. Therefore, half the resonance period can be used to calculate the time interval, represented in (5).

$$t_1 = t_0 + \pi \cdot \sqrt{L_{sec} \cdot C_s} \quad (5)$$

For high voltage operation, when $U_{cs}(t-t_0)$ reaches $-N \cdot (V_{in} + V_{bdQ1})$, the body diode of Q_I is forced to conduct first, afterwards, Q_I is switched on. In fact, compared with time interval [t_1 - t_3] (Q_I on time), the body diode only conducts for so short time that we can assume Q_I is switched on at exact t_1 to simplify the analysis. Hence, the time interval can be calculated through solving (6). The switchover point from low to high voltage operation is when (6) has solution and happens around when $V_{outInitial} + V_{d22}$ reaches $N \cdot (V_{in} + V_{bdQ1})$.

$$U_{cs}(t-t_0)|_{t=t_1} = -N \cdot (V_{in} + V_{bdQ1}) \quad (6)$$

With the solved time interval and (2), we can predict the essential behavior in this stage. However, it must be clarified that this stage does not exist for the first charging cycle due to the energy stored in C_s is 0 at the start.

[t_1 - t_2]: The analysis for this period needs to be divided into low and high output voltage as well. According to [9], in the low voltage operation, V_{in} starts to magnetize the primary inductance, meanwhile, U_{cs} is charged to $-V_{in} \cdot N$ through the resonance between the $L_{prileak}$ and the reflected capacitance of $C_s + C_{oss2}$ in the primary side. Due to D_{2I} , the actual behavior is much more complex, we assume C_{oss2} is in parallel with C_s in order to simplify the model. In this short period, we only consider the resonance and leave the calculation concerning magnetization to the whole [t_1 - t_3] period later. Therefore, $U_{cs}(t-t_1)$ can be obtained through solving (7) under the initial conditions in (8).

$$\begin{aligned} & L_{prileak} \cdot N^2 \cdot (C_s + C_{oss2}) \cdot \left(-\frac{dU_{cs}^2(t-t_1)}{d^2(t-t_1)} \right) + (R_{dsonQ1} + \\ & R_{P1} + \frac{R_{sw}}{N^2}) \cdot N^2 \cdot (C_s + C_{oss2}) \cdot \frac{dU_{cs}(t-t_1)}{d(t-t_1)} - \frac{U_{cs}(t-t_1)}{N} = V_{in} \quad (7) \\ & \begin{cases} U_{cs}(t-t_1)|_{t=t_1} = U_{cs}(t-t_0)|_{t=t_1=t_0+\pi\sqrt{L_{sec}\cdot C_s}} \\ \frac{dU_{cs}(t-t_1)}{d(t-t_1)}|_{t=t_1} = 0 \end{cases} \quad (8) \end{aligned}$$

After deriving $U_{cs}(t-t_1)$, the current through C_s can be solved through (9).

$$I_{cs}(t-t_1) = (C_s + C_{oss2}) \cdot \frac{dU_{cs}(t-t_1)}{d(t-t_1)} \quad (9)$$

For high voltage operation, at the end of period [t_0 - t_1], the body diode in Q_I starts to conduct in order to transfer the energy stored in flyback transformer to primary power source. Owing to the energy stored in L_{seclak} , the resonance between L_{seclak} and $(C_s + C_{oss2})$ takes place in [t_1 - t_2]. Therefore, the similar equation as (7) can be derived to acquire U_{cs} for high voltage operation. However, the initial conditions are different, which are represented in (10).

$$\begin{cases} U_{cs}(t-t_1)|_{t=t_1} = U_{cs}(t-t_0)|_{t=t_1} = -N \cdot (V_{in} + V_{bdQ1}) \\ \frac{dU_{cs}(t-t_1)}{d(t-t_1)}|_{t=t_1} = \frac{dU_{cs}(t-t_0)}{d(t-t_0)}|_{t=t_1} \end{cases} \quad (10)$$

[t_1 - t_3]: This stage is the on time for Q_I . The current through L_{secmag} can be obtained through solving (11).

$$(L_{primag} + L_{prileak}) \cdot \frac{dI_{primag}(t-t_1)}{d(t-t_1)} + (R_{dsonQ1} + R_{P1} + R_{pw}) \cdot I_{primag}(t-t_1) = V_{in} \quad (11)$$

The initial current for I_{primag} in the low voltage operation is 0, however, the current starts from a minus value for high output voltage, which is represented in (12).

$$I_{primag}(t-t_1)|_{t=t_1} = N \cdot I_{cs}(t-t_0)|_{t=t_1} \quad (12)$$

Because of the fixed peak current control, the time interval for this stage can be acquired by solving (13).

$$I_{primag}(t-t_1)|_{t=t_3} = I_{pripeak} \quad (13)$$

Combining the resonance in [t_1 - t_2] and the magnetizing equations for [t_1 - t_3], we can build the model to mimic the behavior in the whole [t_1 - t_3] period.

[t_3 - t_4]: At t_3 , Q_I is switched off. Due to the influence of C_s , D_{22} cannot conduct when Q_I is just turned off. The energy stored in flyback transformer needs to charge C_s until U_{cs} reaches $V_{outInitial} + V_{d22}$. The time interval [t_3 - t_4] is used to express this behavior. Replacing $L_{secmag} + L_{seclak}$ with L_{secmag} in (2), we can get the similar differential equation to calculate U_{cs} in this period and the initial conditions are given in (14).

$$\begin{cases} U_{cs}(t-t_3)|_{t=t_3} = -N \cdot V_{in} \\ \left. \frac{dU_{cs}(t-t_3)}{d(t-t_3)} \right|_{t=t_3} = \frac{I_{pripeak}}{N} \end{cases} \quad (14)$$

The time interval can be obtained through solving (15) with the terminal condition — U_{cs} reaches $V_{outInitial} + V_{d22}$.

$$U_{cs}(t-t_3)|_{t=t_4} = V_{outInitial} + V_{d22} \quad (15)$$

The final current through C_s in this period needs to be calculated as well, represented in (16), which is the actual initial freewheeling current for next time interval.

$$I_{fwInitial} = C_s \cdot \left. \frac{dU_{cs}(t-t_3)}{d(t-t_3)} \right|_{t=t_4} \quad (16)$$

This period can be neglected in the low voltage operation and tends to become more obvious with the increasing of the output voltage. In fact, when Q_1 is switched off, $L_{prileak}$ will resonate with C_{oss1} and C_{sr} to dissipate the stored energy. But this phenomenon only exists for a very short time even compared to the time interval $[t_3-t_4]$. Therefore, this resonance behavior has not been modelled. However, in the worst case, all the energy stored in $L_{prileak}$ is used to charge C_{oss1} , what we need to do is to check the maximum voltage over C_{oss1} will not exceed the voltage rating of Q_1 .

$[t_4-t_5]$: This stage is used to release the energy stored in transformer to the capacitive load. The output voltage V_{out} can be acquired by solving the differential equation in (17) with the initial conditions given in (18).

$$L_{secmag} \cdot C_L \cdot \frac{d^2V_{out}(t-t_4)}{d^2(t-t_4)} + R_{sw} \cdot C_L \cdot \frac{dV_{out}(t-t_4)}{d(t-t_4)} + V_{out}(t-t_4) + V_{d22} = 0 \quad (17)$$

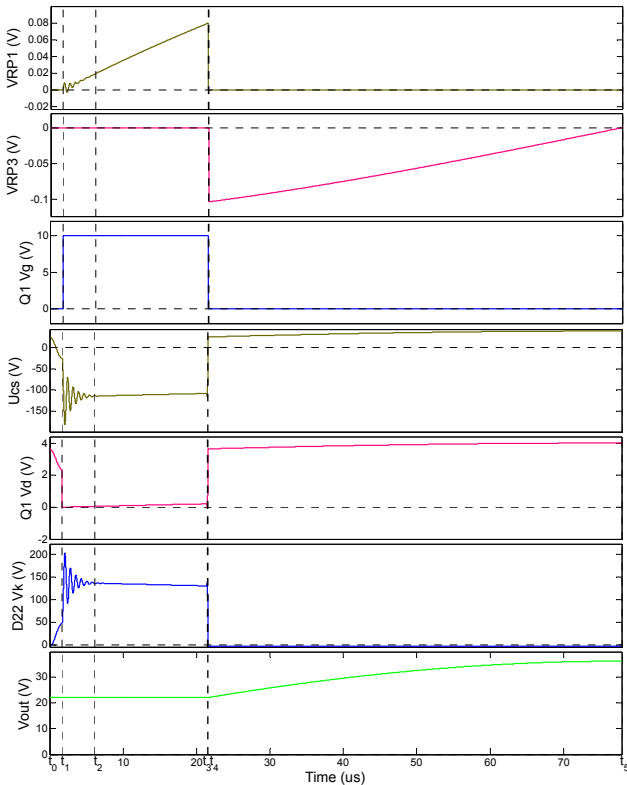


Fig. 5. Calculated critical waveforms of low output voltage operation in charging process.

$$\begin{cases} V_{out}(t-t_4)|_{t=t_4} = V_{outInitial} \\ \left. \frac{dV_{out}(t-t_4)}{d(t-t_4)} \right|_{t=t_4} = \frac{I_{fwInitial}}{C_L} \end{cases} \quad (18)$$

In addition, the freewheeling current is given by

$$I_{CL}(t-t_4) = C_L \cdot \frac{dV_{out}(t-t_4)}{d(t-t_4)} \quad (19)$$

By solving (20), we can get the freewheeling time and calculate the final output voltage for this charging cycle.

$$I_{CL}(t-t_4)|_{t=t_5} = 0 \quad (20)$$

Based on the analysis mentioned above, a Maple program has been completed to solve all the differential equations with initial conditions. The symbolic solutions have been exported to MATLAB for obtaining the critical waveforms of the converter. In order to validate the model, the calculated critical waveforms for low output voltage operation ($V_{outInitial}=22$ V) in the charging process are shown in Fig. 5 and compared with the corresponding experimental waveforms in Fig. 6 and 7. In addition, the calculated critical waveforms for high voltage operation ($V_{outInitial}=2000$ V), shown in Fig. 8, are compared with the experimental waveforms in Fig. 9 and 10 as well. The good match can verify the analysis as well as the mathematical model.

B. Discharging process

Similarly, the analyzed models based on different time intervals and differential equations have been built for one single cycle in discharging process. And the initial output voltage is represented as $V_{outInitial}$ as well.

$[t_0-t_1]$: Q_2 is switched on in this period. The energy in the capacitive load starts to magnetize flyback transformer until secondary current reaches the preset peak value. The output voltage can be obtained through solving similar differential equation represented in (17) with initial conditions given in (21).

$$\begin{cases} V_{out}(t-t_0)|_{t=t_0} = V_{outInitial} \\ \left. \frac{dV_{out}(t-t_0)}{d(t-t_0)} \right|_{t=t_0} = 0 \end{cases} \quad (21)$$

Through similar equation as (4), we can calculate the secondary current $I_{sec}(t-t_0)$. Then the time interval can be

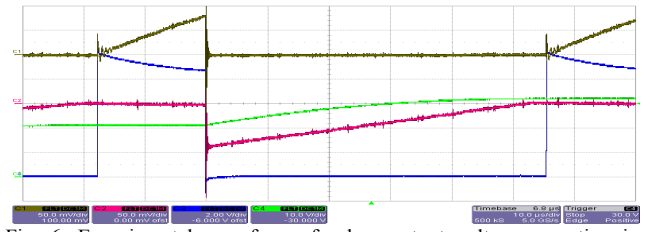


Fig. 6. Experimental waveforms for low output voltage operation in charging process (CH1- V_{RP1} CH2- V_{RP3} CH3- Q_1 V_g CH4- V_{out}).

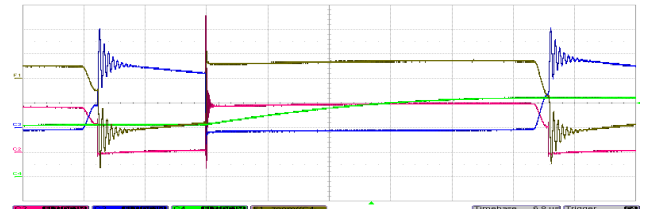


Fig. 7. Experimental waveforms for low output voltage operation in charging process (F1- U_{cs} CH2- Q_1 V_d CH3-D₂₂ V_g CH4- V_{out}).

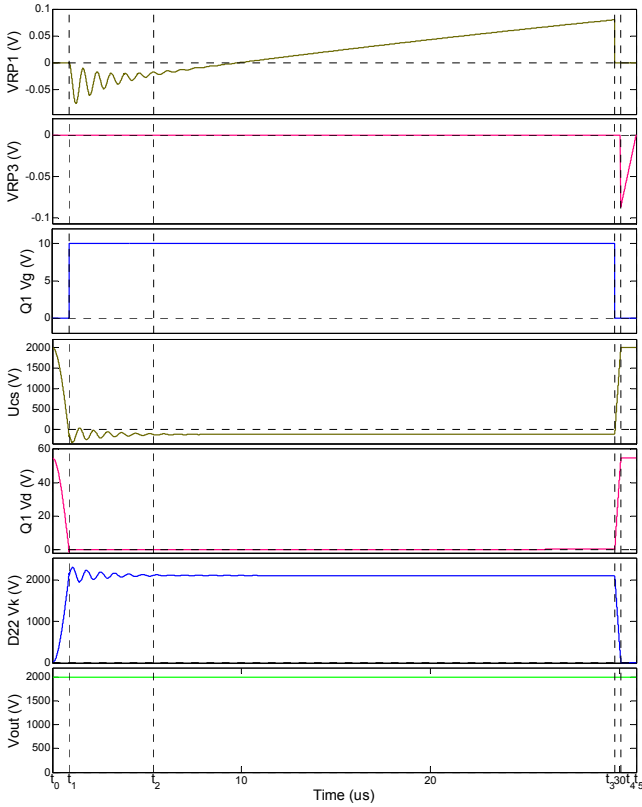


Fig. 8. Calculated critical waveforms of high output voltage operation in charging process.

acquired with analogical equation represented in (13), with which the final output voltage $V_{outFinal}$ can be determined.

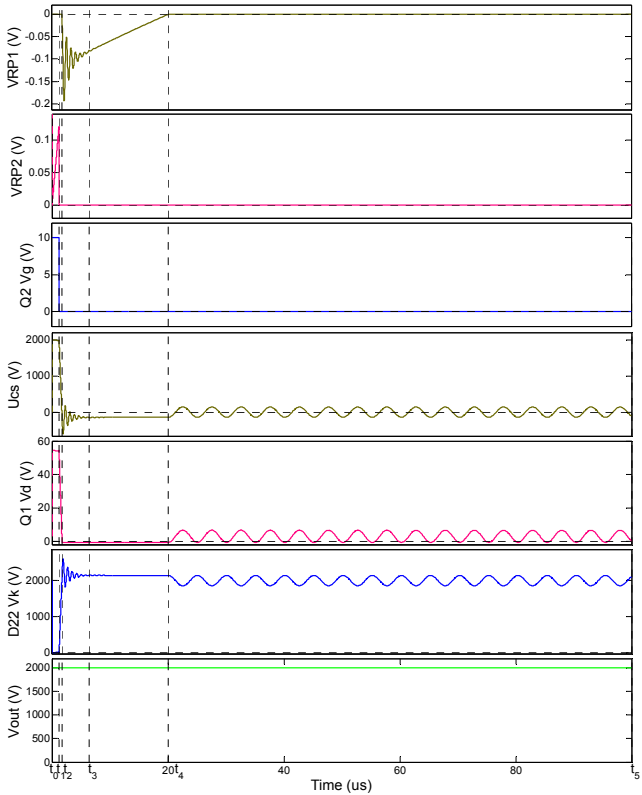


Fig. 11. Calculated critical waveforms of high output voltage operation in charging process.

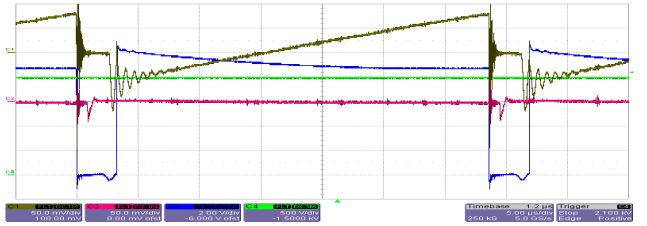


Fig. 9. Experimental waveforms for high output voltage operation in charging process (CH1- V_{RP1} CH2- V_{RP3} CH3- $Q_1 V_g$ CH4- V_{out}).

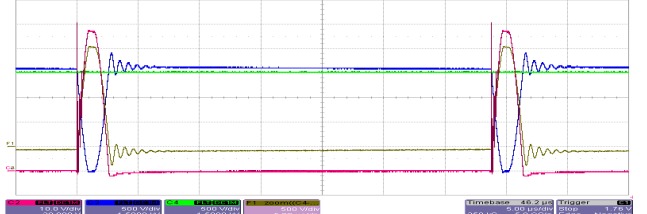


Fig. 10. Experimental waveforms for high output voltage operation in charging process (F1- U_{cs} CH2- $Q_1 V_d$ CH3- $D_{22} V_k$ CH4- V_{out}).

$[t_1-t_2]$: $L_{secmag}+L_{seclak}$ resonate with C_s+C_{oss2} in this period until U_{cs} reaches the $-N \cdot (V_{in}+V_{bdQ1})$. The behaviors are similar as $[t_0-t_1]$ in charging process. Hence, the similar equation as (2) can be derived to calculate U_{cs} with the initial conditions represented in (22).

$$\begin{cases} U_{cs}(t-t_1)|_{t=t_1} = V_{outFinal} \\ \frac{dU_{cs}(t-t_1)}{d(t-t_1)}|_{t=t_1} = -\frac{I_{seclak}}{C_s+C_{oss2}} \end{cases} \quad (22)$$

$[t_2-t_3]$: L_{seclak} resonate with C_s+C_{oss2} in this period until all the energy stored in L_{seclak} dissipates. This stage has the similar behavior as $[t_1-t_2]$ in charging process. The analysis for high voltage operation in charging cycle is valid here and we can derive (23) to calculate U_{cs} in this period with the initial conditions given in (24).

$$L_{seclak} \cdot (C_s + C_{oss2}) \cdot \frac{dU_{cs}^2(t-t_2)}{d^2(t-t_2)} + R_{sw} \cdot (C_s + C_{oss2}) \cdot \frac{dU_{cs}(t-t_2)}{d(t-t_2)} + U_{cs}(t-t_2) = 0 \quad (23)$$

$$\begin{cases} U_{cs}(t-t_2)|_{t=t_2} = U_{cs}(t-t_1)|_{t=t_2} \\ \frac{dU_{cs}(t-t_2)}{d(t-t_2)}|_{t=t_2} = \frac{dU_{cs}(t-t_1)}{d(t-t_1)}|_{t=t_2} \end{cases} \quad (24)$$

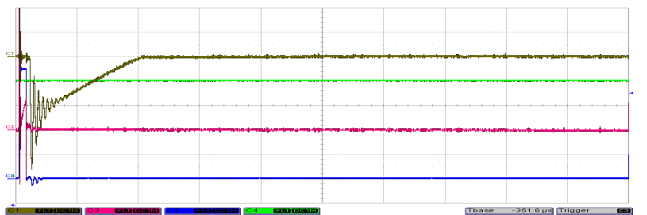


Fig. 12. Experimental waveforms for high output voltage operation in discharging process (CH1- V_{RP1} CH2- V_{RP2} CH3- $Q_2 V_g$ CH4- V_{out}).

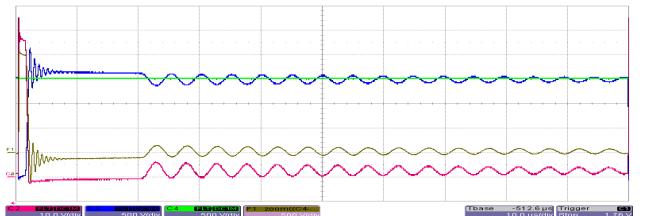


Fig. 13. Experimental waveforms for high output voltage operation in discharging process (F1- U_{cs} CH2- $Q_1 V_d$ CH3- $D_{22} V_k$ CH4- V_{out}).

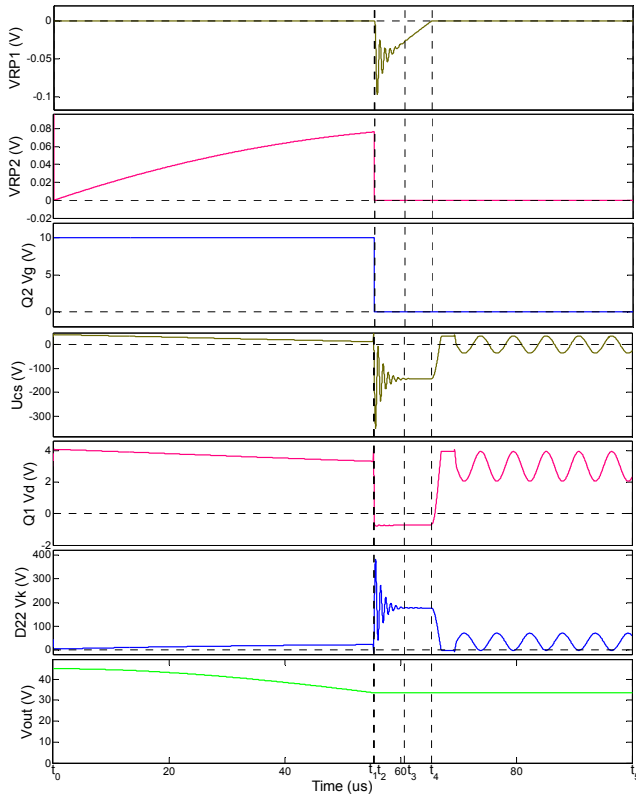


Fig. 14. Calculated critical waveforms of low output voltage operation in charging process.

[t_2-t_4]: In this whole period, the energy stored in flyback transformer is transferred to primary power source through body diode of Q_1 . Similar behaviors occur in stage [t_1-t_3] of charging cycle. The derived equation (11) can be applied here with minor changes.

[t_4-t_5]: This is the discontinuous conduction mode period in discharging cycle. C_s as well as C_{oss2} will resonate with $L_{secmag}+L_{seclak}$ until the start of next cycle. In the high voltage operation, the voltage across C_s can be acquired by solving differential equation (25) with initial conditions in (26).

$$(L_{secmag} + L_{seclak}) \cdot (C_s + C_{oss2}) \cdot \frac{d^2 U_{cs}(t-t_4)}{d^2(t-t_4)} + R_{sw} \cdot (C_s + C_{oss2}) \cdot \frac{dU_{cs}(t-t_4)}{d(t-t_4)} + U_{cs}(t-t_4) = 0 \quad (25)$$

$$\begin{cases} U_{cs}(t-t_4)|_{t=t_4} = U_{cs}(t-t_2)|_{t=t_4} \\ \frac{dU_{cs}(t-t_4)}{d(t-t_4)}|_{t=t_4} = 0 \end{cases} \quad (26)$$

For low output voltage operation, the behavior is more complex because the maximum resonance voltage over C_s is clamped to $V_{outFinal}+V_{d22}$. Similar with charging process, the switchover point occurs when $V_{outFinal}+V_{d22}$ reaches $N \cdot (V_{in}+V_{bdQ1})$.

Another Maple program has been finished to solve all the differential equations for one discharging cycle and the solutions have been transferred to MATLAB in order to draw the critical waveforms as well. The calculated critical waveforms when $V_{outInitial}=46$ V and $V_{outInitial}=2000$ V are shown in Fig. 11 and 14, respectively. The corresponding experimental waveforms, shown in Fig. 12, 13, 15 and 16, indicate the good match with calculated ones.

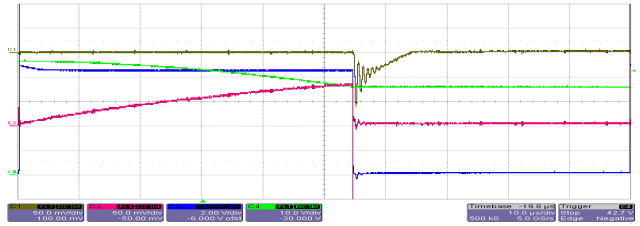


Fig. 15. Experimental waveforms for low output voltage operation in discharging process (CH1- V_{RP1} CH2- V_{RP2} CH3- Q_2 V_g CH4- V_{out}).

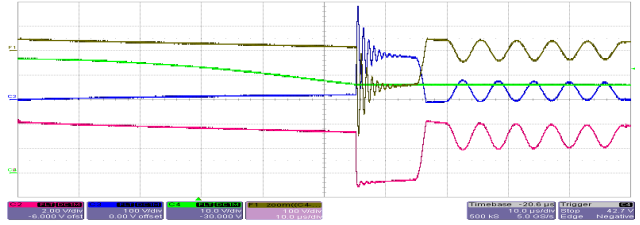


Fig. 16. Experimental waveforms for low output voltage operation in discharging process (F1- U_{cs} CH2- Q_1 V_d CH3- D_{22} V_k CH4- V_{out}).

V. CONCLUSION

As a new type of smart material, DEAP material as well as DEAP based tubular actuator are introduced. The essential characteristics have been investigated. A low input voltage bidirectional flyback based converter for driving DEAP actuator has been implemented. The detailed behavior analysis considering the influence of the critical parasitic elements has been performed for the operation of the converter, including the analysis for both charging and discharging processes in the low and high output voltage conditions. The experimental results can provide a good proof for the analysis.

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