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# An array-based study of increased system lifetime probability

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**Abstract:** Society's increased dependence on electronic systems calls for highly reliable power supplies comprised of multiple converters working in parallel. This paper describes a redundancy control scheme, based on the array technology that increases the overall reliability quite considerably and thereby ensures a stable supply voltage.

**Index Terms:** Array-based logic, control, redundancy, reliability, power system, Markov modeling.

## 1. Introduction

With the ever-increasing dependence on reliable electronic systems, the area of highly reliable power systems is rapidly expanding. When considering highly reliable fault tolerant power systems the word 'redundancy' comes to mind. Indeed, a true fault tolerant power system is comprised of several converters working in parallel. This paper describes part of an ongoing project of building a prototype of a fault tolerant power system with  $N + 2$  redundancy. The paper gives a short description of the mathematics used, the power system in question and the choice of control scheme. Since each of these short descriptions can form the basis for an entire paper, the focal point in this presentation is the redundancy control within the overall power system.

## 2. Array-based logic

Originated at the Department of Electric Power Engineering, the Technical University of Denmark in 1978 with the paper "Group Representations of Finite Polyvalent Logic – a Case Study Using APL Notation" by associate professor Ole I. Franksen [5], the array-based logic has evolved into an effective tool when dealing with combinatorial and/or configuration applications. The foundation of the technology is a geometrical representation of logic in terms of nested arrays. In other words, the array-based concept deals with data objects regarded as arrays [8]. Consequently, all calculations are performed on arrays which implies that systems comprised of large amounts of data often can be systematically simplified by the use of array theoretical operations. In general, the array-based logic can be considered to consist of the following three steps:

**Step one** is the establishment of a discrete  $n$ -dimensional configuration space using the Cartesian product, which ensures completeness. This is accomplished by the use of the tensor product OUTER and [9], which combines the system propositions and unites them to form one conjunctive proposition.

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**Step two** is the inference by colligation, which is the operation of establishing the interconnections of the system. In other words this step finds the solutions that comply with the system constraints.

**Step three** is the determination of states by elimination of variables through an or-reduction.

Having introduced the concept of constraints, it is obvious that prior to completion of the above-mentioned steps, the constraints of the physical system must be translated into array theoretical terms. This is achieved through the use of propositional logic that transforms the system constraints into logic operations suitable for array theoretical implementation. An example of this translation and implementation is shown in section 5.

Summarizing the above description the basic idea of array-based logic can be expressed, according to Franksen, as:

*Array-based logic explores the consequences of considering truth-values as physical measurements. The aim is to formalize logic in accordance with the theoretical structure of discrete systems and express this formalization algebraically in array-theoretic terms.*

## 3. Redundancy as reliability enhancement

Increasing the reliability of a power system can be achieved in a number of ways, some more successful than others. A simple way of increasing the overall system reliability is using high quality components with low failure rates. Although, the overall circuit complexity remains the same the cost for high quality components is considerably higher than that of an implementation using commercial components. Also, the reliability gain using these high quality components is only moderate compared to other reliability enhancement techniques.

For the reasons mentioned above the far most common way of increasing the reliability is the use of redundancy. Once the choice of redundancy has been made the level of redundancy needed must be identified. Ranging from the component level all the way to the system level, the level of redundancy used in the power system in this presentation is what can be characterized as block level redundancy.

As any system a redundant power system has both advantages and drawbacks. Among the advantages is the possibility of a dramatic increase in reliability at the expense of an increase in system dependent parameters such as cost, mass, volume and circuit complexity. Although the increase in reliability can be quite high, added cost, mass, volume, and complexity are drawbacks that must be considered when deciding which approach to take during the design phase of the power system. However, the drawbacks tends to be less important nowadays, since system downtime in case of power failure often results in greater losses in sales, customer services etc.

When implementing system level redundancy in power systems, each converter board within the overall power system must be equipped with a front switch that allows for

controlled shutdown of faulty converter boards, since this is the only way the power system integrity can be maintained. In other words the power system must exhibit failure free operation at the input as well as at the output. Due to this fact, most approaches in designing high reliably power systems originates from the ability of the overall power system to shutdown faulty units whether these consists of converter boards, power distribution units etc. Focusing on the reliability of redundant systems it is noteworthy that making a single path system redundant generally increases the overall reliability with a factor of the reciprocal of the initial failure rate for the single path system, assuming the exponential distribution is valid and that the working conditions in both cases remain unchanged.

Now, suppose the redundant power system had the ability to reconfigure itself during operation. Such a system would further increase the reliability of the overall power system and at the same time reduce the maintenance requirements, since faulty units could be 'replaced' automatically. Due to the dynamic process of continuous measurement of the system integrity and configuration at any given time the simple exponential distribution for reliability calculations does not justify the true reliability potential in a reconfigurable system. To obtain a more truthful measure for the system reliability, one has to adopt the use of Markov modeling techniques, which is a commonly used modeling technique dealing with dynamic systems. Although Markov modeling often includes random noise errors within the sensor feedback system, it is for simplicity assumed that such errors do not occur within the power system at hand. The justification for making this simplification is that the focal point in this presentation is the redundancy control scheme. Furthermore, since the sensor feedback system is discrete with logic truth-values, the noise margin is quite large and random noise errors would be extremely rare in any case. The detailed description of Markov modeling uses can be found in the literature [7] and will not be dealt with in this presentation.

The implementation and control of reconfigurable systems are the subjects of section 4 and section 5, respectively.

#### 4. The power system

It has been chosen to investigate a power system comprised of 5 identical converter boards connected in parallel. As indicated in the introduction this is 2 converter boards more than needed. Thus, the system is  $N + 2$  redundant. On a board level each converter is designed to shutdown in case of a single point failure whereas the overall power system can tolerate 2 failures and still provide the needed power. From a traditional power system point of view two failures reduces the overall power system from a system comprised of 5 parallel-connected converter boards into a system comprised of the 3 converter boards needed to supply the required power to the load.

The proposed power system approaches the parallel connection of the individual converter boards in a way that differs significantly from what have just been described, by splitting the individual converter boards into 5 main blocks. Aligning these main blocks as shown in Figure 1 (ignoring the block 'PWM controller'), each block connects to the previous block on the same converter boards as well as to the previous blocks on the parallel-connected converter boards. This arrangement of multiple inter-connection of individual

blocks allows for intelligently control of combining blocks for maximum number of working converter boards at any given time. It should be noted that it is not an allowable state to have a block deliver power to more than one subsequent block. Thus, the first system constraint is the limitation of blocks being connected to one and only one subsequent block.

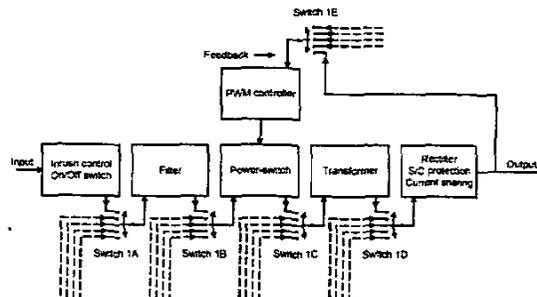


Figure 1 : Block diagram for converter 1

The connecting devices are chosen to be electronically controlled switches but could in theory also be mechanically operated relays. The reason for choosing electronically controlled switches (hereafter referred to as switches) is the fact that activation of the individual interconnecting devices would occur during power system operation, which for long switching periods would require substantial amounts of capacitors at the output of the power system in order to comply with ripple voltage specification. Thus, the timing of the switching is of importance although not critical. A transition time of 0.1 ms is estimated to be reasonable. Compared to the 140 kHz, which has been chosen as the switching frequency for the individual converter boards, it is apparent that the transition times of the interconnecting switches are far from critical.

Even though the interconnecting switches are operated rarely, due to the rather low failure rate of the electronic components used and the transition time from one state to another is fairly quick, the price paid for using extra switches as connecting devices between the different blocks within the overall power system is an increased failure rate for the individual converter boards and an increase in total conduction losses. Furthermore, the overall cost and complexity of the power system is increased due to the use of extra switches. However, if these drawbacks at the board level imply a better probability of continuous operation at a system level, the added cost, complexity and losses might be negligible compared to the gain in reliability. Also, it should be noted that obtaining a similar reliability for a power system comprised of individual converter boards without the interconnecting capability would require more converter boards, which adds to system parameters such as volume and mass.

As mentioned in section 3 the statistical description of the reliability of the power system should be carried out using the Markov modeling approach. However, from a system point of view it should be obvious that the process of combining two defective converter boards to form one working converter board increases the overall system performance concerning both reliability and efficiency. In Figure 2 it can be seen that by combining two converter boards, which have failed in different locations on the board level, an alternative

path for the power throughput can be established. This lowers the stress on the original 3 converter boards, since the load current now is shared among 4 converter boards. As a consequence the power system operating point on the efficiency curve tends to move towards the optimum operating point as shown in Figure 3. Also and most importantly, the overall power system reliability increases as a result of the newly configured power system comprised of 3 + 1 working converter boards.

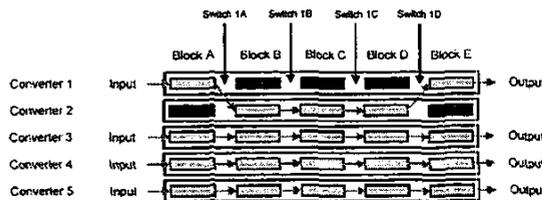


Figure 2 : The 5 parallel connected converter boards

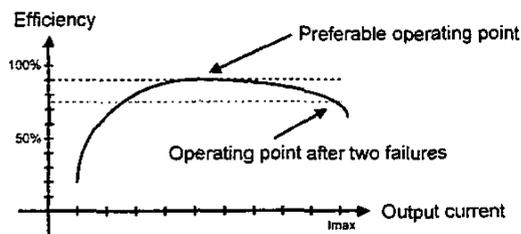


Figure 3 : Example of operating point movement

Referring to Figure 1 and Figure 2 this paragraph describes the abbreviations used to identify the individual blocks and switches within the power system. Starting with the blocks it can be seen from Figure 2 that these can be addressed using the converter number as a row identification and the block letter as column identification. Thus, the first faulty location in the power system shown in Figure 2 can be identified as: **Converter 1, Block B.**

Identification of the interconnection switches is accomplished through the adoption of the following notation: **SXYZ**

Where S is the notation used in the software to identify a switch. X represents the converter number. Y represents the block prior to the switch in question and Z represents the switch position. Hence, the switch between block 1A and block 1B set in position 1 gets the identification S1A1.

In order to provide feedback to the redundancy control system, each block in the overall power system can take on two different logic values - logic 1 for a working block and logic 0 for a faulty block. Since a faulty block is switched off and the redundancy control system continues to check the status of the overall power system, the logic state of any faulty block is latched. This ensures that the redundancy control system always gets the correct logic values from each block, even though the block in question has failed. Having retrieved all truth-values from the blocks within the power system an array containing the retrieved truth-values is generated. As will become apparent in section 5 this array forms the basis for the calculation process as well as for the representation of the results.

Turning the attention towards the operation of the power system the following description represents the actions taken by the redundancy control in case of fault occurrence. Assuming a well functioning structure as the starting point, the power system consists of 5 inputs and 5 outputs. After a failure within the power system, the redundancy control shuts down the blocks associated with the faulty block and leaves the power system comprised of 4 inputs and 4 outputs. Except for the faulty block the rest of the inactive blocks now serve as cold spares for the power system in case of further failures. Now suppose a second fault occurs, for instance due to the increased stress on the remaining active converter boards. Since two faults have occurred it might now be possible to establish an alternative path through the power system and thus increase the number of active converter boards from 3 to 4. The only constraint that makes the establishment of an alternative path impossible is in case the two faults have occurred within the same block. In this case the power system would consist of only 3 converter boards, which is the minimum number possible in order to sustain power delivery to the load.

Depending on the failure rate of the individual blocks it would be a rare situation that two successive faults occur in the same block, hence the probability of successful system reconfiguration is quite high. This indicates that the overall system reliability has increased compared to the situation with 5 separate converter boards.

Based on this short description of the power system and its operation, the mathematical task of the redundancy control can be thought of as a method of finding alternative paths through the power system in case of fault occurrence.

## 5. Array based control scheme

As described in section 2 array-based logic considers truth-values as physical measurements. The truth-values in the application at hand are the discrete values obtained from each block in the power system, upon which the alternative path from input to output is calculated. With reference to Figure 2 it can be seen that the discrete values obtained from the individual blocks can be considered an array of 5 rows and 5 columns. This array is a measurement of the condition of the overall power system and can therefore be used to identify problems within the system. Based on this identification of problems within the power system, the array-based analysis suggests possible alternative paths through the power system. It should be noted that similar results could have been obtained by using standard digital logic. The reason for not implementing the redundancy control using this type of logic is due to the powerful array concept and operations in array theory, which makes it easy to expand the redundancy control scheme to include an arbitrary number of converter boards and switches. Thus, a formal description of a redundant power system comprised of any number of parallel-connected converter boards is straight forward, since the added system constraints are almost replicates of existing board level constraints. Similar implementation using standard digital logic would require considerable recalculations of the power system's interconnections.

With reference to Figure 1 and Figure 2 it can be seen that the number of parameters needed to describe the power system in question is relatively large. For this reason most system constraints have been omitted in this presentation, although an example will be given on the following pages.

Having introduced the fundamentals of the power system, the array-based analysis can be carried out. The mathematical tool used in this project is based on the array theory developed by Dr. Trenchard More in the Seventies and later (early Eighties) implemented in the array-based software 'Queens Nested Interactive Language (Q'Nial)' by Professor Michael Jenkins.

Solving the problem at hand by establishing a generalized configuration space by the use of the Cartesian product of all system parameters would require a tremendous amount of computer memory, since the number of possible combinations exceeds  $10^{30}$ . A different approach has therefore been pursued. Using the allowable positions for each switch, an algorithm has been developed that in a successive way finds a feasible solution within a given amount of time.

Looking at the power system and the tasks of the redundancy control from a topological point of view it should be noted that the system's topological array has characteristics similar to that of the incidence matrix describing electric networks within the field of graph theory. The reason the topological array only has similar and not identical characteristics to the incidence matrix are due to the unidirectional flow of power through the system. The classical approach in electric network theory using arrays is the bi-directional power flow that uses the numbers 0, 1 and -1 to identify the flow direction. Since the power system at hand only allows power to flow in one direction the closest match to the incidence matrix is the use of unidirectional circuit elements such as semi-conductor devices within the electric network itself. From a mathematical point of view this adds considerably to the complexity of the system when performing reliability calculations, since the system now includes multiple failure modes for each block. Also, it should be noted that the analysis assumes a constraint between the blocks 'PWM controller' and 'Power-switch'. This constraint ensures a correct connection between the driving 'PWM controller' and the semi-conductor device making up the switching element of the converter.

As mentioned above the starting point in the development of the software was a series of system constraints that would limit the number of allowable switch combinations. The implementation example used in this presentation is the constraint saying that a switch set in position 1 cannot be set in any other position. Describing this constraint with a 'logic-level' explanation makes the data flow diagram more reader friendly.

If  $s1a1 = 1$  Then  $s1a2 \& s1a3 \& s1a4 \& s1a5 = 0$

This description must now be converted into Nial terms, which results in the following lines of source code:

```
a2:= OUTER <=s1a1 (not s1a2);
a3:= OUTER <=s1a1 (not s1a3);
a4:= OUTER <=s1a1 (not s1a4);
a5:= OUTER <=s1a1 (not s1a5);
```

Noting the replication of parameter  $s1a1$ , the proposition must be united through the operation of colligation:

```
(0 2 4 6) (1) (3) (5) (7) fuse (OUTER and a2 a3 a4 a5)
```

Following the above procedure, the remaining system constraints can be added to the source code. After a few transformations a list of allowable switch positions based on the system constraints can be obtained. Keeping in mind that the initial  $2^5$  combinatorial switch positions for any given switch now has been reduced to 6 allowable switch positions that comply with the system constraints:

```
+-----+-----+-----+-----+-----+-----+
|00000|00001|00010|00100|01000|10000| (1)
+-----+-----+-----+-----+-----+-----+
```

The first entry from the left in (1) is the NULL solution where the block subsequent to the switch in question is disconnected from all blocks within the power system. The entry to the right in (1) is the notation used for switch position 1. The second entry from the right is the notation used for switch position 2 and so forth. As an example the entry to the right in (1) indicates that the left-hand side of the switch in question is connected to **Converter 1** regardless to which converter board the right-hand side of switch is connected. Thus, a '0' in any entry in (1) indicates a disconnection whereas a '1' indicates a connection between two blocks.

Since the software program decides which blocks to interconnect at all times the power system would under normal circumstances interconnect the individual block in a way that enables power throughput within the physical boundaries of each converter board as shown in Figure 2. In case of multiple failures the algorithm would find a way through the system that would ensure a maximum number of working converter boards. In other words, allowing the algorithm to decide which blocks to interconnect, the overall power system is no longer comprised of 5 individual converter boards with interconnecting switches, but 5 times 5 blocks that can be interconnected in an enormous number ways. This gives rise to the previously mentioned increase in reliability. As a consequence a high reliable power system can be build with lower volume and mass than conventional power systems, but at the cost of increased circuit complexity and considerably higher cost prize.

As an example of the capabilities of the algorithm, let the power system suffer from 8 faults located in different places throughout the power system. A condition from which a power system comprised of standard parallel-connected converter boards could not recover. As seen from Figure 4 each row in the power system has suffered at least one failure.

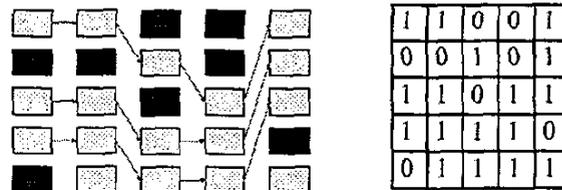


Figure 4 : 8 faults distributed among all 5 converters

The array to the right in Figure 4 is the system truth-values as they are entered into the system array for calculation purposes. The software program now performs the following tasks in order to establish a path between the first two blocks.

The starting point is the answer to the following question:

Does the combination of 1 1 occur in the first two columns in the matrix shown to the right in Figure 4? In Nial terms this can be expressed in a very compact form:

```
Q:= ((0 pick (cols AA) EACHLEFT = ll) link o)
```

```
lolloo
```

The result is shown as truth-values. It should be noted that due to the number of allowable switch positions a falsehood has been attached to the end of the result.

Assigning the correct switch positions to the entries that returned truth is completed through the operation 'sublist'.

```
Y:= Q sublist (reverse Res_1)
```

```
+-----+-----+-----+
| 10000| 00100| 00010|
+-----+-----+-----+
```

 (2)

In order to insert the correct switch positions into the result array, the positions that returned truth must be identified in Index origin 0.

```
Index:= EACH first (Y EACHLEFT
sublist tell (first shape AA))
```

```
0 2 3
```

Finally, the assigned switch positions are inserted into the result array by using the operation 'placeall'.

```
Y (cart Index 0) placeall AA
```

Following a similar procedure the rest of the truth-values in the array shown to the right in Figure 4 are replaced by feasible switch positions. The resulting array for the case of 8 faults distributed among all 5 converters is shown in Figure 5.

10000	00000	00000	00100
00000	10000	00000	00010
00100	00000	01000	00001
00010	00100	00010	00000
00000	00010	00001	00000

Figure 5 : Result array

Comparing the array shown to the right in Figure 4 with the result array shown in Figure 5 it is obvious that the two arrays are linked through a transformation array. Looking at the axes of the two arrays it can be seen that the

transformation array is the previously mentioned incidence matrix for electric networks.

## 6. Concluding remarks

An alternative approach in the design of reliable power systems has been presented. Based on statistical calculations using among others the exponential distribution it has been shown that redundancy is the tool to implement when considering high reliable power systems.

Also, a control scheme for the redundancy control of the power system has been presented. Using the array-based logic a well functioning system capable of establishing the maximum number of working converter boards possible has been implemented.

## 7. Acknowledgment

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