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ACQUISITION PROCESSES IN ADAPTIVE ANTENNA SYSTEMS

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Introduction

Signal processing schemes for adaptive receiving antenna arrays have been analyzed in computer simulation experiments. A major part of the experiments consists of detailed studies of the acquisition process, i.e. the automatic or adaptive adjustment of the system in response to an input signal structure comprising a wanted signal, internal Gaussian noise and interference from distant sources.

The acquisition process of the adaptive antenna system results in capturing the wanted signal and more or less efficient attenuation or rejection of unwanted noise and interference. The simulated systems all belong to the general class of receiving arrays with automatic or adaptive signal processors using predetection combining.

The purpose of this paper is to demonstrate that computer simulation techniques can be readily used in comparative studies of various types of arrays and signal processors.

Principles of adaptive antenna systems

Fig. 1 shows a simplified functional diagram of an adaptive antenna system. The diagram is so comprehensive that it can be applied to the various types of systems that have been investigated.

In an adaptive antenna system, it is necessary to provide some cues by which the system will be able to perform the signal extracting process where the wanted signal is recognized and distinguished from unwanted noise and interference. Such cues may be imbedded in the system, and additional information regarding direction or content of the wanted signal may be required.

In Fig. 1, fundamental methods of introducing a cue or a reference are illustrated by means of a 4-position switch, the reference signal selector, which may not be present in a final system implementation. However, connections corresponding to the switch positions are actually present in the following three types of systems that have been investigated in the computer simulations.

1) Adaptive multichannel processor using regular iteration algorithm for optimization of signal-to-noise ratio through automatic adjustment of the electronic gain in each channel. Adaptive processors of this type were originally developed for sonar and seismic systems and are now being considered with growing interest for use in radio communication systems [1], [2], [3], [4].

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2) **Conjugate-correlator system.** The adjustable complex gain factor in each channel is formed by cross correlating the complex conjugated input signal and the reference signal [4].

3) **Phase-locked system.** Automatic phase adjustment in each channel is performed in a multichannel superheterodyne receiver with phase-lock loop (PLL) circuits. Several kinds of filtering, limiting and adaptive loop parameter adjustment can be incorporated in the system [5].

**Acquisition processes**

The simulation experiments cover various cases of linear, circular and rectangular arrays. The results are collected in computer generated plots comprising signal curves, learning curves, error curves, curves for signal-to-noise ratio and sequences of radiation patterns evolved during the acquisition process.

Examples of results from computer runs with the adaptive multichannel processor and the phase-locked system are illustrated in Figs. 2-3. Specifications covering these examples are given in table I.

The learning curves in Fig. 2 demonstrate that fast acquisition can be achieved in the adaptive multichannel processor by choosing proper values of a feedback factor C. Furthermore, the curves illustrate how an increase in the rate of acquisition involves degradation of the noise and stability properties of the system.

The more elaborate plot data in Fig. 3 from the phase-lock system show how the phase of the voltage controlled oscillators (VCO) in each channel acquires a final value within an error range determined by the level of noise and interference. Development of the radiation pattern during acquisition is clearly influenced not only by the wanted signal (S) but also by interference (I) and noise.

In the investigated systems, the best acquisition performance is obtained in the cases where the reference signal is provided from a local noise-free generator.
### Table I

#### Specifications for computer simulation experiments

<table>
<thead>
<tr>
<th>System:</th>
<th>Adaptive signal processor (Fig. 2)</th>
<th>Phased locked system (Fig. 3)</th>
</tr>
</thead>
<tbody>
<tr>
<td>Reference signal:</td>
<td>Local generator</td>
<td>Local generator</td>
</tr>
<tr>
<td>Selector position (Fig. 1):</td>
<td>A</td>
<td>A</td>
</tr>
<tr>
<td>Array:</td>
<td>Circular array, N=7 isotropic elements, radius R=2λ</td>
<td></td>
</tr>
<tr>
<td>Signal level:</td>
<td>10 dB</td>
<td>-6 dB</td>
</tr>
<tr>
<td>CW-interference level:</td>
<td>-</td>
<td>-6 dB</td>
</tr>
<tr>
<td>Gaussian noise level:</td>
<td>Wideband, 0 dB</td>
<td>Narrow band, 0 dB</td>
</tr>
<tr>
<td>Computer:</td>
<td>IBM 370/165 at NEUCC, Tech. Univ. of Denmark, Lyngby</td>
<td></td>
</tr>
<tr>
<td>Program:</td>
<td>&quot;ADA 7&quot; (PL/I)</td>
<td>PLL (FORTRAN)</td>
</tr>
<tr>
<td>Signal representation:</td>
<td>Real</td>
<td>Complex</td>
</tr>
<tr>
<td>Processing time (CPU):</td>
<td>449 sec (13 runs)</td>
<td>40 sec (1 run)</td>
</tr>
</tbody>
</table>
References


