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How the relative permittivity of solar cell materials influences solar cell performance

Andrea Crovetto\textsuperscript{a,}\textsuperscript{*}, Mathias K. Huss-Hansen\textsuperscript{a}, Ole Hansen\textsuperscript{a,b}

\textsuperscript{a}DTU Nanotech, Technical University of Denmark, DK-2800 Kgs. Lyngby, Denmark
\textsuperscript{b}V-SUSTAIN, Villum Center for the Science of Sustainable Fuels and Chemicals, Technical University of Denmark, DK-2800 Kgs. Lyngby, Denmark

Abstract

The relative permittivity of the materials constituting heterojunction solar cells is usually not considered as a design parameter when searching for novel combinations of heterojunction materials. In this work, we investigate whether such an approach is valid. Specifically, we show the effect of the materials permittivity on the physics and performance of the solar cell by means of numerical simulation supported by analytical relations. We demonstrate that, depending on the specific solar cell configuration and materials properties, there are scenarios where the relative permittivity has a major influence on the achievable conversion efficiency, and scenarios where its influence can be safely ignored. In particular, we argue that high-permittivity materials should always be the preferred choice as heterojunction partners of the absorber layer when prototyping new materials combinations. When the heterojunction partner has a high permittivity, solar cells are consistently more robust against several non-idealities that are especially likely to occur in early-stage development, when the device is not yet optimized.

Keywords: permittivity, dielectric constant, heterojunction solar cell, device physics, CIGS

1. Introduction

Within the last few years, impressive power conversion efficiencies have been demonstrated by various heterojunction solar cell technologies. Si-based heterojunction intrinsic thin layer (HIT), CdTe, Cu(In,Ga)Se\textsubscript{2} (CIGS), and metal halide perovskite solar cells have all reached efficiencies above 20\% [1] and can be manufactured at a relatively low cost. The earth-abundant and non-toxic absorber Cu\textsubscript{2}ZnSn(S,Se)\textsubscript{4} (CZTS) has also been investigated extensively and has reached a promising 12.6\% record efficiency [1].

Assuming a p-type absorber material, heterojunction solar cells typically feature a moderately doped thin layer (n-type), often called "buffer layer" or "intrinsic layer", as the immediate heterojunction partner of the absorber layer. This is followed by a layer with heavier doping (n\textsuperscript{+}-type), often called "window layer" or "electron transport layer". Such a solar cell architecture is shown in Fig. 1. Especially for CIGS and CZTS solar cells, there is an ongoing effort to replace the standard CdS buffer layer with another material [2]. The reasons are the toxicity of Cd and relatively low band gap of CdS (2.4 eV), which means that an appreciable fraction of light is absorbed in this layer, where carrier collection efficiency is low.

Selection of potential candidates as buffer layer materials is typically based on criteria such as 1) wide band gap, 2) natural occurrence of doping of opposite type as the absorber layer, 3) favorable conduction band offset with the absorber layer, 4) lattice matching with the absorber layer, 5) good coverage of the absorber layer, 6) absence of detrimental chemical interdiffusion with the absorber layer. While all the above reasons are valid, efficiency limitations arising from an unfavorable relative permittivity of the buffer layer have not been discussed in detail. In general, the relative permittivity $\varepsilon_r(\omega)$ of a material, also known as dielectric constant, depends on frequency $\omega$ or, equivalently, on photon energy. It describes how the material polarizes under the application of an electric field. For solar cell applications, two spectral regions are of primary interest. The first is the static permittivity $\varepsilon_r(0)$, or simply $\varepsilon_r$, which describes polarization under a constant electric field. This is a relevant quantity in the depletion region of a solar cell, where a constant electric field exists due to the presence of a space charge region [3]. The second important spectral region corresponds to the solar spectrum (optical frequencies). In this region, the relative permittivity becomes a complex number and describes the optical phenomena of light absorption and refraction in the solar cell material [4, 5]. Optical optimization of solar cells has already received much attention in solar cell research and will not be treated here. Instead, this work will focus on the static component of the permittivity while the optical component is kept fixed. The static component will be simply called "relative permittivity" or "permittivity" in the following for simplicity.

To study the effect of the materials permittivity on solar cell performance, we will use a generic absorber/buffer/window solar cell architecture inspired by CIGS solar cell technology. This approach follows the recommendation of a reference book on heterojunction solar cell design [6]. Throughout this work, we will present different scenarios in which the relative permittivity of the solar cell materials affects the power conversion efficiency significantly. We will show which physical mecha-

\textsuperscript{*}Corresponding author, email: ancro@nanotech.dtu.dk, Ørsteds Plads, building 345 East, DK-2800 Kgs. Lyngby, Denmark, Tel.: +45 45258455

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nisms are responsible for the observed changes in efficiency and we will quantify such changes by device simulation. Based on those results, we will suggest a design rule for the relative permittivity of novel heterojunction solar cell materials. The rule is relatively easy to implement since the relative permittivity of materials is usually known or easily measurable [7].

2. Theory

2.1. Effect on depletion region width

Using standard equations for p-n heterojunctions in the depletion approximation with a p-type absorber and an n-type partner, the effect of the relative permittivity of the two heterojunction materials on device operation can be summarized by two mathematical relations [3]. The first is

\[ W = \left( \frac{2V_{bi}\varepsilon_n\varepsilon_p (N_d + N_A)^2}{\varepsilon N_d N_a (\varepsilon_n N_d + \varepsilon_p N_A)} \right)^{1/2} \]  

Here \( W \) is the total width of the depletion region, \( \varepsilon_n \) and \( \varepsilon_p \) are the static permittivities of the n- and p-type semiconductors, \( V_{bi} \) is the built-in voltage in equilibrium, \( \varepsilon \) is the elementary charge and \( N_d \) and \( N_a \) are the acceptor and donor densities of the p- and n-type material, respectively (Fig. 1). In most practical cases, both light absorption and collection efficiency are low in the n-type heterojunction partner, so it can be more instructive to deal with the part of the depletion region that lies in the p-type absorber, which has a width

\[ W_p = \left( \frac{2V_{bi}\varepsilon_n\varepsilon_p N_d}{\varepsilon N_a (\varepsilon_n N_d + \varepsilon_p N_A)} \right)^{1/2} \]  

Eq. 2 predicts that the width of the depletion region increases as the permittivity of the two heterojunction materials increases. Notice that, if one of the two sides of the junction is doped heavily with respect to the other, then it is only the permittivity of the lightly-doped side of the junction that contributes to depletion region widening. Since minority carriers are collected most efficiently within a depth corresponding to the depletion width in the absorber layer plus their diffusion length, widening of the depletion region increases the depth at which the carriers can be efficiently collected. This can noticeably increase the short circuit current density \( J_{sc} \) in the cases where the total width of the two above regions is not enough to efficiently collect all photo-generated carriers, especially those due to long-wavelength photons. If, on the contrary, the collection length is large compared to the light absorption depth in the absorber material, the short circuit current is unaffected by the permittivities. The influence of the permittivity on solar cell performance in those two scenarios will be shown in detail in Sec. 4.5.

2.2. Effect on built-in voltage distribution

The second relation [3] describing the effect of the materials permittivity on device performance is the following:

\[ \frac{V_{bi,n}}{V_{bi,p}} = \frac{\varepsilon_p N_d}{\varepsilon_n N_A} \]  

Here, \( V_{bi,n} \) and \( V_{bi,p} \) are the built-in voltage drops in the n-type and p-type junction materials, as shown in Fig. 1. This relation is valid for a single heterojunction without interface charge. If there is a high interface charge density the \( V_{bi,n}/V_{bi,p} \) ratio is not determined by Eq. 3 anymore, but it is determined by the energy level of the charged state at the interface (“Fermi level pinning”). For intermediate cases with moderate interface charge density, no simple expression exists to calculate the \( V_{bi,n}/V_{bi,p} \) ratio, which must therefore be determined by numerical simulation. This will be done in Sec. 4.3 and Sec. 4.4.

For an ideal heterointerface (i.e., with zero interface recombination velocity), the \( V_{bi,n}/V_{bi,p} \) ratio does not affect the efficiency [6]. However, in the presence of interface recombination, an increase in the \( V_{bi,n}/V_{bi,p} \) ratio leads to a decrease in the open circuit voltage \( V_{oc} \) (for \( 0 < V_{bi,n}/V_{bi,p} < 1 \)) and in the short circuit current \( J_{sc} \) (for \( V_{bi,n}/V_{bi,p} > 1 \)), due to trends in the relative abundance of electrons and holes at the interface [6]. Hence, the \( \varepsilon_n/\varepsilon_p \) ratio is a key quantity that can in principle be used to tune the built-in voltage drop distribution \( V_{bi,n}/V_{bi,p} \) and therefore the efficiency of the solar cell. Specifically, it appears that increasing the \( \varepsilon_n/\varepsilon_p \) ratio is beneficial for increasing solar cell efficiency. However, this statement must be qualified by several factors. First of all, if the n-type material is much more highly doped than the p-type material, \( V_{bi,n} \) will be very small compared to \( V_{bi,p} \) regardless of the permittivity ratio, as permittivities of most semiconductors only vary over one order of magnitude. This renders all buffer-free solar cell architectures — with the absorber in direct contact with a highly doped window — insensitive to the \( \varepsilon_n/\varepsilon_p \) ratio. On the other hand, if \( N_d \) is not much greater than \( N_a \), then the permittivity ratio can significantly influence the voltage drop ratio, as will become evident in Sec. 4.1. However, even under those conditions, if the lowly-doped buffer layer is thin enough it will be completely depleted, such that \( V_{bi,n} \) will be dictated solely by its doping.
density and thickness, and not by its permittivity. This will also be shown numerically in Sec. 4.1. In the limiting case of an infinitely thin buffer layer, we are again in the case of a direct heterojunction discussed above. Finally, if interface charge is present in appreciable amount, Fermi level pinning occurs and Eq. 3 is again invalidated, because the built-in voltage drop distribution is now dictated by the (pinned) position of the Fermi level at the interface. Negative interface charge can be present in the form of occupied acceptor states (Sec. 4.4), which have a detrimental effect on $V_{oc}$ because they increase $V_{bi,n}/V_{bi,p}$. On the other hand, unoccupied donor states close to the conduction band (positive interface charge, Sec. 4.3) have a beneficial effect on $V_{oc}$ because they decrease $V_{bi,n}/V_{bi,p}$.

In Sec. 4, we will quantify variations in solar cell performance in the cases where the permittivity does play a role. From now on, the $p$ and $n$ subscripts of the discussed physical quantities always refer to the absorber and buffer layers respectively. This is because the window layer used in the model does not contribute to any built-in voltage drop due to its higher doping density (about two orders of magnitude higher than the absorber and the buffer, see Table 1).

3. Simulation details

Device-level simulations were performed numerically in one dimension with the finite-element method as implemented in the thin-film solar cell simulation software SCAPS [8]. The simulated solar cell has the absorber/buffer/window architecture and its simulated band diagram is shown in Fig. 1. The actual materials parameters, shown in Table 1, were taken from various literature sources [6, 9, 10] and are based on a CIGS absorber, a CdS buffer, and a ZnO window. This approach was also followed in [6] as a case study to formulate general design rules for heterojunction solar cells. We emphasize that we have tried to keep the solar Cell model as simple as possible and we have purposely avoided simulating peculiar aspects of CIGS solar cells, such as band gap broadening at the heterointerface due to formation of the ordered vacancy compound [10, 11]. This is because our goal is to provide a reasonable baseline model to predict qualitatively how the permittivity influences the efficiency in a generic heterojunction solar cell, rather than to model a CIGS solar cell as accurately as possible. Except for the parameters varied in each simulation, and unless otherwise specified, the parameters used in all simulations are always fixed to their baseline values, which are specified in Table 1.

4. Results

To show how solar cell performance is sensitive to changes in the $\varepsilon_n/\varepsilon_p$ ratio, two cases are considered in each simulation. Case A (B) corresponds to a low (high) buffer-to-absorber permittivity ratio. In both cases, the permittivity of the absorber is fixed at $\varepsilon_p = 13.6$ as in a CIGS absorber [12]. In Case A (B), the permittivity of the buffer is set to $\varepsilon_n = 8$ ($\varepsilon_n = 20$), corresponding to ZnS and to CdO respectively [13, 14]. The permittivity of commonly used buffer layer materials falls within this range. The permittivity ratio sensitivity factor $\Gamma$ is then employed as a single indicator to summarize how solar cell performance is sensitive to changes in the $\varepsilon_n/\varepsilon_p$ ratio. $\Gamma$ is defined as follows:

$$\Gamma = \frac{\eta_B}{\eta_A}$$

where $\eta_A$ is the simulated power conversion efficiency of the solar cell for Case A (low buffer-to-absorber permittivity ratio), and $\eta_B$ is the simulated efficiency for Case B (high buffer-to-absorber permittivity ratio). In short, if $\Gamma \sim 1$, the $\varepsilon_n/\varepsilon_p$ ratio does not affect the efficiency; if $\Gamma < 1$, the efficiency decreases as the $\varepsilon_n/\varepsilon_p$ ratio decreases.

In Figs. 2-5, we quantify variations in $\Gamma$ due to the influence of the permittivity ratio $\varepsilon_n/\varepsilon_p$ on voltage drop ratio $V_{bi,n}/V_{bi,p}$ (Eq. 3 and Sec. 2.2). Those variations in $\Gamma$ are primarily due to open circuit voltage variations as long as $V_{bi,n}/V_{bi,p}$ is not too large, as discussed in Sec. 2.2.

4.1. Dependence on buffer doping density and thickness

Fig. 2 demonstrates quantitatively what was qualitatively discussed in Sec. 2.2, i.e., that in the limit of high buffer doping or small buffer thickness, the efficiency is independent of the $\varepsilon_n/\varepsilon_p$ ratio. For high buffer doping, this occurs because $V_{bi,n}$ remains small even in the case of a high $\varepsilon_n/\varepsilon_p$ ratio. For a thin buffer with low doping, on the other hand, this occurs because the buffer is completely depleted regardless of the permittivity ratio, and therefore $V_{bi,n}$ is determined only by the donor doping density and buffer thickness.

This simulation demonstrates that a high $\varepsilon_n/\varepsilon_p$ ratio helps maintain a reasonably high efficiency even in solar cells with a thick and/or weakly doped buffer layer.

4.2. Dependence on heterointerface quality

In Fig. 3, we analyze the effect of interface quality on $\Gamma$. By conduction band offset, we intend the conduction band discontinuity at the heterointerface. In our model, the offset is equal to the difference in electron affinity between the absorber and the buffer. As discussed in Sec. 2.2, increasing interface recombination velocity increases the severity of the losses related to the potential drop $V_{bi,n}$ in the buffer, and $\Gamma$ decreases accordingly due to Eq. 3. This effect becomes more severe as the conduction band offset decreases, because the device becomes more sensitive to the $V_{bi,n}/V_{bi,p}$ ratio [6, 9].

This simulation demonstrates that a high $\varepsilon_n/\varepsilon_p$ ratio alleviates the efficiency losses related to non-idealities at the heterointerface, such as unfavorable band alignment and high interface recombination velocity.

4.3. Dependence on interface donor states

In Fig. 4, we examine the influence of Fermi level pinning on $\Gamma$. While keeping the (neutral) interface defects responsible for the baseline interface recombination velocity as in the above studies, we add a single interface donor state with different densities and at different energy levels. If the interface donors are sufficiently abundant and sufficiently close to the conduction band, the interface Fermi level is pinned to the donor level, the
V_{\text{in,a}}/V_{\text{in,p}} ratio is determined by the donor level instead of by Eq. 3, and Γ saturates to 1. At the other extreme (low donor density and low energy level), Γ approaches the baseline value of 71% shown in Fig. 2 regardless of the donor properties. This is because the amount of additional positive charge at the interface is not enough to influence the V_{\text{in,a}}/V_{\text{in,p}} ratio significantly, which is now determined by Eq. 3 as in the baseline case.

This simulation shows that, when the Fermi level is pinned by interface donors, the $\varepsilon_n/\varepsilon_p$ ratio is irrelevant for the efficiency of the solar cell.

4.4. Dependence on interface acceptor states

In a similar fashion, we examine in Fig. 5 the influence of an interface acceptor on Γ. If the energy level of the acceptor state is close enough to the conduction band for it to be mostly unoccupied, or if its density is too low to contribute with a appreciable amount of extra negative fixed charge, Γ is fixed to its baseline value as in the interface donor case. If the fixed negative charge provided by the acceptor becomes significant, Γ decreases without approaching a saturation value.

This simulation shows that a high $\varepsilon_n/\varepsilon_p$ ratio limits the decrease in efficiency that necessarily occurs when a high density of acceptor states in present at the heterointerface.
4.5. Dependence on minority carrier lifetime and heterojunction band alignment

Finally, in Fig. 6 we show variations in $\Gamma$ that are not related to the built-in voltage drop distribution, but that are instead related to variations in the depletion region width (Eq. 2 and Sec. 2.1). To exclude the effects related to the built-in voltage drop distribution (Figs. 2-5), we set the interface recombination velocity to zero, so that the $V_{\text{bi,n}}/V_{\text{bi,p}}$ ratio does not influence the efficiency, as discussed in Sec. 2.2. All other parameters are kept at their baseline value. The explored parameters are the conduction band offset and the minority carrier (electron) lifetime in the bulk of the absorber. The lifetime determines the collection depth of the photogenerated carriers out of the depletion region, with higher lifetimes giving a longer collection depth. The lifetime is inversely proportional to the density of bulk defects in the absorber $N_i$ (Table 1), which is the parameter that has been changed in practice to obtain the range of lifetimes shown in Fig. 6. In this particular study, the variations in $\Gamma$ are primarily due to short circuit current variations because changing the $\varepsilon_n/\varepsilon_p$ ratio changes the depletion region width $W_p$ in the absorber under short circuit conditions, and therefore the total collection depth. Unless the conduction band offset is large and positive, $\Gamma$ decreases as the lifetime decreases. This is in accordance with Eq. 2 because, as lifetimes decrease, a larger fraction of $J_{sc}$ comes from carriers generated in the depletion region, hence the variation in depletion region width by variation in buffer permittivity becomes more important. Note that, even though we keep using $\Gamma$ as a measure of permittivity-related effects, here it is not the permittivity ratio $\varepsilon_n/\varepsilon_p$ that causes the observed effect but simply the permittivity of any of the two materials, according to Eq. 2. Since we keep $\varepsilon_p$ constant in our simulation, it is $\varepsilon_n$ alone that influences the solar cell efficiency. For a large positive conduction band offset, another mechanism limiting the short circuit current exists, i.e., the barrier to electron flow due to the conduction band spike. Electron transport through the spike depends on the kinetic energy of the electrons reaching the barrier from the absorber. This energy is, at maximum, equal to $eV_{\text{bi,p}}$ if electrons are generated outside the depletion region or at its edge. In case of generation within the depletion region, the expected value of the kinetic energy depends on the voltage drop between the point of generation and the heterojunction. That explains the drop of $\Gamma$ at a conduction band offset of $0.4 \text{ eV}$ for low lifetimes. In such a situation, most of the collected electrons come from the depletion region, so $J_{sc}$ changes significantly due to changes in $V_{\text{bi,p}}$ by different permittivity ratios. For a spike larger that roughly $0.5 \text{ eV}$, all electron current from the absorber is blocked regardless of how the voltage drop is distributed and the only (few) collected electrons are from the buffer layer. Hence, $\Gamma$ becomes independent of the absorber lifetimes and, in the extreme case of a $0.6 \text{ eV}$ barrier, $\Gamma$ is larger than 1 because in such conditions it is beneficial to have a high $V_{\text{bi,n}}/V_{\text{bi,p}}$ ratio to maximize collection efficiency in the buffer.

This simulation shows that a high permittivity in either the buffer or the absorber generally improves the efficiency of solar cells with short minority carrier lifetimes, even though the degree of improvement varies depending on the band alignment at the heterointerface.

5. Discussion

With the exception of this last rather unrealistic case (CBO $> 0.5 \text{ eV}$), we note that $\Gamma \leq 1$ in all parameter ranges explored in Figs. 2-6. Assuming that the permittivity of the absorber is fixed, this means that a high permittivity in the buffer is, in general, either beneficial or irrelevant for device performance. In state-of-the-art Cu(In,Ga)Se$_2$ solar cells it is most likely to be irrelevant. This is because high-efficiency Cu(In,Ga)Se$_2$ solar cells have sufficiently high (above 10 ns) minority carrier lifetimes, so that a high buffer permittivity is not expected to influence current collection, as explained in Sec. 2.1 and Sec. 4.5. Moreover, band gap broadening at the heterointerface and Fermi level pinning by interface donors [10, 11] are expected to lead to a low interface recombination rate and a low $V_{\text{bi,n}}/V_{\text{bi,p}}$ ratio regardless of the permittivity of the materials, as explained in Sec. 2.2 and Sec. 4.3.

On the other hand, higher band gap absorbers such as Cu(In,Ga)S$_2$, CuInS$_2$, and Cu$_2$ZnSnS$_4$ are believed to be more sensitive to interface recombination [10, 11, 15], so a high-permittivity buffer layer may be an advantage in those cases as it would decrease the $V_{\text{bi,n}}/V_{\text{bi,p}}$ ratio and increase the open circuit voltage (Sec. 2.2 and Sec. 4.2). In the case of Cu$_2$ZnSnS$_4$ and Cu$_2$ZnSnSe$_4$, the minority carrier lifetimes appear to be significantly lower than in Cu(In,Ga)Se$_2$ [16]. Hence, with such absorbers, a high buffer permittivity may also be beneficial for current collection by widening the depletion region (Sec. 2.1).
and Sec. 4.5). Note that, the lower the permittivity of the absorber, the easier it is to obtain a high $\varepsilon_n/\varepsilon_p$ ratio by different buffer materials. This means that finding a high-permittivity buffer material is a less stringent requirement in solar cells based on low-permittivity absorbers.

6. Conclusion

When designing novel absorber-buffer-window combinations for heterojunction solar cells, it is advantageous to choose among high-permittivity buffer materials. This is because a high buffer-to-absorber permittivity ratio makes a solar cell more robust against non-idealities that may be encountered in early stage research, such as low buffer doping, large required buffer thickness, suboptimal band alignment, interface recombination, and negative interface charge. Even among existing heterojunction solar cell technologies, those that are limited by interface recombination may experience an improvement in open circuit voltage if a high-permittivity buffer material is used. Many oxide materials have a high permittivity, in addition to other desirable properties such as high band gaps and natural n-type doping. Hence, they may be the preferred class of materials as buffer layers for p-type absorbers and have already been used with promising results [17–19]. A high permittivity in either the buffer or the absorber also improves current collection when minority carrier lifetimes are low. Therefore, while there is a clear design rule for the permittivity of the buffer layer, there is not a universal design rule for the permittivity of the absorber layer.

Acknowledgments

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References

Table 1: The baseline parameters used for simulating the absorber/buffer/window heterojunction solar cell analyzed in this work. The materials parameters for each layer are taken from CIGS (absorber), CdS (buffer), and ZnO (window) following various literature sources [6, 9, 10]. $S_e$ and $S_h$ are the interface recombination velocities for electrons and holes; $\phi_{Bn0}$ and $\phi_{Bp0}$ are the contact barrier heights; $E_g$ is the band gap; $\chi$ is the electron affinity; $\varepsilon_r$ is the static relative permittivity; $N_C$ and $N_V$ are the effective density of states in the conduction and valence band respectively; they are derived from the effective masses of electrons and holes, which are taken as $0.09m_0$ and $0.7m_0$ respectively ($m_0$ is the electron rest mass); $\mu_e$ and $\mu_h$ are the electron and hole mobilities; $N_d/a$ is the shallow dopant density in the case of donors and acceptors, respectively; $N_t$ is the defect density; $E_t$ is the defect energy level (referenced to the valence band maximum $E_{V}$ of the material); $\sigma_e$ and $\sigma_h$ are the capture cross sections for electrons and holes respectively. In the simulation, the conduction band offsets (CBO) between layers are determined by the difference in their electron affinity. Hence, the baseline CBO at the absorber/buffer (buffer/window) interface is +0.2 eV (−0.2 eV).

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<tr>
<td>$\sigma_e, \sigma_h$ (cm$^2$)</td>
<td>$10^{-16}$</td>
<td></td>
</tr>
</tbody>
</table>