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Massahi, Sonny; Girou, David Alain; Della Monica Ferreira, Desiree; Christensen, Finn Erland; Jakobsen, Anders Clemen; Shortt, B; Collon, M.; Landgraf, B.

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Investigation of Photolithography Process on SPOs for the ATHENA Mission

S. Massahi, D.A. Girou, D.D.M. Ferreira, F.E. Christensen, A.C. Jakobsen, B. Shortt, M. Collon and B. Landgraf

aNSI, Elektrovej 327, Kgs. Lyngby, Denmark;
bESTEC, Keplerlaan 1, Noordwijk, The Netherlands;
cCOSINE, J.H.Oortweg 19, Leiden, The Netherlands;

ABSTRACT

As part of the ongoing effort to optimize the throughput of the Athena optics we have produced mirrors with a state-of-the-art cleaning process. We report on the studies related to the importance of the photolithographic process. Pre-coating characterization of the mirrors has shown and still shows photoresist remnants on the SiO$_2$-rib bonding zones, which influences the quality of the metallic coating and ultimately the mirror performance. The size of the photoresist remnants is on the order of 10 nm which is about half the thickness of final metallic coating.

An improved photoresist process has been developed including cleaning with O$_2$ plasma in order to remove the remaining photoresist remnants prior to coating. Surface roughness results indicate that the SiO$_2$-rib bonding zones are as clean as before the photolithography process is performed.

Keywords: Athena, Silicon Pore Optics (SPO), AFM, SEM, Photoresist, Surface Roughness, Photolithography Process, O$_2$ Plasma, Stacking

1. INTRODUCTION

The Advanced Telescope for High Energy Astrophysics (Athena), has been selected as the second Large Mission of the European Space Agency’s Cosmic Vision Science Programme, future X-ray Space telescope mission. Athena is a next-generation X-ray space observatory designed to study the hot, million-degree universe (e.g. supermassive black holes, evolution of galaxies and large-scale structures and matter under extreme conditions). The observatory concept is based on novel telescope optics - Silicon Pore Optics (SPO), with the focal plane instrumentation consisting of a Wide Field Imager (WFI) and Cryogenic X-ray Spectrometer (X-ray Integral Field Unit (X-IFU)).

The telescope is likely to consist of $\sim$ 120,000 specially designed mirror plates arranged into SPO mirror modules.

The development of SPO has been ongoing for more than a decade with early efforts focused on demonstrating the basic principle, manufacturing steps and potential for use in space. The confirmation of the second Large Mission theme as the hot and energetic Universe and the subsequent selection of Athena, has allowing the technology development to become more focused through the consolidation of requirements. Current technology developments are focused on the angular resolution performance, mirror integration approach demonstration, environmental ruggedness and the mirror plate production process.

The process scheme from a 300 mm silicon wafer to a stack of SPO comprising a mirror module is illustrated in fig. 1. Prior to the metallic deposition, the mirror plates are oxidized (2, fig. 1), diced (3, fig. 1), ribbed (4, fig. 1), wedged (5, fig. 1), and have undergone a photolithography process (6, fig. 1). These steps are prerequisite for the silicon-to-silicon bonding of the plates into mirror modules. The angular wedge consists of a thermally grown SiO$_2$-layer on both sides of the plate, with a linearly graded thickness over the mirror plate. The backside of the SPO is ribbed in order to produce the pores through which the X-rays propagate with the ribs serving as
the mechanical interface (bond) between adjacent plates. The size of the mirror plates investigated in this paper is 65.7x65.7x0.775 mm$^3$.

In the photolithography process, the top-side of the mirror plate is covered with a film-layer referred to as photoresist. The photoresist layer is spray-coated onto the SPO, which provides a uniform thickness of the film. An identical pattern in relation to the ribbed backside is produced in the film with a mask and UV-light, providing a top-surface pattern of SiO$_2$ and photoresist. The goal of the activity reported in this paper, is to identify the optimal and cleanest photolithography process performed on the mirrors, such that the surface roughness and reflectivity meet the requirements (angular resolution of 5" HEW at < 7 keV).

Figure 1. Process scheme of the production of an SPO mirror plate and mirror stack. 1. 300 mm Silicon wafer is prepared for oxidation. 2. An oxide layer is grown on the Silicon wafer. 3. The 300 mm wafer is diced into squares. 4. The plates are ribbed on the backside. 5. A linearly graded wedge is etched in the oxide layer. 6. A photolithography process (photoresist spraycoating, patterning exposure and development) is performed on the wafer. 7. Metal deposition, cleaning and lift-off. 8. The SPOs are bonded into a stack.
2. PRE-COATING CHARACTERIZATION OF THE MIRRORS

A pre-coating characterization of SPO mirror plates processed up to step six (figure 1) has been performed. The purpose of the pre-coating characterization is that photoresist remnants have previously been observed on the area where the metallic coating has to remain on the mirrors i.e. the X-ray reflecting stripes of the mirror plate. This was first discussed in a previous publication. An AFM (Atomic Force Microscope) was found to be the most useful tool in terms of characterizing the photoresist remnants. Furthermore a SEM (Scanning Electron Microscope) was used to characterize the photoresist stripes on a larger scale.

The AFM instrument used was a Bruker AFM Icon. The instrument has a xy scanning range up to 90 µm². Two scanning areas were used in this study; 1x1 µm² and 10x10 µm². The noisefloor of the AFM is as low as 30 pm which makes it possible to produce subnanometer measurements. The AFM probe used for the measurements is SCANASYST-AIR with a tip-radius lower than 12 nm which was used in Peak Force mode.

Three spots were chosen for each mirror plate characterized by AFM, as indicated in figure 2. These spots are chosen to lie inbetween the photoresist stripes i.e. the SiO₂ stripes which will ultimately be the Ir/B4C coated reflecting surfaces.

![Figure 2](image)

Figure 2. Indication of the 3 AFM scanning spots on the SPO mirror plate. The right image shows that the measurements are performed on the SiO₂ layer inbetween the photoresist stripes.

Fig. 2, shows an SPO with 3 indicated spots, each representing a scanning area of spot 1, 2 and 3. Three spots were chosen in all the measurements, which provides good statistics of the overall morphology of the plates. A typical set of SPO mirror plate images produced with the SEM is illustrated in fig. 3. The right image shows the smooth edge of the photoresist (thinner darker stripes) after the development process (step 6).

![Figure 3](image)

Figure 3. Images produced with a SEM. The photoresist is indicated by the dark-grey areas and the light-grey areas indicate the SiO₂ layer.
The two top rows in figure 4 show AFM measurements of a plate that has undergone the ribbing process (up to process step 5). This is before the photolithography process and as the images indicate the SiO$_2$ surface is clean. The two lower rows in fig. 4, show images of a substrate that has undergone the current baseline spray-coated photoresist deposition process (up to process step 6). There is a clear indication of photoresist remnants on the surface both on small and large scale. The height of the tallest remnant is $\sim$ 10 nm, which is $\sim$ 53% of the total thickness of the Ir/B$_4$C baseline coating.$^7,^8$ This affects the geometry of the metallic coating and can even produce holes in the coating after the lift-off process.

Figure 4. AFM measurements performed on SiO$_2$. The two upper rows show the SiO$_2$ layer before the photolithography process. The two lower rows show the SiO$_2$ layer after the development process. The scanning area is 1x1 $\mu$m$^2$ for the first and third row and 10x10 $\mu$m$^2$ for the second and fourth row. Spot 1 is the left column, spot 2 is the center column and spot 3 is the right column.
3. OPTIMIZATION OF THE PHOTOLITHOGRAPHY PROCESS

Based on previous investigations, reconfirmed through the results of the pre-coating characterization as described in section 2, an effort to optimize the steps in the photolithography process (step 6) leading to a cleaner SiO$_2$ surface has been made. The photolithography process studies have been performed at DTU Danchip, which is the Danish national center for micro- and nanofabrication. The process scheme, which utilizes spin coating, is shown in fig. 5 and a short description of each step is given below:

**Step 6.1** Hexamethyldisilazane (HMDS) treatment, which increases the adhesion of the photoresist to the SiO$_2$ surface. HMDS is introduced into the chamber, where the wafer is placed, as a vapor. This produces a layer of hydrophilic molecules on top of the hydrophobic surface (SiO$_2$), which easily bonds with the photoresist.

**Step 6.2** Photoresist deposition via spin coating, the thickness of the deposited resist layer was measured to be $\sim 9$ µm. The photoresist applied is a negative photoresist called nLOF 2070. After the deposition of photoresist the wafer is soft baked on a hotplate (this removes solvents in the resist)

**Step 6.3** UV-exposure, the exposed areas become more dismissive for the developing chemical. This applies for a negative resist as the one used in the spin-coating process. For a positive resist which is the one used for the spray-coating process, the opposite applies. After the UV-exposure the wafer is post baked on a hot plate (this cross-links the polymers in the resist)

**Step 6.4** Development, a special chemical removes the unexposed areas of the photoresist

![Figure 5. Photolithography process steps. 1) HMDS on SPO. 2) Photoresist deposition of nLOF 2070 via spin coating. 3) UV-exposure of the substrate with a mask. 4) Development of the UV-exposed areas.](image)

Following implementation of the described process, it was discovered that enhanced cleanliness of the process was achievable within the development process step (step 6.4). The chemical used to dissolve the unexposed photoresist has a particular importance. The chemical used for the initial process study was AZ 726 MIF (a solution of 2.38% tetra Methyl Ammonium Hydroxide). This developer showed a similar photoresist remnant pattern as the spray-coated plates (figure 4, (bottom two rows)), see figure 6 (top two rows).

From an investigation of the datasheet for the AZ 726 MIF developer, an alternative developer, AZ 826 MIF, with added surfactant, was identified. This developer was considered interesting as the added surfactant and additives should yield a cleaner and scum-free development.

The photolithography process was repeated multiple times using the AZ 826 MIF developer with the results being shown in fig. 6 (lower two rows). A significant improvement in the cleanliness of the plates has been obtained. The improvement is quantified through surface roughness measurements which are presented in Section 5.

Even though the residual photoresist on the surface was significantly reduced, it was decided to implement a further cleaning process in order to remove the remaining residuals. This cleaning process is described in the following section.
Figure 6. AFM measurements performed on SiO₂ before metal deposition. Measurements have been performed after the development process. Development chemical AZ 726 MIF was used on the two top rows and AZ 826 MIF was used on the two lower rows. The scanning area is 1x1 µm² for the first and third row and 10x10 µm² for the second and fourth row. Spot 1 is the left column, spot 2 is the center column and spot 3 is the right column. The significant improvement achieved through the use of the surfactant enriched developer is clear.
4. O₂ PLASMA CLEANING

As stated in the previous section a cleaning method was investigated in order to remove the residuals remaining following the switch to the better performing developer. Initially, a wet chemical cleaning was under consideration, however such an etching method is likely to increase the surface roughness. Alternatively, a dry cleaning process such as an O₂ plasma cleaning can be used and is in fact usually recommended after a development process in order to remove scumming (water combined with photoresist). Therefore it was chosen to perform this cleaning process on the mirror plates. A low intensity O₂ plasma clean was chosen so as not to remove the photoresist stripes required for step 7 of the overall SPO process i.e. metal deposition and lift-off.

The O₂ plasma etching process removes amounts of photoresist in an isotropic way and was in this case used to clean the substrate surface after the photolithography process. The bombardment of Oxygen atoms on top of the substrate either carries away or reacts with the organic photoresist on the surface. The result of this reaction is H₂O and CO₂. The process takes place in a vacuum chamber located at Danchip with an extraction system, so the H₂O and the CO₂ are removed. An illustration of this process is shown in figure 7.

![Oxygen Plasma Diagram](https://via.placeholder.com/150)

Figure 7. O₂-plasma cleaning process. **Left** ionized Oxygen atoms are bombarded onto the sample, resulting in a reaction between the Oxygen and the organic material. **Right** actual image of a cleaning process at DTU of an SPO.

In order to determine the optimized O₂-plasma cleaning process different intensities, gas inflows and durations were tested.

The intensity is directly related to the temperature of the chamber and at high intensities (power of 1000 W) the temperature reaches as high as 110°C. The high intensity affects the sidewalls of the photoresist, which changes the final pattern on the mirror. If the intensity is too low, the gasses do not ignite, resulting in no cleaning. Intensities between the two extremes were chosen. The process settings are shown in table 1.

The reason for a high start temperature for the wafer developed with AZ 726 MIF is that the chamber was used at a high intensity prior to this cleaning run. In general, the chamber is a room temperature at the start of a cleaning run, as seen for the two other experiments. In addition, the O₂ flow rate (400 sccm) was reduced to 200 (sccm) in order to have a less intense process for the second and third runs.

<table>
<thead>
<tr>
<th>Experiment</th>
<th>Deposition method</th>
<th>Developer Type</th>
<th>Inflow O₂ (sccm)</th>
<th>Inflow N₂ (sccm)</th>
<th>Power (W)</th>
<th>Duration (min)</th>
<th>T_start (°C)</th>
<th>T_max (°C)</th>
</tr>
</thead>
<tbody>
<tr>
<td>1</td>
<td>Spin</td>
<td>AZ 726 MIF</td>
<td>400</td>
<td>70</td>
<td>150</td>
<td>20</td>
<td>46</td>
<td>51</td>
</tr>
<tr>
<td>2</td>
<td>Spin</td>
<td>AZ 826 MIF</td>
<td>200</td>
<td>70</td>
<td>100</td>
<td>15</td>
<td>21</td>
<td>36</td>
</tr>
<tr>
<td>3</td>
<td>Spray</td>
<td>Fuji OPD 4262</td>
<td>200</td>
<td>70</td>
<td>100</td>
<td>30</td>
<td>23</td>
<td>36</td>
</tr>
</tbody>
</table>

Table 1. Parameters for the 3 different O₂ plasma cleaning process runs. The top two experiments utilize the optimized process as described in section 3. The bottom row (experiment 3) is an SPO mirror plate produced using the previously existing spray coating approach.
The images produced with the AFM after the cleaning are illustrated in figures 8 and 9. Visually the two top rows (AZ 726 MIF) in fig. 8 look slightly cleaner than before cleaning (fig. 6) but the size and quantity of residuals are too large for the cleaning process to remove them. The two lower rows in fig. 8 are very clean. The small amount of photoresist that was left after development with AZ 826 MIF has successfully been removed and the mirror plate is prepared for coating. In addition to investigating the cleaning efficiency upon the mirror plates with spin coated resist (experiment 1 and 2) it was decided to also check the process effect upon a mirror plate with spray coated resist (experiment 3). It can be seen that the low intensity cleaning for 30 min on the spray-coated photoresist was not sufficient to remove the residuals with the images in fig. 9 being similar to the ones before cleaning (fig. 4).

Figure 8. AFM measurements performed on SiO$_2$ after an O$_2$-plasma clean. Development chemical AZ 726 MIF was used on the two top rows and AZ 826 MIF was used on the two lower rows, both deposited via spin coating. The scanning area is 1x1 $\mu$m$^2$ for the first and third row and 10x10 $\mu$m$^2$ for the second and fourth row. Spot 1 is the left column, spot 2 is the center column and spot 3 is the right column.
5. SURFACE ROUGHNESS RESULTS

In order to quantify the improvement due to the optimized photolithography process the surface roughness of the SPO mirror plates has been calculated from the AFM results. To extract the surface roughness ($\sigma$) results the 1-dimensional power spectral density (1D-PSD) has been calculated. The surface roughness of the SPOs are required to be low after the multilayer coating and lift-off, in order to achieve the reflectivity and image quality specifications essential for the ATHENA mission. The software used to analyze the AFM measurements is called NanoScope. It has a built in 1D-PSD algorithm, which was used to calculate the surface roughness of the substrates. Prior to the calculation of the surface roughness a third order line wise correction of the full image was performed. The third order polynomial calculates the least squares fit for each line in the image, which cancels the Z-offset between the scanning lines as well as the tilt and bow in each line. By filtering out the low frequency noise, the tilt and the bow in the data, a more accurate surface roughness is achieved.

The calculated surface roughness of the plates developed with AZ 726 MIF, AZ 826 MIF and the developer from Micronit are given in tables 2-4. The average SPO mirror plate surface roughness before the photolithography process is 0.31 nm, given in table 2. The only plate that has an average surface roughness below 0.5 nm is the one developed with AZ 826 MIF and cleaned with a low intensity O$_2$-plasma cleaning process (experiment 2). The value is 0.42 nm. This is a clear improvement from the other developer AZ 726 MIF (experiment 1), which resulted in a surface roughness average of a factor 10 higher and the developer Fuji OPD 4262 (experiment 3) which is a factor of 3 higher.
### Table 2
Table showing the σ-values of the SPO developed with AZ 726 MIF (experiment 1). The scanning areas are; 1 µm x 1 µm and 10 µm x 10 µm, of three different spots.

<table>
<thead>
<tr>
<th>Developer</th>
<th>Spot 1</th>
<th>Spot 2</th>
<th>Spot 3</th>
</tr>
</thead>
<tbody>
<tr>
<td>AZ 726 MIF</td>
<td>Scan size</td>
<td>σ</td>
<td>σ</td>
</tr>
<tr>
<td></td>
<td>(µm)</td>
<td>(nm)</td>
<td>(nm)</td>
</tr>
<tr>
<td>Before photolithography</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>1.00 × 1.00</td>
<td>0.28</td>
<td>0.26</td>
<td>0.27</td>
</tr>
<tr>
<td>10.0 × 10.0</td>
<td>0.38</td>
<td>0.36</td>
<td>0.29</td>
</tr>
<tr>
<td>With resist</td>
<td>1.00 × 1.00</td>
<td>4.47</td>
<td>3.79</td>
</tr>
<tr>
<td></td>
<td>10.0 × 10.0</td>
<td>4.88</td>
<td>5.28</td>
</tr>
<tr>
<td>O₂ clean for 20 min</td>
<td>1.00 × 1.00</td>
<td>0.57</td>
<td>1.16</td>
</tr>
<tr>
<td></td>
<td>10.0 × 10.0</td>
<td>1.89</td>
<td>1.31</td>
</tr>
</tbody>
</table>

### Table 3
Table showing the σ-values of the SPO developed with AZ 826 MIF (experiment 2). The scanning areas are; 1 µm x 1 µm and 10 µm x 10 µm, of three different spots.

<table>
<thead>
<tr>
<th>Developer</th>
<th>Spot 1</th>
<th>Spot 2</th>
<th>Spot 3</th>
</tr>
</thead>
<tbody>
<tr>
<td>AZ 826 MIF</td>
<td>Scan size</td>
<td>σ</td>
<td>σ</td>
</tr>
<tr>
<td></td>
<td>(µm)</td>
<td>(nm)</td>
<td>(nm)</td>
</tr>
<tr>
<td>With resist</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>1.00 × 1.00</td>
<td>0.37</td>
<td>0.36</td>
<td>0.41</td>
</tr>
<tr>
<td>10.0 × 10.0</td>
<td>0.53</td>
<td>0.49</td>
<td>0.65</td>
</tr>
<tr>
<td>O₂-clean for 15 min</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>1.00 × 1.00</td>
<td>0.32</td>
<td>0.40</td>
<td>0.44</td>
</tr>
<tr>
<td>10.0 × 10.0</td>
<td>0.38</td>
<td>0.39</td>
<td>0.56</td>
</tr>
</tbody>
</table>

### Table 4
Table showing the σ-values of the SPO developed with Fuji OPD 4262 (experiment 3). The scanning areas are; 1 µm x 1 µm and 10 µm x 10 µm, of three different spots.

<table>
<thead>
<tr>
<th>Developer</th>
<th>Spot 1</th>
<th>Spot 2</th>
<th>Spot 3</th>
</tr>
</thead>
<tbody>
<tr>
<td>Fuji OPD 4262</td>
<td>Scan size</td>
<td>σ</td>
<td>σ</td>
</tr>
<tr>
<td></td>
<td>(µm)</td>
<td>(nm)</td>
<td>(nm)</td>
</tr>
<tr>
<td>With resist</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>1.00 × 1.00</td>
<td>1.39</td>
<td>1.02</td>
<td>1.32</td>
</tr>
<tr>
<td>10.0 × 10.0</td>
<td>1.30</td>
<td>1.14</td>
<td>1.29</td>
</tr>
<tr>
<td>O₂ clean for 30 min</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>1.00 × 1.00</td>
<td>1.17</td>
<td>1.06</td>
<td>1.26</td>
</tr>
<tr>
<td>10.0 × 10.0</td>
<td>1.30</td>
<td>1.20</td>
<td>1.27</td>
</tr>
</tbody>
</table>

### 6. SUMMARY

We have reported on the production of a Silicon Pore Optic and qualified the wafers prior metallic coating process. A pre-characterization of the plates after a spray-coated photoresist deposition indicates that photoresist remnants (≈10 nm thick) are located on the X-ray reflective surface. The small photoresist remnants have a large impact on the final geometry of the metallic coating and hereby the overall performance of the X-ray optics for the Athena mission.

We have developed a spin-coating photoresist process and located that the cleanliness of the Silicon Pore Optics can be improved in the development step by changing the developing chemical (AZ 826 MIF). The AZ 826 MIF dissolves photoresist and contains additives that remove photoresist residuals to a certain extend. The photoresist remnants are still observed but with a large reduction in size.

Furthermore we have implemented a cleaning step consisting of a dry Oxygen plasma process that has proven to remove the very small-scale (≈3 nm thick) photoresist remnants at a low intensity (power of 100 W) that are still present after the development process.
The characterization of the photoresist remnants has been performed with an Atomic Force Microscope. The surface roughness, related to the morphology of the surface, has been derived in a software called NanoScope and been used to compare the wafers after development and cleaning with different development chemicals. The lowest surface roughness was observed with the AZ 826 MIF developer type to be in the order of 0.4 nm compared to the wafer surface roughness prior the photoresist process ($\sigma = 0.3$ nm). The spray-coated photoresist developed with Fuji OPD 4262 showed an average surface roughness of 1.2 nm. The AZ 826 MIF will be tested in the spray-coating photoresist process instead of the Fuji OPD 4262 to examine if the same results are perceived as for the spin-coating process.

7. ACKNOWLEDGMENTS

We would like to thank DTU Danchip for providing us with their facilities and their knowledge. Especially, A. M. Jørgensen for helping designing the photoresist project and T. A. Anhøj for sharing his knowledge about photoresist processes in general.

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