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Novel field test design for acquisition of DC and AC parameters during service

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Abstract — Being able to monitor early signs of PV module degradation, is needed to ensure stable power production throughout the service life of a PV installation. Recently, impedance spectroscopy is proven to be a useful tool for detection of the presence and location of significant errors, and may have potential for more. In this work we describe a field test design where the modules are operating at their maximum power point, and via relays is switched out one by one for acquisition of an IV curve and an impedance spectrum. Some of the modules involved will undergo stimuli to accelerate certain degradation mechanisms, and fitting parameters extracted from the field test will be correlated with irradiance and compared to similar parameters of virgin modules of same kind, and conventional laboratory measurements on the same modules. The proposed method will provide data for exploration of early degradation signs using impedance measurements.

I. INTRODUCTION

Despite manufacture guarantee of 25 years, many defect and failure mechanisms occur on PV-modules in the field [1], [2]. For most non–solar farm installations the performance ratio is not monitored carefully [3]. Thus defects and failures are not discovered until they have developed significantly, a significant visual defect occurs or the power production from the installation decreases 10’s of % from one year to another. For residential installations, inspections and error searching are also associated with significant costs, almost comparable to the value of the installation.

Time domain reflectometry has been proposed and proved to be able to locate faults on a single PV string [4]. Recently, a new method has become commercially available for detecting the location of significant faults as open circuits, shorts, failing bypass diodes and earth faults [5]. This technique is based on advanced analysis of impedance measurements. Impedance spectroscopy has been widely used for DSC cells (Dye Sensitized Solar Cells) and thin-film technologies for decades. However, recent research shows promising results also applied on PV modules. This work will describe a measurement setup and preliminary results for a further investigation of which information about the PV modules health can be extracted from impedance analysis.

II. THEORY

Interpretation of impedance spectra’s using AC equivalent circuits for one specific spectra is fairly well described in the literature [6]-[8] and from [8] the simplified equivalent circuit model shown in Fig. 1. is found.

![Simplified equivalent AC circuit model of a PV-module](image)

In the model the corresponding complex impedance is given by $Z_{PV}$

$$Z_{PV} = R_s + \frac{R_p}{(\omega R_p C_p)^2 + 1} + j \frac{\omega R_p C_p}{(\omega R_p C_p)^2 + 1} \quad (1)$$

which can be fitted to measured data, and from there values of for $R_s$, $R_p$ and $C_p$ can be found. However, it is well known from the literature that these parameters change with illumination, and the bias.[6], [7], [9].

III. FIELD TEST

The overall objective of this work is to establish a field test that enables to investigate how the faults on PV modules develop during normal operation. Of particular interest is to monitor and compare the impedance spectra recorded in the open circuit bias with the IV-curves. The recorded panel data will then be correlated with irradiance data and module temperature data.

The objective described above will be achieved by connecting the modules to commercially available load electronics for grid connection. A relay system will be created which can switch the modules into a “measurement bus” facilitating a measurement of an IV–curve and acquisition of an impedance spectrum. The modules mounted on the field test will all be of the same kind and batch from the producer.
However, some will be new, and some will have been exposed to accelerated aging to stimulate specific degradation mechanisms.

The overall principle is shown in Fig. 2, and the load system is designed with the aim to maximize the time each module is operating in its maximum power point.

The wiring diagram is shown in Fig. 3, it consists of a module part switching the individual modules in to a measurement bus, consisting of both sense and current wires, and a measurement part directing the other end of the measurement bus to the desired characterization equipment.

The relays and measurement system is controlled via LabVIEW using the FPGA based Compact Rio technology where fast data acquisition is possible, and safety checks in the measurement routines allows only one panel at a time to be switched to the measurement bus.

Out of the 18 panels, 8 will be characterized in a cyclic routine with IV-curves and impedance spectra. 17 modules give a total nominal DC voltage on the module side of 306 V. This is chosen in order to give power optimizers a relatively low boost ratio of approximately 15 %, and thereby maximizing their operating range.

For the IV-tracing an electronic load will load the module to obtain 1. quadrant IV curves, with the voltages and current being measured by an external data logger capable of simultaneous sampling with a rate of 50 ks/sec. For impedance measurement a Z100 device from EmaZys Technologies Aps and a Gamry 3000 AE potentiostat will be used. The impedance of the cables will be measured with the cable shorted and used to correct the impedance measurements on the modules. An outdoor reference cell and a pyranometer will be mounted in plane of array (POA), with the first being able to detect fast changes in radiation and the latter being able to measure the POA irradiation precisely. The fast response time of the reference cell combined with the fast logging using the FPGA allows for point irradiances corrections of the IV curves. The temperature on the back of each module will also be logged using thermocouples.

### III. Experimental Plan

The aim of the field test is to search for signs of early degradation of modules, of particular interest is:

- By pass diode failures
- Potential induced degradation (PID)
- Micro cracks
- Ribbon damage
- Delamination

To obtain the best possible starting point 32 modules, fabricated in the same batch, will be used for the investigation.

- 2 modules will be flashed and stored in darkness to be used as reference
- 5 modules will be mounted outside and used as outdoor reference to monitor natural degradation
- 10 modules will be exposed to mechanical loads introducing micro cracks
- 5 modules will be exposed to high voltage to stimulate PID degradation
- 10 modules will be reserved for thermal aging

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Fig. 2. Conceptual sketch of the field test.

SolarEdge power optimizers (OP 250-LV) will be connected to the modules, allowing independent module operation for module voltages above 5 volts. The output of the power optimizers is connected in series to a Solar Edge 3 KVA inverter. The serial architecture of the power optimizer system allows tracking at low voltages, which increases the module choice, to better match physical limitations of e.g. chambers for accelerated ageing. However, a minimum number of panels are required to insure sufficient input voltage to the inverter. The first type of PV modules to be mounted on the testing rack are multi crystalline panels with 36 cells. The modules have a peak power of 150 W, and a maximum power point voltage of 18 Volts. In the field test, 18 panels of these will be mounted resulting in 2.55 kWp since one module will always be switched out for measurements and thus the inverter is oversized to avoid power limitations of the inverter.

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Fig. 3. Wiring concept of the field test.
Prior to any stimuli, flash curves, impedance spectra and electroluminescence (EL) test are performed on the modules. The outdoor reference modules together with a selection of modules that has received degradation stimuli will be mounted on the field. Prior to mounting on the field, test, flash tests, EL-test and laboratory impedance spectra’s will be recorded as references.

The fitted values for the AC parameters being the parallel resistance, the series resistance and the parallel capacitance will be correlated with irradiation and temperature, and their development will be monitored and compared with flashing results and EL imaging. It is the expectation from the authors, that these experiments will guide the way for using impedance spectroscopy on installations to detect early signs of module degradation.

IV. SUMMARY

In this work we have described a field test design with the purpose of getting more knowledge of PV module degradation by investigation by impedance spectroscopy alongside IV characterization. The modules are operating at their maximum power point, and are switched out one by one via relays for acquisition of an IV curve and an impedance spectrum. Some of the modules involved will undergo stimuli to accelerate certain degradation mechanisms, and fitting parameters extracted from the field test will be correlated with irradiance and compared to similar parameters of virgin modules of same kind, and conventional laboratory measurements on the same modules. The proposed method will provide data for exploration of early degradation signs using impedance measurements. The field test is in the moment in its final state of preparation and the software is close to being finished enabling a commissioning during the summer of 2016.

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