Time domain analysis of open loop distortion in class D amplifier output stages - DTU Orbit
(23/12/2018)

Time domain analysis of open loop distortion in class D amplifier output stages

General information
State: Published
Organisations: Electronics & Signal Processing, Department of Electrical Engineering, Department of Informatics and Mathematical Modeling, Centre for Physical Electronics
Contributors: Nyboe, F., Risbo, L., Andreani, P.
Publication date: 2005

Host publication information
Title of host publication: 27th International AES Conference
URLs:
Source: orbit
Source-ID: 188220
Research output: Research - peer-review › Article in proceedings – Annual report year: 2005