Publications:

**System-Level Design Methodologies for Networked Multiprocessor Systems-on-Chip**
Publication: Research › Ph.D. thesis – Annual report year: 2008

**ARTS: A SystemC-based framework for multiprocessor Systems-on-Chip modelling**
Publication: Research - peer-review › Journal article – Annual report year: 2007

**Functional Testing of Wireless Sensor Node Designs**
Publication: Research - peer-review › Article in proceedings – Annual report year: 2007

**ARTS: A System-Level MPSoC Modelling Framework**
Publication: Research › Interactive production – Annual report year: 2006

**ARTS: A System-Level Framework for Modeling MPSoC Components and Analysis of their Causality**
Publication: Research - peer-review › Article in proceedings – Annual report year: 2005

**Design of A Development Platform for HW/SW Codesign of Wireless IOnTEGRATED Sensor Nodes**
Publication: Research - peer-review › Article in proceedings – Annual report year: 2005

**Multiprocessor Systems - Modelling and Simulation**
Publication: Research - peer-review › Book chapter – Annual report year: 2005

**System-level Modelling of Wireless Integrated Sensor Networks**
Publication: Research - peer-review › Article in proceedings – Annual report year: 2005

**System-Level Modelling and Simulation of MEMS-Based Sensors**
Publication: Research - peer-review › Article in proceedings – Annual report year: 2005

**Abstract System-on-Chip modelling in SystemC**
Publication: Research › Article in proceedings – Annual report year: 2004

**A SystemC-Based Abstract Real-Time Operating System Model for Multiprocessor System-on-Chip**
Publication: Research - peer-review › Book chapter – Annual report year: 2004

**A system-level multiprocessor system-on-chip modeling framework**
Publication: Research - peer-review › Article in proceedings – Annual report year: 2004

**A system-level multiprocessor system-on-chip modelling framework**
Publication: Research - peer-review › Article in proceedings – Annual report year: 2004

**Network-Centric System-Level Model for Multiprocessor System-on-Chip Simulation**
Publication: Research - peer-review › Book chapter – Annual report year: 2004

**Abstract RTOS Modelling for Multiprocessor System-on-Chip**
Publication: Research - peer-review › Article in proceedings – Annual report year: 2003
Network-on-Chip Modeling for System-Level Multiprocessor Simulation
Publication: Research - peer-review › Article in proceedings – Annual report year: 2003

Resource Allocation Model for Modelling Abstract RTOS on Multiprocessor System-on-Chip
Publication: Research - peer-review › Article in proceedings – Annual report year: 2003

Projects:

System-Level Design Methodologies for Platform-based Multiprocessor SoC Designs
Project: PhD