Evangelia Kasapaki - DTU Orbit (04/02/2018)
Evangelia Kasapaki

Publications:

Argo: A Real-Time Network-on-Chip Architecture With an Efficient GALS Implementation
Publication: Research - peer-review › Journal article – Annual report year: 2015

An Asynchronous Time-Division-Multiplexed Network-on-Chip for Real-Time Systems
Publication: Research › Ph.D. thesis – Annual report year: 2015

T-CREST: Time-predictable multi-core architecture for embedded systems
Publication: Research - peer-review › Journal article – Annual report year: 2015

The Argo NOC: Combining TDM and GALS
Publication: Research - peer-review › Article in proceedings – Annual report year: 2015

A Loosely Synchronizing Asynchronous Router for TDM-Scheduled NOCs
Publication: Research - peer-review › Article in proceedings – Annual report year: 2014

Argo: A Time-Elastic Time-Division-Multiplexed NOC using Asynchronous Routers
Publication: Research - peer-review › Article in proceedings – Annual report year: 2014

Synthesis and Layout of an Asynchronous Network-on-Chip using Standard EDA Tools
Publication: Research - peer-review › Article in proceedings – Annual report year: 2014

An area-efficient network interface for a TDM-based Network-on-Chip
Publication: Research - peer-review › Article in proceedings – Annual report year: 2013

Router Designs for an Asynchronous Time-Division-Multiplexed Network-on-Chip
Publication: Research - peer-review › Article in proceedings – Annual report year: 2013

A Statically Scheduled Time-Division-Multiplexed Network-on-Chip for Real-Time Systems
Publication: Research - peer-review › Article in proceedings – Annual report year: 2012

Projects:

Asynchronous Network-on-Chip for Time-Predictable Multi-Core Embedded Systems
Project: PhD