Aske Wiid Brekling - DTU Orbit (16/07/2018)

Aske Wiid Brekling

Publications:

**A practical approach to model checking Duration Calculus using Presburger Arithmetic**
Publication: Research - peer-review › Journal article – Annual report year: 2013

**On Tool Support for Duration Calculus on the Basis of Presburger Arithmetic**
Publication: Research - peer-review › Article in proceedings – Annual report year: 2011

**Analysis for Embedded Systems: Experiments with Priced Timed Automata**
Publication: Research - peer-review › Journal article – Annual report year: 2010

**Modelling and Analyses of Embedded Systems Design**
Publication: Research › Ph.D. thesis – Annual report year: 2011

**Modelling and Analysis for Embedded Systems**
Publication: Research - peer-review › Book chapter – Annual report year: 2010

**A modelling and analysis framework for embedded systems**
Publication: Research - peer-review › Book chapter – Annual report year: 2009

**Analysis of Quantitative Properties of Hardware Specifications**
Publication: Research - peer-review › Article in proceedings – Annual report year: 2009

**MoVES - A Framework for Modelling and Verifying Embedded Systems**
Publication: Research - peer-review › Article in proceedings – Annual report year: 2009

**Proceedings of the 21st Nordic Workshop on Programming Theory, NWPT'09, Lyngby, Denmark, 14-16 October 2009**
Publication: Research › Report – Annual report year: 2009

**Analysis of costs of embedded systems: experiments with priced timed automata**
Publication: Research - peer-review › Article in proceedings – Annual report year: 2008

**Formal Verification of Design Properties of Hardware Architectures.**
Publication: Research - peer-review › Poster – Annual report year: 2008

**Models and formal verification of multiprocessor system-on-chips**
Publication: Research - peer-review › Journal article – Annual report year: 2008

**System-level verification of multi-core embedded systems using timed automata**
Publication: Research - peer-review › Article in proceedings – Annual report year: 2008

**Hardware modelling language and verification of design properties**
Publication: Research - peer-review › Article in proceedings – Annual report year: 2007

**MOVES - A tool for Modeling and Verification of Embedded Systems**
Publication: Research › Article in proceedings – Annual report year: 2007
Semantics and verification of a language for modelling hardware architectures
Publication: Research - peer-review › Book chapter – Annual report year: 2007

A Timed-Automata Semantics for a System-Level MPSoC model
Publication: Research - peer-review › Article in proceedings – Annual report year: 2006

Projects:

Models and Analyses for Embedded Systems Design
Project: PhD