Embedded Systems Engineering - Publications - DTU Orbit (29/04/2018)

Activity-based DEVS modeling
Publication: Research - peer-review › Journal article – Annual report year: 2018

A multicore processor for time-critical applications
Publication: Research - peer-review › Journal article – Annual report year: 2018

An Integrated Framework to Specify Domain-Specific Modeling Languages
Publication: Research - peer-review › Article in proceedings – Annual report year: 2018

AntibioTic: Protecting IoT Devices Against DDoS Attacks
Publication: Research - peer-review › Article in proceedings – Annual report year: 2018

DDoS-Capable IoT Malwares: Comparative Analysis and Mirai Investigation
Publication: Research - peer-review › Journal article – Annual report year: 2018

Double-Loop Health Technology: Enabling Socio-technical Design of Personal Health Technology in Clinical Practice
Publication: Research - peer-review › Book chapter – Annual report year: 2018

Face Recognition using Approximate Arithmetic
Publication: Research › Report – Annual report year: 2018

Lipsi: Probably the Smallest Processor in the World
Publication: Research - peer-review › Article in proceedings – Annual report year: 2018

Model-Based Systems Engineering for Life-Sciences Instrumentation Development
Publication: Research - peer-review › Journal article – Annual report year: 2018

Paper-based sensors for rapid detection of virulence factor produced by Pseudomonas aeruginosa
Publication: Research - peer-review › Journal article – Annual report year: 2018

Patmos: a time-predictable microprocessor
Publication: Research - peer-review › Journal article – Annual report year: 2018

Reconfiguration of Computation and Communication Resources in Multi-Core Real-Time Embedded Systems
Publication: Research › Ph.D. thesis – Annual report year: 2018
Selected papers from the 2nd IEEE Nordic Circuits and Systems Conference (NorCAS), 2016.
Sparsø, J. 2018 In : Microprocessors and Microsystems. 3 p.
Publication: Research - peer-review › Editorial – Annual report year: 2018

Towards Domain-specific Flow-based Languages
Publication: Research - peer-review › Article in proceedings – Annual report year: 2018

Supporting smartphone-based behavioral activation: A simulation study
Publication: Research - peer-review › Article in proceedings – Annual report year: 2017

Improving performance of single-path code through a time-predictable memory hierarchy
Publication: Research - peer-review › Article in proceedings – Annual report year: 2017

Pin-count reduction for continuous flow microfluidic biochips
Publication: Research - peer-review › Journal article – Annual report year: 2017

A Controller for Dynamic Partial Reconfiguration in FPGA-Based Real-Time Systems
Publication: Research - peer-review › Article in proceedings – Annual report year: 2017

Activity-Based Collaboration for Interactive Spaces
Publication: Research - peer-review › Book chapter – Annual report year: 2017

A language-based approach to modelling and analysis of Twitter interactions
Publication: Research - peer-review › Journal article – Annual report year: 2017

A Multi-Format Floating-Point Multiplier for Power-Efficient Operations
Publication: Research - peer-review › Article in proceedings – Annual report year: 2017

Analysis of DDoS-capable IoT malwares
Publication: Research - peer-review › Article in proceedings – Annual report year: 2017

An Introduction to Malware
Publication: Education › Compendium/lecture notes – Annual report year: 2017
A resource-efficient network interface supporting low latency reconfiguration of virtual circuits in time-division multiplexing networks-on-chip
Publication: Research - peer-review › Journal article – Annual report year: 2017

A Shared Scratchpad Memory with Synchronization Support
Publication: Research - peer-review › Article in proceedings – Annual report year: 2017

A Study on Text-Score Disagreement in Online Reviews
Fazzolari, M., Cozza, V., Petrocchi, M. & Spognardi, A. 2017 In : Cognitive Computation. 9, 5, p. 689-701
Publication: Research - peer-review › Journal article – Annual report year: 2017

A systematic and practical method for selecting systems engineering tools
Publication: Research - peer-review › Article in proceedings – Annual report year: 2018

A Top-down Approach to Genetic Circuit Synthesis and Optimized Technology Mapping
Publication: Research - peer-review › Article in proceedings – Annual report year: 2017

Best practice for caching of single-path code
Publication: Research - peer-review › Journal article – Annual report year: 2017

Best Practice for Caching of Single-Path Code
Publication: Research - peer-review › Article in proceedings – Annual report year: 2017

Big Data hvor N=1
Publication: Research › Book chapter – Annual report year: 2017

Can Real-Time Systems Benefit from Dynamic Partial Reconfiguration?
Publication: Research - peer-review › Article in proceedings – Annual report year: 2017

Can smartphone-based electronic markers discriminate between patients with bipolar disorder, healthy first-degree relatives and healthy control individuals
Publication: Research - peer-review › Conference abstract in journal – Annual report year: 2017

Collaborative Affordances of Medical Records
Publication: Research - peer-review › Journal article – Annual report year: 2017

Co-Simulation of Cyber-Physical System with Distributed Embedded Control
Publication: Research › Ph.D. thesis – Annual report year: 2017
Design and Simulation of a Quaternary Memory Cell based on a Physical Memristor
Publication: Research - peer-review › Article in proceedings – Annual report year: 2016

Designing Context-Aware Cognitive Behavioral Therapy for Unipolar and Bipolar Disorders
Publication: Research - peer-review › Article in proceedings – Annual report year: 2016

Design optimization for security-and safety-critical distributed real-time applications
Publication: Research - peer-review › Journal article – Annual report year: 2016

Design Optimization of Cyber-Physical Distributed Systems using IEEE Time-sensitive Networks (TSN)
Publication: Research - peer-review › Journal article – Annual report year: 2016

Discounted Duration Calculus
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DNA-Inspired Online Behavioral Modeling and Its Application to Spambot Detection
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D-VASim: An Interactive Virtual Laboratory Environment for the Simulation and Analysis of Genetic Circuits
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D-VASim: A Software Tool to Simulate and Analyze Genetic Logic Circuits
Baig, H. & Madsen, J. 2016 Case study.
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Electronic self-monitoring of mood using IT platforms in adult patients with bipolar disorder: A systematic review of the validity and evidence
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Evaluation of Professional Cloud Password Management Tools
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FPGA Acceleration by Dynamically-Loaded Hardware Libraries
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Game-based verification and synthesis
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Hardware locks for a real-time Java chip multiprocessor
Publication: Research - peer-review › Journal article – Annual report year: 2016

Implementation of Hardware Accelerators on Zynq
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Improved 64-bit Radix-16 Booth Multiplier Based on Partial Product Array Height Reduction
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Lessons learned from the EU project T-CREST
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Microfluidic Very Large Scale Integration (VLSI): Modeling, Simulation, Testing, Compilation and Physical Synthesis
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Publication: Research - peer-review › Journal article – Annual report year: 2016

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Fame for sale: Efficient detection of fake Twitter followers

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Flow-Based Biochips: Fault-Tolerant Design and Error Recovery

Hardware Locks with Priority Ceiling Emulation for a Java Chip-Multiprocessor

Hardware Transactional Memory Optimization Guidelines, Applied to Ordered Maps

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Integrating Attributes into Role-Based Access Control

Interfacing Hardware Accelerators to a Time-Division Multiplexing Network-on-Chip

Introduction

Message Passing on a Time-predictable Multicore Processor

Microfluidic very large-scale integration for biochips: Technology, testing and fault-tolerant design

Modelling and Analysis for Cyber-Physical Systems: An SMT-based approach

Models of Communication for Multicore Processors

Multiprocessor Priority Ceiling Emulation for Safety-Critical Java

No Need Knowing Numerous Neighbours: Towards a Realizable Interpretation of MLSL

Off-line mapping of multi-rate dependent task sets to many-core platforms

Optimal Scheduling of Stochastic Production Processes Through Model Checking
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Persistence-Based Branch Misprediction Bounds for WCET Analysis
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Synthesis of Biochemical Applications on Digital Microfluidic Biochips with Operation Execution Time Variability
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A practical approach to model checking Duration Calculus using Presburger Arithmetic

Argo: A Time-Elastic Time-Division-Multiplexed NOC using Asynchronous Routers

A Synthesizable Multicore Platform for Microwave Imaging

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Automated evolutionary restructuring of workflows to minimise errors via stochastic model checking

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Certifiable Java for Embedded Systems

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