


Sparsø, J 2018, 'Selected papers from the 2nd IEEE Nordic Circuits and Systems Conference (NorCAS), 2016.' Microprocessors and Microsystems, vol 60, pp. 38-39. DOI: 10.1016/j.micpro.2018.03.008


Schneider, A, Pop, P & Madsen, J 2017, 'Pin-count reduction for continuous flow microfluidic biochips' Microsystem Technologies, pp. 1-12. DOI: 10.1007/s00542-017-3401-1


Bardram, JE, Esbensen, M & Tabard, A 2017, Activity-Based Collaboration for Interactive Spaces. in C Anslow, P Campos & J Jorge (eds), Collaboration Meets Interactive Spaces. Springer, pp. 233-257. DOI: 10.1007/978-3-319-45853-3_11


Hansen, HE, Maroun, EJ, Kristensen, AT, Schoeberl, M & Marquart, J 2017, A Shared Scratchpad Memory with Synchronization Support. in Proceedings of the IEEE NorCAS 2017. IEEE.


Baig, H & Madsen, J 2016, 'Timing Analysis of Genetic Logic Circuits using D-VASim' 19th Conference and Exhibition on Design, Automation and Test in Europe Conference and Exhibition (DATE 2016), Dresden, Germany, 14/03/2016 - 18/03/2016,


Gavrilut, VM & Pop, P 2016, 'Traffic class assignment for mixed-criticality frames in TTEthernet' ACM SIGBED Review, vol 13, no. 4, pp. 31-36. DOI: 10.1145/3015037.3015042


Guo, J, Jensen, CD & Ma, J 2015, Continuous Context-Aware Device Comfort Evaluation Method. in CD Jensen, S Marsh, T Dimitrakos & Y Murayama (eds), Proceedings of the 9th IFIP WG 11.11 International Conference on Trust Management IX (IFIPTM 2015). Springer, pp. 203-211. IFIP AICT - Advances in Information and Communication technology, no. 454, DOI: 10.1007/978-3-319-18491-3_16


Tamas-Selicean, D, Pop, P & Steiner, W 2015, 'Design optimization of TTEthernet-based distributed real-time systems' Real-Time Systems, vol 51, no. 1, pp. 1-35. DOI: 10.1007/s11241-014-9214-8
Nyborg, M, Probst, CW & Stassen, F 2015, Developing merged CDIO based curricula for diploma (B.Eng.) IT study programs at DTU. in Proceedings of the 11th International CDIO Conference. CDIO.


Puffitsch, W 2015, Fernando: An educational ahead-of-time bytecode compiler. in Proceedings of the 13th International Workshop on Java Technologies for Real-time and Embedded Systems (JTRES ’15)., 14, Association for Computing Machinery. DOI: 10.1109/2822304.2822316


Pezzarossa, L, Sørensen, RB, Schoeberl, M & Sparsø, J 2015, Interfacing Hardware Accelerators to a Time-Division Multiplexing Network-on-Chip. in Proceedings of the 1st Nordic Circuits and Systems Conference (NORCAS 2015). IEEE. DOI: 10.1109/NORCHIP.2015.7364392


Lander Raagaard, M & Pop, P 2015, Pin count-aware biochemical application compilation for mVLSI biochips. in Design, Test, Integration and Packaging of MEMS/MOEMS (DTIP), 2015 Symposium on. IEEE, pp. 1-6. DOI: 10.1109/DTDIP.2015.7161026


Jensen, NB, Probst, CW & Karlsson, S 2014, 'Collaborative Compiler Vectorization' 9th International Conference on High-Performance and Embedded Architectures and Compilers , Vienna, Austria, 20/01/2014 - 22/01/2014,

Alistar, M 2014, 'Compilation and Synthesis for Fault-Tolerant Digital Microfluidic Biochips (DMBs)' 2014 Design, Automation & Test in Europe Conference & Exhibition (DATE), Dresden, Germany, 24/03/2014 - 28/03/2014,


Nannarelli, A 2014, Decimal Engine for Energy-Efficient Multicore Processors. in Proceedings of the 22nd IFIP/IEEE International Conference on Very Large Scale Integration (VLSI-SoC). IEEE. DOI: 10.1109/VLSI-SoC.2014.7004176


Bonnichsen, LF, Karlsson, S & Probst, CW 2014, 'ELB-trees - Efficient Lock-free B+trees' 9th International Conference on High-Performance and Embedded Architectures and Compilers , Vienna, Austria, 20/01/2014 - 22/01/2014,


Brock-Nannestad, L, DelSignore, J, Squyres, JM, Karlsson, S & Mohror, K 2014, 'MPI Debugging with Handle Introspection' Paper presented at Workshop on Exascale MPI (ExaMPI 2014), New Orleans, United States, 17/11/2014,


Lakis, E & Schoeberl, M 2013, An SDRAM controller for real-time systems. in 2013 IEEE 16th International Symposium on Object/Component/Service-Oriented Real-Time Distributed Computing (ISORC) . IEEE. DOI: 10.1109/ISORC.2013.6913224


Abbaspour, S, Brandner, F & Schoeberl, M 2013, A Time-predictable Stack Cache. in 2013 IEEE 16th International Symposium on Object/Component/Service-Oriented Real-Time Distributed Computing (ISORC) . IEEE. DOI: 10.1109/ISORC.2013.6913225


Minhass, WH, Pop, P, Madsen, J & Ho, T-Y 2013, Control Synthesis for the Flow-Based Microfluidic Large-Scale Integration Biochips. in 2013 18th Asia and South Pacific Design Automation Conference (ASP-DAC). IEEE, pp. 205-212. DOI: 10.1109/ASPDAC.2013.6509597


Gruian, F & Schoeberl, M 2013, 'Hardware support for CSP on a Java chip multiprocessor' Microprocessors and Microsystems, vol 37, no. 4-5, pp. 472-481. DOI: 10.1016/j.micpro.2012.08.004


Vithanage, MD, Fafoutis, X, Andersen, CB & Dragoni, N 2013, Medium Access Control for Thermal Energy Harvesting in Advanced Metering Infrastructures. in 2013 IEEE EUROCON. IEEE, pp. 291-299. DOI: 10.1109/EUROCON.2013.6624999


Jozwiak, L & Madsen, J 2013, Quality-Driven Model-Based Design of MultiProcessor Embedded Systems for Highlydemanding Applications. in *2013 2nd Mediterranean Conference on Embedded Computing (MECO)*. IEEE. DOI: 10.1109/MECO.2013.6601388

Kasapaki, E, Sparso, J, Sørensen, RB & Goossens, K 2013, Router Designs for an Asynchronous Time-Division-Multiplexed Network-on-Chip. in *2013 Euromicro Conference on Digital System Design (DSD)*. IEEE, pp. 319-326. DOI: 10.1109/DSD.2013.40

Mazzara, M, Biselli, L, Greco, PP, Dragoni, N, Marraffa, A, Qamar, N & de Nicola, S 2013, Social Networks and Collective Intelligence: A Return to the Agora. in *Social Network Engineering for Secure Web Data and Services*. IGI global, pp. 88-113. DOI: 10.4018/978-1-4666-3926-3.ch005
