


Bogdanov A, Kavun EB, Tischhauser E, Yalcin T. Efficient reconfigurable hardware architecture for accurately computing success probability and data complexity of linear attacks. In International Conference on Reconfigurable Computing and FPGAs (ReConFig) 2012. IEEE. 2012. Available from, DOI: 10.1109/ReConFig.2012.6416744