


Bogdanov, A., Moradi, A., & Yalcin, T. (2013). Efficient and side-channel resistant authenticated encryption of FPGA bitstreams. In Proceedings of 2012 International Conference on Reconfigurable Computing and FPGAs (ReConFig) IEEE. DOI: 10.1109/ReConFig.2012.6416743


Bogdanov, A., Kavun, E. B., Tischhauser, E., & Yalcin, T. (2012). Efficient reconfigurable hardware architecture for accurately computing success probability and data complexity of linear attacks. In *International Conference on Reconfigurable Computing and FPGAs (ReConFig) 2012* IEEE. DOI: 10.1109/ReConFig.2012.6416744